

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD

**M. TECH. IN ELECTRONICS AND COMMUNICATION ENGINEERING.
EFFECTIVE FROM ACADEMIC YEAR 2017 - 18 ADMITTED BATCH**

COURSE STRUCTURE AND SYLLABUS

I Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-1	Digital System Design	25	75	4	0	0	4
PC-2	Coding Theory and Techniques	25	75	4	0	0	4
PC-3	Advanced Digital Signal Processing	25	75	4	0	0	4
PE-1	Detection and Estimation Theory Digital Signal Processors and Architectures Speech Signal Processing	25	75	3	0	0	3
PE-2	Advanced Computer Architecture Real Time Operating Systems Adhoc Wireless Sensor Networks	25	75	3	0	0	3
OE-1	*Open Elective – I	25	75	3	0	0	3
Laboratory I	Digital System Design Lab	25	75	0	0	3	2
Seminar I	Seminar - I	100	0	0	0	3	2
Total		275	525	21	0	6	25

II Semester

Category	Course Title	Int. marks	Ext. marks	L	T	P	C
PC-4	Embedded System Design	25	75	4	0	0	4
PC-5	System on Chip Architectures	25	75	4	0	0	4
PC-6	Wireless Communications and Networks	25	75	4	0	0	4
PE-3	Spread Spectrum Communications Network Security And Cryptography Adaptive Signal Processing	25	75	3	0	0	3
PE4	Design of Fault Tolerant Systems Image and Video Processing Optical Communications and Networks	25	75	3	0	0	3
OE-2	*Open Elective – II	25	75	3	0	0	3
Laboratory II	Wireless Communications and Networks Lab	25	75	0	0	3	2
Seminar II	Seminar - II	100	0	0	0	3	2
Total		275	525	21	0	6	25

III Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Technical Paper Writing	100	0	0	3	0	2
Comprehensive Viva-Voce	0	100	0	0	0	4
Project work Review II	100	0	0	0	22	8
Total	200	100	0	3	22	14

IV Semester

Course Title	Int. marks	Ext. marks	L	T	P	C
Project work Review III	100	0	0	0	24	8
Project Evaluation (Viva-Voce)	0	100	0	0	0	16
Total	100	100	0	0	24	24

*Open Elective subjects must be chosen from the list of open electives offered by **OTHER** departments.

For Project review I, please refer 7.10 in R17 Academic Regulations.

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M. TECH. I YEAR II SEMESTER ELECTRONICS AND COMMUNICATION ENGINEERING

EMBEDDED SYSTEM DESIGN (PC - 4)

UNIT - I

Introduction to Embedded Systems: Definition of Embedded System, Embedded Systems Vs General Computing Systems, History of Embedded Systems, Classification, Major Application Areas, Purpose of Embedded Systems, Characteristics and Quality Attributes of Embedded Systems.

UNIT - II

Typical Embedded System: Core of the Embedded System: General Purpose and Domain Specific Processors, ASICs, PLDs, Commercial Off-The-Shelf Components (COTS), Memory: ROM, RAM, Memory according to the type of Interface, Memory Shadowing, Memory selection for Embedded Systems, Sensors and Actuators, Communication Interface: Onboard and External Communication Interfaces.

UNIT - III

Embedded Firmware: Reset Circuit, Brown-out Protection Circuit, Oscillator Unit, Real Time Clock, Watchdog Timer, Embedded Firmware Design Approaches and Development Languages.

UNIT - IV

RTOS Based Embedded System Design: Operating System Basics, Types of Operating Systems, Tasks, Process and Threads, Multiprocessing and Multitasking, Task Scheduling.

UNIT - V

Task Communication: Shared Memory, Message Passing, Remote Procedure Call and Sockets, Task Synchronization: Task Communication/Synchronization Issues, Task Synchronization Techniques, Device Drivers, How to Choose an RTOS.

TEXT BOOKS:

1. Shibu K.V, "Introduction to Embedded Systems", McGraw Hill.

REFERENCE BOOKS:

1. Raj Kamal, "Embedded Systems", TMH.
2. Frank Vahid, Tony Givargis, "Embedded System Design", John Wiley.
3. Lyla, "Embedded Systems", 2013, Pearson.
4. David E. Simon, "An Embedded Software Primer", Pearson Education.

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SYSTEM ON CHIP ARCHITECTURES (PC - 5)

UNIT – I

Introduction to the System Approach: System Architecture, Components of the system, Hardware & Software, Processor Architectures, Memory and Addressing. System level interconnection, An approach for SOC Design, System Architecture and Complexity.

UNIT – II

Processors: Introduction , Processor Selection for SOC, Basic concepts in Processor Architecture, Basic concepts in Processor Micro Architecture, Basic elements in Instruction handling. Buffers: minimizing Pipeline Delays, Branches, More Robust Processors, Vector Processors and Vector Instructions extensions, VLIW Processors, Superscalar Processors.

UNIT – III

Memory Design for SOC: Overview of SOC external memory, Internal Memory, Size, Scratchpads and Cache memory, Cache Organization, Cache data, Write Policies, Strategies for line replacement at miss time, Types of Cache, Split – I, and D – Caches, Multilevel Caches, Virtual to real translation , SOC Memory System, Models of Simple Processor – memory interaction.

UNIT - IV

Interconnect Customization and Configuration: Inter Connect Architectures, Bus: Basic Architectures, SOC Standard Buses , Analytic Bus Models, Using the Bus model, Effects of Bus transactions and contention time. SOC Customization: An overview, Customizing Instruction Processor, Reconfiguration Technologies, Mapping design onto Reconfigurable devices, Instance-Specific design, Customizable Soft Processor, Reconfiguration - overhead analysis and trade-off analysis on reconfigurable Parallelism.

UNIT – V

Application Studies / Case Studies: SOC Design approach, AES algorithms, Design and evaluation, Image compression – JPEG compression.

TEXT BOOKS:

1. Michael J. Flynn and Wayne Luk, “Computer System Design System-on-Chip”, Wiley India Pvt. Ltd.
2. Steve Furber, “ARM System on Chip Architecture “, 2nd Edition, 2000, Addison Wesley Professional.

REFERENCE BOOKS:

1. Ricardo Reis, “Design of System on a Chip: Devices and Components”, 1st Edition, 2004, Springer
2. Jason Andrews, “Co-Verification of Hardware and Software for ARM System on Chip Design (Embedded Technology)”, Newnes, BK and CDROM.
3. Prakash Rashinkar, Peter Paterson and Leena Singh L, “System on Chip Verification – Methodologies and Techniques”, 2001, Kluwer Academic Publishers.

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WIRELESS COMMUNICATIONS AND NETWORKS (PC - 6)

UNIT - I

The Cellular Concept-System Design Fundamentals: Introduction, Frequency Reuse, Channel Assignment Strategies, Handoff Strategies- Prioritizing Handoffs, Practical Handoff Considerations, Interference and system capacity – Co channel Interference and system capacity, Channel planning for Wireless Systems, Adjacent Channel interference , Power Control for Reducing interference, Trunking and Grade of Service, Improving Coverage & Capacity in Cellular Systems- Cell Splitting, Sectoring .

UNIT – II

Mobile Radio Propagation: Large-Scale Path Loss: Introduction to Radio Wave Propagation, Free Space Propagation Model, Relating Power to Electric Field, The Three Basic Propagation Mechanisms, Reflection-Reflection from Dielectrics, Brewster Angle, Reflection from perfect conductors, Ground Reflection (Two-Ray) Model, Diffraction-Fresnel Zone Geometry, Knife-edge Diffraction Model, Multiple knife-edge Diffraction, Scattering, Outdoor Propagation Models- Longley-Ryce Model, Okumura Model, Hata Model, PCS Extension to Hata Model, Walfisch and Bertoni Model, Wideband PCS Microcell Model, Indoor Propagation Models-Partition losses (Same Floor), Partition losses between Floors, Log-distance path loss model, Ericsson Multiple Breakpoint Model, Attenuation Factor Model, Signal penetration into buildings, Ray Tracing and Site Specific Modeling.

UNIT – III

Mobile Radio Propagation: Small –Scale Fading and Multipath: Small Scale Multipath propagation-Factors influencing small scale fading, Doppler shift, Impulse Response Model of a multipath channel- Relationship between Bandwidth and Received power, Small-Scale Multipath Measurements-Direct RF Pulse System, Spread Spectrum Sliding Correlator Channel Sounding, Frequency Domain Channels Sounding, Parameters of Mobile Multipath Channels-Time Dispersion Parameters, Coherence Bandwidth, Doppler Spread and Coherence Time, Types of Small-Scale Fading-Fading effects Due to Multipath Time Delay Spread, Flat fading, Frequency selective fading, Fading effects Due to Doppler Spread-Fast fading, slow fading, Statistical Models for multipath Fading Channels-Clarke's model for flat fading, spectral shape due to Doppler spread in Clarke's model, Simulation of Clarke and Gans Fading Model, Level crossing and fading statistics, Two-ray Rayleigh Fading Model.

UNIT - IV

Equalization and Diversity: Introduction, Fundamentals of Equalization, Training A Generic Adaptive Equalizer, Equalizers in a communication Receiver, Linear Equalizers, Non linear Equalization-Decision Feedback Equalization (DFE), Maximum Likelihood Sequence Estimation (MLSE) Equalizer, Algorithms for adaptive equalization-Zero Forcing Algorithm, Least Mean Square Algorithm, Recursive least squares algorithm. Diversity Techniques-Derivation of selection Diversity improvement, Derivation of Maximal Ratio Combining improvement, Practical Space Diversity Consideration-Selection Diversity, Feedback or Scanning Diversity, Maximal Ratio Combining, Equal Gain Combining, Polarization Diversity, Frequency Diversity, Time Diversity, RAKE Receiver.

UNIT - V

Wireless Networks: Introduction to wireless Networks, Advantages and disadvantages of Wireless Local Area Networks, WLAN Topologies, WLAN Standard IEEE 802.11, IEEE 802.11 Medium Access Control, Comparison of IEEE 802.11 a,b,g and n standards, IEEE 802.16 and its enhancements, Wireless PANs, Hiper Lan, WLL.

TEXT BOOKS:

1. Theodore, S. Rappaport, "Wireless Communications, Principles, Practice", 2nd Edition, 2002, PHI.
2. Andrea Goldsmith, "Wireless Communications", 2005 Cambridge University Press.
3. Kaveh Pah Laven and P. Krishna Murthy, "Principles of Wireless Networks", 2002, PE
4. Gottapu Sasibhushana Rao, "Mobile Cellular Communication", Pearson Education, 2012.

REFERENCE BOOKS:

1. Kamilo Feher, "Wireless Digital Communications", 1999, PHI.
2. William Stallings, "Wireless Communication and Networking", 2003, PHI.

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SPREAD SPECTRUM COMMUNICATIONS (PE - 3)

UNIT - I

Introduction to Spread Spectrum Systems: Fundamental Concepts of Spread Spectrum Systems, Pseudo Noise Sequences, Direct Sequence Spread Spectrum, Frequency Hop Spread Spectrum, Hybrid Direct Sequence Frequency Hop Spread Spectrum, Code Division Multiple Access.

Binary Shift Register Sequences for Spread Spectrum Systems: Introduction, Definitions, Mathematical Background and Sequence Generator Fundamentals, Maximal Length Sequences, Gold Codes.

UNIT - II

Code Tracking Loops: Introduction, Optimum Tracking of Wideband Signals, Base Band Delay-Lock Tracking Loop, Tau-Dither Non-Coherent Tracking Loop, Double Dither Non-Coherent Tracking Loop.

UNIT - III

Initial Synchronization of the Receiver Spreading Code: Introduction, Problem Definition and the Optimum Synchronizer, Serial Search Synchronization Techniques, Synchronization using a Matched Filter, Synchronization by Estimated the Received Spreading Code.

UNIT - IV

Cellular Code Division Multiple Access (CDMA) Principles: Introduction, Wide Band Mobile Channel, the Cellular CDMA System, Single User Receiver in a Multi User Channel, CDMA System Capacity,

Multi-User Detection in CDMA Cellular Radio: Optimal Multi-User Detection, Linear Suboptimal Detectors, Interference Combat Detection Schemes, Interference Cancellation Techniques.

UNIT - V

Performance of Spread Spectrum Systems in Jamming Environments: Spread Spectrum Communication System Model, Performance of Spread Spectrum Systems without Coding.

Performance of Spread Spectrum Systems with Forward Error Correction: Elementary Block Coding Concepts, Optimum Decoding Rule, Calculation of Error Probability, Elementary Convolution Coding Concepts, Viterbi Algorithm, Decoding and Bit-Error Rate.

TEXT BOOKS:

1. Rodger E Ziemer, Roger L. Peterson and David E Borth, "Introduction to Spread Spectrum Communication", Pearson, 1st Edition, 1995.
2. Mosa Ali Abu-Rgheff, "Introduction to CDMA Wireless Communications", Elsevier Publications, 2008.

REFERENCE BOOKS:

1. George R. Cooper, Clare D. Mc Gillem , "Modern Communication and Spread Spectrum", McGraw Hill, 1986.
2. Andrew j. Viterbi, "CDMA: Principles of spread spectrum communication", Pearson Education, 1st Edition, 1995.
3. Kamillo Feher, "Wireless Digital Communications", PHI, 2009.
4. Andrew Richardson, "WCDMA Design Handbook", Cambridge University Press, 2005.
5. Steve Lee, "Spread Spectrum CDMA", McGraw Hill, 2002.

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NETWORK SECURITY AND CRYPTOGRAPHY (PE - 3)

UNIT - I

Introduction: Attacks, Services and Mechanisms, Security attacks, Security services, A Model for Internetwork security.

Classical Techniques: Conventional Encryption model, Steganography, Classical Encryption Techniques.

Modern Techniques: Simplified DES, Block Cipher Principles, Data Encryption standard, Strength of DES, Differential and Linear Cryptanalysis, Block Cipher Design Principles and Modes of operations.

UNIT - II

Encryption Algorithms: Triple DES, International Data Encryption algorithm, Blowfish, RC5, CAST-128, RC2, Characteristics of Advanced Symmetric block ciphers.

Conventional Encryption: Placement of Encryption function, Traffic confidentiality, Key distribution, Random Number Generation.

UNIT - III

Public Key Cryptography: Principles, RSA Algorithm, Key Management, Diffie-Hellman Key exchange, Elliptic Curve Cryptography.

Number Theory: Prime and Relatively prime numbers, Modular arithmetic, Fermat's and Euler's theorems, Testing for primality, Euclid's Algorithm, the Chinese remainder theorem, Discrete logarithms.

UNIT - IV

Message Authentication and Hash Functions: Authentication requirements and functions, Message Authentication, Hash functions, Security of Hash functions and MACs.

Hash and Mac Algorithms: MD File, Message digest Algorithm, Secure Hash Algorithm, RIPEMD-160, HMAC.

Digital signatures and Authentication protocols: Digital signatures, Authentication Protocols, Digital signature standards.

Authentication Applications: Kerberos, X.509 directory Authentication service.

Electronic Mail Security: Pretty Good Privacy, S/MIME.

UNIT - V

IP Security: Overview, Architecture, Authentication, Encapsulating Security Payload, Combining security Associations, Key Management. Web Security: Web Security requirements, Secure sockets layer and Transport layer security, Secure Electronic Transaction.

Intruders, Viruses and Worms: Intruders, Viruses and Related threats.

Fire Walls: Fire wall Design Principles, Trusted systems.

TEXT BOOKS:

1. William Stallings, "Cryptography and Network Security: Principles and Practice", Pearson Education.
2. William Stallings, "Network Security Essentials (Applications and Standards)", Pearson Education.

REFERENCE BOOKS:

1. Eric Maiwald, "Fundamentals of Network Security", Dreamtech press

2. Charlie Kaufman, Radia Perlman and Mike Speciner, "Network Security - Private Communication in a Public World", Pearson/PHI.
3. Whitman, "Principles of Information Security", Thomson.
4. Robert Bragg, Mark Rhodes, "Network Security: The complete reference", TMH
5. Buchmann, "Introduction to Cryptography", Springer.

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ADAPTIVE SIGNAL PROCESSING (PE - 3)

UNIT – I

Introduction to Adaptive Systems Adaptive Systems: Definitions, Characteristics, Applications, Example of an Adaptive System. The Adaptive Linear Combiner - Description, Weight Vectors, Desired Response Performance function - Gradient & Mean Square Error.

UNIT – II

Development of Adaptive Filter Theory & Searching the Performance surface: Introduction to Filtering - Smoothing and Prediction – Linear Optimum Filtering, Problem statement, Principle of Orthogonally - Minimum Mean Square Error, Wiener- Hopf equations, Error Performance - Minimum Mean Square Error, Estimation of phase shift between two narrow band signals using Orthogonal Decomposer.

UNIT – III

Steepest Descent Algorithms: Searching the performance surface – Methods & Ideas of Gradient Search methods - Gradient Searching Algorithm & its Solution - Stability & Rate of convergence - Learning Curves Gradient Search by Newton's Method, Method of Steepest Descent, Comparison of Learning Curves.

UNIT – IV

LMS Algorithm & Applications: Overview - LMS Adaptation algorithms, Stability & Performance analysis of LMS Algorithms - LMS Gradient & Stochastic algorithms - Convergence of LMS algorithm. **Applications:** Adaptive BFSK, BPSK, ASK demodulators and delay estimation. Adaptive Beam forming, concept of IQ channels, Adaptive filter implementation of Hilbert Transform. Introduction to MUSIC

UNIT – V

State Estimators: Introduction to RLS Algorithm, Statement of Kalman filtering problem, The Innovation Process, Estimation of State using the Innovation Process- Expression of Kalman Gain, Filtering Example estimation of state from observations of noisy observed narrow band signals. Target tracking using only DOA.

TEXT BOOKS:

1. Adaptive Signal Processing - Bernard Widrow, Samuel D. Stearns, 2005, PE.
2. Adaptive Filter Theory - Simon Haykin-, 4th Edition, 2002, PE Asia.

REFERENCE BOOKS:

1. Kaluri V. Rangarao, Ranjan K. Mallik, "Digital Signal Processing: A Practitioner's Approach", ISBN: 978- 0-470-01769-2, 210 pages, November 2006, John Wiley (UK)
2. Sophocles. J. Orfamadis, "Optimum signal processing: An introduction", 2nd Edition, 1988, McGraw-Hill, New York
3. S. Thomas Alexander, "Adaptive signal processing-Theory, and Applications, 1986, Springer –Verlag.
4. Candy, "Signal analysis", McGraw Hill Int. Student Edition
5. James V. Candy, "Signal Processing: A Modern Approach", McGraw-Hill, International Edition, 1988.

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DESIGN OF FAULT TOLERANT SYSTEMS (PE - 4)

UNIT- I

Fault Tolerant Design: Basic concepts: Reliability concepts, Failures & faults, Reliability and Failure rate, Relation between reliability and mean time between failure, maintainability and availability, reliability of series, parallel and parallel-series combinational circuits.

Fault Tolerant Design: Basic concepts-static, dynamic, hybrid, triple modular redundant system (TMR), 5MR reconfiguration techniques, Data redundancy, Time redundancy and software Redundancy concepts. [TEXTBOOK-1]

UNIT- II

Self Checking circuits & Fail safe Design: Self Checking Circuits: Basic concepts of self checking circuits, Design of Totally self checking checker, Checkers using m out of n codes, Berger code, Low cost residue code.

Fail Safe Design: Strongly fault secure circuits, fail safe design of sequential circuits using partition theory and Berger code, totally self checking PLA design. [TEXTBOOK-1]

UNIT- III

Design for Testability: Design for testability for combinational circuits: Basic concepts of Testability, Controllability and observability, The Reed Muller's expansion technique, use of control and syndrome testable designs.

Design for testability by means of scan: Making circuits Testable, Testability Insertion, Full scan DFT technique- Full scan insertion, flip-flop Structures, Full scan design and Test, Scan Architectures- full scan design, Shadow register DFT, Partial scan methods, multiple scan design, other scan designs.[TEXTBOOK-2]

UNIT- IV

Logic Built-in-self-test: BIST Basics-Memory-based BIST,BIST effectiveness, BIST types, Designing a BIST, Test Pattern Generation-Engaging TPGs, exhaustive counters, ring counters, twisted ring counter, Linear feedback shift register, Output Response Analysis-Engaging ORA's, One's counter, transition counter, parity checking, Serial LFSRs, Parallel Signature analysis, BIST architectures-BIST related terminologies, A centralized and separate Board-level BIST architecture, Built-in evaluation and self test(BEST), Random Test socket(RTS), LSSD On-chip self test, Self – testing using MISR and SRSG, Concurrent BIST, BILBO, Enhancing coverage, RT level BIST design-CUT design, simulation and synthesis, RTS BIST insertion, Configuring the RTS BIST, incorporating configurations in BIST, Design of STUMPS, RTS and STUMPS results. [TEXTBOOK-2]

UNIT- V

Standard IEEE Test Access Methods: Boundary Scan Basics, Boundary scan architecture- Test access port, Boundary scan registers, TAP controller, the decoder unit, select and other units, Boundary scan Test Instructions-Mandatory instructions, Board level scan chain structure-One serial scan chain, multiple-scan chain with one control test port, multiple-scan chains with one TDI,TDO but multiple TMS, Multiple-scan chain, multiple access port, RT Level boundary scan-inserting boundary scan test hardware for CUT, Two module test case, virtual boundary scan tester, Boundary Scan Description language. [TEXTBOOK-2]

TEXTBOOKS:

1. Parag K. Lala, "Fault Tolerant & Fault Testable Hardware Design", 1984, PHI

2. Zainalabedin Navabi, "Digital System Test and Testable Design using HDL models and Architectures", Springer International Edition.

REFERENCES:

1. Miron Abramovici, Melvin A. Breuer and Arthur D. Friedman, "Digital Systems Testing and Testable Design", Jaico Books
2. Bushnell & Vishwani D. Agarwal, "Essentials of Electronic Testing", Springer.
3. Alfred L. Crouch, "Design for Test for Digital IC's and Embedded Core Systems", 2008, Pearson Education.

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IMAGE AND VIDEO PROCESSING (PE - 4)

UNIT – I

Fundamentals of Image Processing and Image Transforms: Basic steps of Image Processing System Sampling and Quantization of an image, Basic relationship between pixels.

Image Segmentation: Segmentation concepts, Point, Line and Edge Detection, Thresholding, Region based segmentation.

UNIT – II

Image Enhancement: Spatial domain methods: Histogram processing, Fundamentals of Spatial filtering, Smoothing spatial filters, Sharpening spatial filters.

Frequency domain methods: Basics of filtering in frequency domain, Image smoothing, Image sharpening, Selective filtering.

UNIT – III

Image Compression: Image compression fundamentals - Coding Redundancy, Spatial and Temporal redundancy, Compression models: Lossy & Lossless, Huffman coding, Bit plane coding, Transform coding, Predictive coding, Wavelet coding, Lossy Predictive coding, JPEG Standards.

UNIT - IV

Basic Steps of Video Processing: Analog Video, Digital Video. Time-Varying Image Formation models: Three-Dimensional Motion Models, Geometric Image Formation, Photometric Image Formation, Sampling of Video signals, filtering operations.

UNIT – V

2-D Motion Estimation: Optical flow, General Methodologies, Pixel Based Motion Estimation, Block-Matching Algorithm, Mesh based Motion Estimation, Global Motion Estimation, Region based Motion Estimation, Multi resolution motion estimation, Waveform based coding, Block based transform coding, Predictive coding, Application of motion estimation in Video coding.

TEXT BOOKS:

1. Gonzalez and Woods, "Digital Image Processing", 3rd Edition, Pearson.
2. Yao Wang, Joem Ostermann and Ya-quin Zhang, "Video Processing and Communication", 1st Edition, PH Int.

REFERENCE BOOKS:

1. Gonzalez and Woods, "Digital Image Processing using MATLAB", 2nd Edition, McGraw Hill Education, 2010
2. Milan Sonka, Vaclan Hlavac, "Image Processing Analysis, and Machine Vision", 3rd Edition, CENGAGE, 2008
3. A Murat Tekalp, "Digital Video Processing", PERSON, 2010
4. S. Jayaraman, S. Esakkirajan, T. Veera Kumar, "Digital Image Processing", TMH, 2009

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OPTICAL COMMUNICATIONS AND NETWORKS (PE - 4)

UNIT - I

Optical Fibers: Structures, waveguiding and Fabrication- Nature of Light, Basic optical laws and definitions, Single mode fibers, Graded index fiber structure, Attenuation, Signal Dispersion in fibers.

Optical Sources- LEDs, Laser Diodes, Line Coding.

UNIT - II

Photodetectors: Photo detector Noise, Detector Response Time, Avalanche Multiplication Noise.

Optical Receiver Operation: Fundamental receiver operation, Digital receiver performance, Eye diagrams.

WDM Concepts and Components: Passive optical Couplers, Isolators and Circulators

UNIT - III

Digital Links: Point to point links, power penalties, error control, Coherent detection, Differential Quadrature Phase Shift Keying.

Analog Links: Carrier to noise ration, Multichannel Transmission Techniques, RF over Fiber, Radio over fiber links, Microwave Photonics.

UNIT - IV

Optical Networks: Network Concepts, Network Topologies, SONET/SDH, High speed lightwave links, Optical add/ Drop Multiplexing, Optical Switching, WDM Network, Passive Optical Networks, IP Over DWDM, Optical Ethernet, Mitigation of Transmission Impairments

UNIT - V

Performance Measurement and Monitoring: Measurement standards, Basic Test Equipment, Optical power measurement, Optical fiber characterization, Eye diagram tests, optical time domain reflectometer, optical performance monitoring, optical fiber system performance measurements.

TEXTBOOK:

1. Gerd Keiser, "Optical Fiber Communications", 5th Edition, McGraw Hill.

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WIRELESS COMMUNICATIONS AND NETWORKS LAB

Note:

- A. Minimum of 10 Experiments have to be conducted
- B. All the Experiments may be Conducted using Network Simulation software like NS-2/ NSG-2.1/ WireSHARK/ SDR etc..

Note: For Experiments 1 to 7 Performance may be evaluated through simulation by using the parameters Throughput, Packet Delivery Ratio, Delay etc.

1. Evaluate the performance of various LAN Topologies
2. Evaluate the performance of Drop Tail and RED queue management schemes
3. Evaluate the performance of CBQ and FQ Scheduling Mechanisms
4. Evaluate the performance of TCP and UDP Protocols
5. Evaluate the performance of TCP, New Reno and Vegas
6. Evaluate the performance of AODV, DSR and DSDV routing protocols
7. Evaluate the performance of IEEE 802.11 and IEEE 802.15.4
8. Capturing and Analysis of TCP and IP Packets
9. Simulation and Analysis of ICMP and IGMP Packets
10. Analyze the Protocols SCTP , ARP, NetBIOS, IPX VINES
11. Analysis of HTTP ,DNS and DHCP Protocols
12. Analysis of OFDM Spectrum
13. Analysis CDMA Downlink