

2007-2008

**ACADEMIC REGULATIONS
COURSE STRUCTURE
AND
DETAILED SYLLABUS**

**COMPUTER SCIENCE
AND
SYSTEMS ENGINEERING**

For
B.TECH. FOUR YEAR DEGREE COURSE
(Applicable for the batches admitted from 2007-2008)



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
KUKATPALLY, HYDERABAD - 500 085.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

HYDERABAD - 500 085

Academic Regulations 2007

for

B. Tech (Regular)

(Effective for the students admitted into I year
from the Academic Year 2007-2008 and onwards)

1. Award of B.Tech. Degree

A student will be declared eligible for the award of the B. Tech. Degree if he fulfils the following academic regulations:

- Pursued a course of study for not less than four academic years and not more than eight academic years.
- Registered for 224 credits and secured 216 credits with compulsory subjects as listed in Table-1.

Table 1: Compulsory Subjects

Sl. No.	Subject Particulars
1.	All the first year subjects
2.	All practical subjects
3.	Industry oriented mini project
4.	Comprehensive Viva-Voce
5.	Seminar
6.	Project work

- Students, who fail to fulfil all the academic requirements for the award of the degree within eight academic years from the year of their admission, shall forfeit their seat in B. Tech course.

3. Courses of study

The following courses of study are offered at present for specialization for the B. Tech. Course:

Branch Code Branch

- | | |
|-------|--|
| I. | Aeronautical Engineering. |
| II. | Automobile Engineering. |
| III. | Bio-Medical Engineering. |
| IV. | Biotechnology. |
| V. | Chemical Engineering. |
| VI. | Civil Engineering. |
| VII. | Computer Science and Engineering. |
| VIII. | Computer Science and Systems Engineering. |
| IX. | Electrical and Electronics Engineering. |
| X. | Electronics and Communication Engineering. |
| XI. | Electronics and Computer Engineering. |
| XII. | Electronics and Control Engineering. |
| XIII. | Electronics and Instrumentation Engineering. |
| XIV. | Electronics and Telematics Engineering. |

- Information Technology.
- Instrumentation and Control Engineering.
- Mechanical Engineering (Mechatronics).
- Mechanical Engineering (Production).
- Mechanical Engineering.
- Metallurgical Engineering.
- Metallurgy and Material Technology.

and any other course as approved by the authorities of the University from time to time.

4. Credits

	I Year		Semester	
	Periods / Week	Credits	Periods / Week	Credits
Theory	03	06	04	04
	02	04		
	04	08	—	—
Practical	03	04	03	02
	06	08	06	04
Drawing	03	04	03	02
	06	08	06	04
Mini Project	—	—	—	02
Comprehensive Viva Voce	—	—	—	02
Seminar	—	—	—	02
Project	—	—	—	12

5. Distribution and Weightage of Marks

- The performance of a student in each semester / I year shall be evaluated subject -wise with a maximum of 100 marks for theory and 75 marks for practical subject. In addition, Industry oriented mini-project, seminar and project work shall be evaluated for 50, 50 and 200 marks respectively.
- For theory subjects the distribution shall be 20 marks for Internal Evaluation and 80 marks for the End-Examination.
- For theory subjects, during the semester there shall be 4 tests, which include, 2 objective type tests each for duration of 20 minutes each and 2 subjective type tests each for duration of 90 minutes each. One objective type test and one subjective test to be conducted in 1-4 units and one objective type test and one subjective type test are to be conducted in 5-8 units of each semester. The best 3 tests will be considered for awarding 20 sessional marks. For the I year class which shall be on yearly basis, there shall be 6 tests which include, 3 objective type tests and 3 subjective type tests with the same duration and weightage for each test as mentioned above. However, the performance in the best 4 tests will be considered for awarding 20 sessional marks. The distribution of syllabus for the conduct of objective and subjective type tests in the first year shall be as follows:

1 - 2 Units	one Objective type and one Subjective type test.
3 - 5 Units	one Objective type and one Subjective type test.
6 - 8 Units	one Objective type and one Subjective type test.

 Each objective test question paper shall contain 20 objective type questions

for 20 marks. Each subjective type test question paper shall contain 5 questions out of which any 3 questions need to be answered. The subjective type question paper should also be for 20 marks. Though the test pattern is different, all the tests (objective and subjective type tests) have equal weightage.

- iv. For practical subjects there shall be a continuous evaluation during the semester for 25 sessional marks and 50 end examination marks. Of the 25 marks for internal, 15 marks shall be awarded for day-to-day work and 10 marks to be awarded by conducting an internal laboratory test. The end examination shall be conducted by the teacher concerned and another member of the staff of the same department.
- v. For the subject having design and / or drawing, (such as Engineering Graphics, Engineering Drawing, Machine Drawing) and estimation, the distribution shall be 20 marks for internal evaluation (10 marks for day-to-day work and 10 marks for internal tests) and 80 marks for end examination. There shall be two internal tests in a Semester and the better of the two shall consider for the award of marks for internal tests. However in the I year class, there shall be three tests and the best two will be taken into consideration.
- vi. There shall be an industry-oriented mini-Project, in collaboration with an industry of their specialization, to be taken up during the vacation after III year II Semester examination. However, the mini project and its report shall be evaluated with the project work in IV year II Semester. The industry oriented mini project shall be submitted in report form and should be presented before the committee, which shall evaluate for 50 marks. The committee consists of an external examiner, head of the department, the supervisor of mini project and a senior faculty member of the department. There shall be no internal marks for industry oriented mini project.
- vii. There shall be a seminar presentation in IV year II Semester. For the seminar, the student shall collect the information on a specialized topic and prepare a technical report, showing his understanding over the topic, and submit to the department, which shall be evaluated by the Departmental committee consisting of Head of the department, seminar supervisor and a senior faculty member. The seminar report shall be evaluated for 50 marks. There shall be no external examination for seminar.
- viii. There shall be a Comprehensive Viva-Voce in IV year II semester. The Comprehensive Viva-Voce will be conducted by a Committee consisting of (i) Head of the Department (ii) two Senior Faculty members of the Department. The Comprehensive Viva-Voce is aimed to assess the students' understanding in various subjects he / she studied during the B.Tech course of study. The Comprehensive Viva-Voce is valued for 100 marks by the Committee. There are no internal marks for the Comprehensive viva-voce.
- ix. Out of a total of 200 marks for the project work, 40 marks shall be for Internal Evaluation and 160 marks for the End Semester Examination. The End Semester Examination (viva-voce) shall be conducted by the same committee appointed for industry oriented mini project. In addition the project supervisor shall also be included in the committee. The topics for industry oriented mini project, seminar and project work shall be different from each other. The evaluation of project work shall be conducted at the end of the IV year. The Internal Evaluation shall be on the basis of two seminars given by each student on the topic of his project.

- x. Laboratory marks and the sessional marks awarded by the College are not final. They are subject to scrutiny and scaling by the University wherever felt desirable. The sessional and laboratory marks awarded by the College will be referred to a Committee. The Committee will arrive at a scaling factor and the marks will be scaled as per the scaling factor. The recommendations of the Committee are final and binding. The laboratory records and internal test papers shall be preserved in the respective departments as per the University norms and shall be produced to the Committees of the University as and when they ask for.

6. Attendance Requirements:

- i. A student shall be eligible to appear for University examinations if acquires a minimum of 75% of attendance in aggregate of all the subjects.
- ii. Condonation of shortage of attendance in aggregate up to 10% (65% and above and below 75%) in each semester or I year may be granted by the College Academic Committee.
- iii. A student will not be promoted to the next semester unless he satisfies the attendance requirement of the present semester / I year, as applicable. They may seek re-admission for that semester / I year when offered next.
- iv. Shortage of Attendance below 65% in aggregate shall in NO case be condoned.
- v. Students whose shortage of attendance is not condoned in any semester / I year are not eligible to take their end examination of that class and their registration shall stand cancelled.
- vi. A stipulated fee shall be payable towards condonation of shortage of attendance.

7. Minimum Academic Requirements:

The following academic requirements have to be satisfied in addition to the attendance requirements mentioned in item no. 6

- i. A student shall be deemed to have satisfied the minimum academic requirements and earned the credits allotted to each theory or practical design or drawing subject or project if he secures not less than 35% of marks in the end examination and a minimum of 40% of marks in the sum total of the internal evaluation and end examination taken together.
- ii. A student shall be promoted from II to III year only if he fulfils the academic requirement of 42 credits from one regular and one supplementary examinations of I year, and one regular examinations of II year I semester irrespective of whether the candidate takes the examination or not.
- iii. A student shall be promoted from third year to fourth year only if he fulfils the academic requirements of total 70 credits from the following examinations, whether the candidate takes the examinations or not.
 - a. Two regular and two supplementary examinations of I year.
 - b. Two regular and one supplementary examinations of II year I semester.
 - c. One regular and one supplementary examinations of II year II semester.
 - d. One regular examination of III year I semester.
- iv. A student shall register and put up minimum attendance in all 224 credits and earn the 216 credits. Marks obtained in the best 216 credits shall be considered for the calculation of percentage of marks.
- v. Students who fail to earn 216 credits as indicated in the course structure including compulsory subjects as indicated in Table-1 within eight academic years from the year of their admission shall forfeit their seat in B.Tech course and their admission shall stand cancelled.

8. Course pattern:

- The entire course of study is of four academic years. The first year shall be on yearly pattern and the second, third and fourth years on semester pattern.
- A student eligible to appear for the end examination in a subject, but absent at it or has failed in the end examination may appear for that subject at the supplementary examination.
- When a student is detained due to lack of credits / shortage of attendance he may be re-admitted when the semester / year is offered after fulfilment of academic regulations. Whereas the academic regulations hold good with the regulations he /she first admitted.

9. Award of Class:

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of B. Tech. Degree he shall be placed in one of the following four classes:

Class Awarded	% of marks to be secured	From the aggregate marks secured from the best 216 Credits.
First Class with Distinction	70% and above	
First Class	Below 70% but not less than 60%	
Second Class	Below 60% but not less than 50%	
Pass Class	Below 50% but not less than 40%	

(The marks in internal evaluation and end examination shall be shown separately in the marks memorandum)

10. Minimum Instruction Days:

The minimum instruction days for each semester / I year shall be 95/180 clear instruction days.

- There shall be no branch transfers after the completion of admission process.
- There shall be no place transfer within the Constituent Colleges and Units of Jawaharlal Nehru Technological University.

13. General:

- Where the words "he", "him", "his", occur in the regulations, they include "she", "her", "hers".
- The academic regulation should be read as a whole for the purpose of any interpretation.
- In the case of any doubt or ambiguity in the interpretation of the above rules, the decision of the Vice-Chancellor is final.
- The University may change or amend the academic regulations or syllabi at any time and the changes or amendments made shall be applicable to all the students with effect from the dates notified by the University.



JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY

HYDERABAD - 500 085

Academic Regulations for B. Tech. (Lateral Entry Scheme)

(Effective for the students getting admitted into II year from the Academic Year 2008-2009 and onwards)

- The Students have to acquire 160 credits from II to IV year of B.Tech. Program (Regular) for the award of the degree. Registered for 168 credits and secured 160 credits with compulsory subjects as listed in Table-1.

Table 1: Compulsory Subjects

Sl. No.	Subject Particulars
1.	All practical subjects
2.	Industry oriented mini project
3.	Comprehensive Viva-Voce
4.	Seminar
5.	Project work

- Students, who fail to fulfil the requirement for the award of the degree in 6 consecutive academic years from the year of admission, shall forfeit their seat.
- The same attendance regulations are to be adopted as that of B. Tech. (Regular).
- Promotion Rule:**
A student shall be promoted from third year to fourth year only if he fulfils the academic requirements of 42 credits from the examinations.
 - Two regular and one supplementary examinations of II year I semester.
 - One regular and one supplementary examinations of II year II semester.
 - One regular examination of III year I semester.

5. Award of Class:

After a student has satisfied the requirements prescribed for the completion of the program and is eligible for the award of B. Tech. Degree he shall be placed in one of the following four classes:

First Class with Distinction	70% and above	From the aggregate marks secured from the best 160 Credits. (i.e II year to IV year)
First Class	Below 70% but not less than 60%	
Second Class	Below 60% but not less than 50%	
Pass Class	Below 50% but not less than 40%	

(The marks in internal evaluation and end examination shall be shown separately in the marks memorandum)

- All other regulations as applicable for B. Tech. Four-year degree course (Regular) will hold good for B. Tech. (Lateral Entry Scheme)

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD,**

B. TECH. COMPUTER SCIENCE AND SYSTEMS ENGINEERING

I Year

COURSE STRUCTURE

Code	Subject	T	P	C
07A1BS01	English	2+1*	-	4
07A1BS02	Mathematics – I	3+1*	-	6
07A1BS06	Mathematical Methods	3+1*	-	6
07A1BS05	Applied Physics	2+1*	-	4
07A1EC01	C Programming and Data Structures	3+1*	-	6
07A1EC07	Basic Electrical Engineering	2+1*	-	4
07A1EC06	Electronics Devices and Circuits	3+1*	-	6
07A11591	Engineering Drawing	-	3	4
07A11592	Computer Programming Lab	-	3	4
07A11593	Electrical and Electronics Lab	-	3	4
07A11594	English Language Communication Skills Lab	-	3	4
07A11595	IT Work-Shop	-	3	4
Total		25	15	56

II Year

I Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A3BS03	Probability and Statistics	4+1*	-	4
07A3BS04	Mathematical Foundations of Computer Science	4+1*	-	4
07A3EC20	Advanced Data Structures and Algorithms	4+1*	-	4
07A3EC16	Digital Logic Design	4+1*	-	4
07A3HS01	Managerial Economics and Financial Analysis	4+1*	-	4
07A3EC22	Data Base Management Systems	4+1*	-	4
07A31591	Advanced Data Structures and Algorithms Lab	-	3	2
07A31592	Data Base Management Systems Lab	-	3	2
TOTAL		30	6	28

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

B.TECH. COMPUTER SCIENCE AND SYSTEMS ENGINEERING

I Year

II Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A4EC08	Object Oriented Programming	4+1*	-	4
07A4EC13	Computer Organization	4+1*	-	4
07A40404	Data Communication Systems	4+1*	-	4
07A4EC18	Microprocessors and Interfacing	4+1*	-	4
07A4EC01	Environmental Studies	4+1*	-	4
07A4EC12	Software Engineering	4+1*	-	4
07A41591	Object Oriented Programming Lab	-	3	2
07A41592	Microprocessors interfacing Lab	-	3	2
TOTAL		30	6	28

III Year

I Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A50207	Optimization Techniques	4+1*	-	4
07A5EC20	Automata and Compiler Design	4+1*	-	4
07A50504	Unix Programming	4+1*	-	4
07A51501	Systems Programming	4+1*	-	4
07A5EC11	Computer Graphics	4+1*	-	4
07A5EC21	Operating Systems	4+1*	-	4
07A51591	Advanced English Communications Skills Lab	-	3	2
07A51592	Unix Programming and Compiler Design Lab	-	3	2
TOTAL		30	6	28

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

B.TECH. COMPUTER SCIENCE AND SYSTEMS ENGINEERING

III Year

II Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A6EC08	Computer Networks	4+1*	-	4
07A60507	Mathematical Modeling and Simulation	4+1*	-	4
07A6EC09	Object Oriented Analysis and Design	4+1*	-	4
07A60508	Advanced Computer Architecture	4+1*	-	4
07A6EC13	Web Technologies	4+1*	-	4
07A60509	Artificial Intelligence	4+1*	-	4
07A61591	Computer Networks and UML Lab	-	3	2
07A61592	Web Technologies Lab	-	3	2
Total		30	6	28

IV Year

I- Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A7EC22	Multimedia and Application Development	4+1*	-	4
07A7EC20	Embedded Systems	4+1*	-	4
07A71501	Performance Evaluation of Computer Systems	4+1*	-	4
07A71502	Computer Systems Maintenance	4+1*	-	4
Elective - I		4+1*	-	4
07A7EC25	Network Management Systems			
07A7EC39	Information Security			
07A7EC40	Human Computer Interface			
Elective - II		4+1*	-	4
07A7EC24	Advanced Computing Concepts			
07A70509	Distributed Databases			
07A7EC44	Neural Networks			
07A71591	Embedded Systems Lab	-	3	2
07A71592	Multimedia and Application Development Lab-	-	3	2
TOTAL		30	6	28

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

B.TECH. COMPUTER SCIENCE AND SYSTEMS ENGINEERING

IV Year

II- Semester

COURSE STRUCTURE

Code	Subject	T	P	C
07A8HS02	Management Science	4+1*	-	4
Elective - III		4+1*	-	4
07A81202	Mobile computing			
07A8EC25	VLSI Design			
07A8EC26	Data Warehousing and Data Mining			
Elective - IV		4+1*	-	4
07A8EC13	Design Patterns			
07A80514	Software Testing Methodologies			
07A8EC24	Pattern Recognition			
07A81591	Industry Oriented Mini Project	-	-	2
07A81592	Seminar	-	-	2
07A81593	Project	-	-	10
07A81594	Comprehensive viva	-	6	2
Total		15	6	28

NOTE: All University Examinations (Theory and Practical) are of 3 hours duration.

* Tutorial

T: Theory periods per week

P: Practical /Drawing Periods per week

C: Total Credits for the subject

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

I Year B. Tech CSSE

T	P	C
2+1*	0	4

(07A1BS01) ENGLISH

1. INTRODUCTION :

In view of the growing importance of English as a tool for global communication and the consequent emphasis on training students to acquire communicative competence, the syllabus has been designed to develop linguistic and communicative competence of Engineering students. The prescribed books and the exercises are meant to serve broadly as students' handbooks.

In the English classes, the focus should be on the skills of reading, writing, listening and speaking and for this the teachers should use the text prescribed for detailed study. For example, the students should be encouraged to read the texts/selected paragraphs silently. The teachers can ask comprehension questions to stimulate discussion and based on the discussions students can be made to write short paragraphs/essays etc.

The text for non-detailed study is for extensive reading/reading for pleasure by the students. Hence, it is suggested that they read it on their own with topics selected for discussion in the class. The time should be utilized for working out the exercises given after each section, as also for supplementing the exercises with authentic materials of a similar kind for example, from newspaper articles, advertisements, promotional material etc.. *However, the stress in this syllabus is on skill development and practice of language skills.*

2. OBJECTIVES:

- To improve the language proficiency of the students in English with emphasis on LSRW skills.
- To equip the students to study academic subjects with greater facility through the theoretical and practical components of the English syllabus.
- To develop the study skills and communication skills in formal and informal situations.

3. SYLLABUS :

Listening Skills:

Objectives

- To enable students to develop their listening skill so that they may appreciate its role in the LSRW skills approach to language and improve their pronunciation
- To equip students with necessary training in listening so that can comprehend the speech of people of different backgrounds and regions

Students should be given practice in listening to the sounds of the language to be able to recognise them, to distinguish between them to mark stress and recognise and use the right intonation in sentences.

- Listening for general content
- Listening to fill up information

- Intensive listening
- Listening for specific information

Speaking Skills :

Objectives

- To make students aware of the role of speaking in English and its contribution to their success.
- To enable students to express themselves fluently and appropriately in social and professional contexts.

- Oral practice
- Describing objects/situations/people
- Role play – Individual/Group activities (Using exercises from all the nine units of the prescribed text: *Learning English : A Communicative Approach.*)
- Just A Minute(JAM) Sessions.

Reading Skills:

Objectives

- To develop an awareness in the students about the significance of silent reading and comprehension.
- To develop the ability of students to guess the meanings of words from context and grasp the overall message of the text, draw inferences etc.

- Skimming the text
- Understanding the gist of an argument
- Identifying the topic sentence
- Inferring lexical and contextual meaning
- Understanding discourse features
- Recognizing coherence/sequencing of sentences

NOTE : *The students will be trained in reading skills using the prescribed text for detailed study.*

They will be examined in reading and answering questions using 'unseen' passages which may be taken from the non-detailed text or other authentic texts, such as magazines/newspaper articles.

Writing Skills :

Objectives

- To develop an awareness in the students about writing as an exact and formal skill
- To equip them with the components of different forms of writing, beginning with the lower order ones.

- Writing sentences
- Use of appropriate vocabulary
- Paragraph writing
- Coherence and cohesiveness
- Narration / description
- Note Making
- Formal and informal letter writing
- Editing a passage

4. TEXTBOOKS PRESCRIBED:

In order to improve the proficiency of the student in the acquisition of the four skills mentioned above, the following texts and course content, divided into **Eight Units**, are prescribed:

For Detailed study

1. **LEARNING ENGLISH: A Communicative Approach**, Hyderabad: Orient Longman, 2006. (Six Selected Lessons)

For Non-detailed study

2. **WINGS OF FIRE: An Autobiography – APJ Abdul Kalam**, Abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004.

A. STUDY MATERIAL:**Unit –I**

1. Astronomy from **LEARNING ENGLISH: A Communicative Approach**, Orient Longman, 2005.
2. Chapters 1-4 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

Unit –II

3. Information Technology from **LEARNING ENGLISH: A Communicative Approach, Orient Longman, 2005**.
4. Chapters 5-8 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

Unit –III

5. Humour from **LEARNING ENGLISH: A Communicative Approach, Orient Longman, 2005**.
6. Chapters 9-12 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

Unit –IV

7. Environment from **LEARNING ENGLISH: A Communicative Approach, Orient Longman, 2005**.
8. Chapters 13-16 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

Unit –V

9. Inspiration from **LEARNING ENGLISH: A Communicative Approach, Orient Longman, 2005**.
10. Chapters 17-20 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

Unit – VI

11. Human Interest from **LEARNING ENGLISH: A Communicative Approach, Orient Longman, 2005**.
12. Chapters 21-24 from **Wings of Fire: An Autobiography – APJ Abdul Kalam, an abridged version with Exercises, Universities Press (India) Pvt. Ltd., 2004**.

* Exercises from the lessons not prescribed shall also be used for classroom tasks.

Unit – VII**Exercises on**

Reading and Writing Skills
Reading Comprehension
Situational dialogues
Letter writing
Essay writing

Unit – VIII**Practice Exercises on Remedial Grammar covering**

Common errors in English, Subject-Verb agreement, Use of Articles and Prepositions,
Tense and aspect

Vocabulary development covering

Synonyms & Antonyms, one-word substitutes, prefixes & suffixes, Idioms & phrases, words often confused.

REFERENCES :

1. **Strengthen Your English**, Bhaskaran & Horsburgh, Oxford University Press
2. **Basic Communication Skills for Technology**, Andrea J Rutherford, Pearson Education Asia.
3. **Murphy's English Grammar with CD**, Murphy, Cambridge University Press
4. **English Skills for Technical Students** by Orient Longman
5. **Everyday Dialogues in English** by Robert J. Dixon, Prentice-Hall of India Ltd., 2006.
6. **English For Technical Communication**, Vol. 1 & 2, by K. R. Lakshmi Narayanan, Sci tech. Publications.
7. **A Hand book of English for Engineers & Technologists** by Dr. P. Eliah, B. S. Publications.
8. **Developing Communication Skills** by Krishna Mohan & Meera Benerji (Macmillan)
9. **Speaking and Writing for Effective Business Communication**, Francis Soundararaj, MacMillan India Ltd., 2007.
10. **The Oxford Guide to Writing and Speaking**, John Seely, Oxford

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

I Year B. Tech CSSE

T	P	C
3+1*	0	6

(07A1BS02) MATHEMATICS – I

UNIT – I

Differential equations of first order and first degree – exact, linear and Bernoulli. Applications to Newton's Law of cooling, Law of natural growth and decay, orthogonal trajectories.

UNIT – II

Non-homogeneous linear differential equations of second and higher order with constant coefficients with RHS term of the type e^{ax} , $\sin ax$, $\cos ax$, polynomials in x , $eV(x)$, $xV(x)$, method of variation of parameters.

UNIT – III

Rolle's Theorem – Lagrange's Mean Value Theorem – Cauchy's mean value Theorem – Generalized Mean Value theorem (all theorems without proof) Functions of several variables – Functional dependence- Jacobian- Maxima and Minima of functions of two variables with constraints and without constraints

UNIT – IV

Radius, Centre and Circle of Curvature – Evolutes and Envelopes Curve tracing – Cartesian, polar and Parametric curves.

UNIT – V

Applications of integration to lengths, volumes and surface areas in Cartesian and polar coordinates multiple integrals - double and triple integrals – change of variables – change of order of integration.

UNIT – VI

Sequences – series – Convergences and divergence – Ratio test – Comparison test – Integral test – Cauchy's root test – Raabe's test – Absolute and conditional convergence

UNIT – VII

Vector Calculus: Gradient- Divergence- Curl and their related properties of sums-products- Laplacian and second order operators. Vector Integration - Line integral – work done – Potential function – area- surface and volume integrals Vector integral theorems: Green's theorem-Stoke's and Gauss's Divergence Theorem (With out proof). Verification of Green's - Stoke's and Gauss's Theorems.

UNIT – VIII

Laplace transform of standard functions – Inverse transform – first shifting Theorem, Transforms of derivatives and integrals – Unit step function – second shifting theorem – Dirac's delta function – Convolution theorem – Periodic function – Differentiation and integration of transforms-Application of Laplace transforms to ordinary differential equations Partial fractions-Heaviside's Partial fraction expansion theorem.

Text Books:

1. A text Book of Engineering Mathematics, Vol-1 T. K. V. Iyengar, B. Krishna Gandhi and Others, S. Chand & Company.
2. A text Book of Engineering Mathematics, C. Sankaralah, V. G. S. Book Links.
3. A text Book of Engineering Mathematics, Shahnaz Bathul, Right Publishers.
4. A text Book of Engineering Mathematics, P. Nageshwara Rao, Y. Narasimhulu & N. Prabhakar Rao, Deepthi Publications.

References:

1. A text Book of Engineering Mathematics, B. V. Raman, Tata Mc Graw Hill.
2. Advanced Engineering Mathematics, Irvin Kreyszig, Wiley India Pvt. Ltd.
3. A text Book of Engineering Mathematics, Thomson Book Collection.

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

I Year B. Tech CSSE

T	P	C
3+1*	0	6

(07A1BS06) MATHEMATICAL METHODS

UNIT – I

Matrices and Linear systems of equations: Elementary row transformations-Rank-Echelon form, Normal form – Solution of Linear Systems – Direct Methods- LU Decomposition- LU Decomposition from Gauss Elimination –Solution of Tridiagonal Systems-Solution of Linear Systems

UNIT – II

Eigen values, eigen vectors – properties – Cayley-Hamilton Theorem - Inverse and powers of a matrix by Cayley-Hamilton theorem – Diagonalization of matrix. Calculation of powers of matrix – Modal and spectral matrices.

UNIT – III

Real matrices – Symmetric, skew - symmetric, orthogonal, Linear Transformation – Orthogonal Transformation. Complex matrices: Hermitian, Skew-Hermitian and Unitary – Eigen values and eigen vectors of complex matrices and their properties. Quadratic forms- Reduction of quadratic form to canonical form – Rank - Positive, negative definite - semi definite - index - signature - Sylvester law.

UNIT – IV

Solution of Algebraic and Transcendental Equations: Introduction – The Bisection Method – The Method of False Position – The Iteration Method – Newton-Raphson Method.

Interpolation: Introduction- Errors in Polynomial Interpolation – Finite differences- Forward Differences- Backward differences –Central differences – Symbolic relations and separation of symbols-Differences of a polynomial-Newton's formulae for interpolation – Central difference interpolation Formulae – Gauss Central Difference Formulae –Interpolation with unevenly spaced points-Lagrange's Interpolation formula.

UNIT – V

Curve fitting: Fitting a straight line –Second degree curve-exponential curve-power curve by method of least squares. Numerical Differentiation and Integration–Trapezoidal rule – Simpson's 1/3 Rule –Simpson's 3/8 Rule.

UNIT – VI

Numerical solution of Ordinary Differential equations: Solution by Taylor's series-Picard's Method of successive Approximations-Euler's Method-Runge-Kutta Methods –Predictor-Corrector Methods- Adams- Moulton Method –Milne's Method.

UNIT – VII

Fourier Series: Determination of Fourier coefficients – Fourier series – even and odd functions – Fourier series in an arbitrary interval – even and odd periodic continuation – Half-range Fourier sine and cosine expansions. Fourier integral theorem (only statement)– Fourier sine and cosine integrals. Fourier transform – Fourier sine and cosine transforms – properties – inverse transforms – Finite Fourier transforms.

UNIT – VIII

Formation of partial differential equations by elimination of arbitrary constants and arbitrary functions –solutions of first order linear (Lagrange) equation and nonlinear (standard type) equations. Method of separation of variables. z-transform – inverse z-transform - properties – Damping rule – Shifting rule – Initial and final value theorems. Convolution theorem – Solution of difference equation by z-transforms.

Text Books:

1. Mathematical Methods, T. K. V. Iyengar, B. Krishna Gandhi and Others, S. Chand & Company.
2. Mathematical Methods, C. Sankaraiah, V. G. S. Book Links.
3. A text book of Mathematical Methods, V. Ravindranath, A. Vijayalaxmi, Himalaya Publishers.
4. A text book of Mathematical Methods, Shahnaz Bathul, Right Publissers.

References:

1. A text Book of Engineering Mathematics, B. V. Raman, Tata Mc Graw Hill.
2. Advanced Engineering Mathematics, Irvin Kreyszig, Wiley India Pvt. Ltd.
3. Numerical Methods for Scientific and Engineering Computation, M. K. Jain, S. R. K. Iyengar & R. K. Jain, New Age International Publishers.
4. Elementary Numerical Analysis, Aitkinson & Han, Wiely India, 3rd Edition, 2006

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(07A1BS05) APPLIED PHYSICS

UNIT I

BONDING IN SOLIDS : Introduction - Types of bonding in solids - Estimation of cohesive energy - Madelung constant.

CRYSTAL STRUCTURES AND X-RAY DIFFRACTION: Introduction - Space lattice - Basis - Unit cell - Lattice parameter - Bravais lattices - Crystal systems - Structure and packing fractions of Simple cubic - Body centered cubic - Face centered cubic crystals - Directions and planes in crystals - Miller indices - Separation between successive [h k l] planes - Diffraction of X-rays by crystal planes - Bragg's law - Laue method - Powder method.

UNIT II

PRINCIPLES OF QUANTUM MECHANICS: Waves and particles - Planck's quantum theory - de Broglie hypothesis - Matter waves - Davisson and Germer experiment - G. P. Thomson experiment - Heisenberg uncertainty principle - Schrödinger's time independent wave equation - Physical significance of the wave function - Particle in one dimensional potential box.

UNIT III

ELECTRON THEORY OF METALS: Classical free electron theory - Mean free path - Relaxation time and drift velocity - Quantum free electron theory - Fermi-Dirac distribution (analytical) and its dependence on temperature - Fermi energy - Electron scattering and resistance.

BAND THEORY OF SOLIDS: Bloch theorem - Kronig-Penney model (qualitative treatment) - Origin of energy band formation in solids - Classification of materials into conductors, semi conductors & insulators - Concept of effective mass of an electron.

UNIT IV

DIELECTRIC PROPERTIES: Introduction - Dielectric constant - Electronic, ionic and orientational polarizations - Internal fields in solids - Clausius - Mossotti equation - Dielectrics in alternating fields - Frequency dependence of the polarizability - Ferro and Piezo electricity.

MAGNETIC PROPERTIES : Permeability - Magnetization - Origin of magnetic moment - Classification of magnetic materials - Dia, para and ferro magnetism - Hysteresis curve - Soft and hard magnetic materials.

UNIT V

SEMICONDUCTORS : Introduction - Intrinsic semiconductor and carrier concentration - Equation for conductivity - Extrinsic semiconductor and carrier concentration - Drift and diffusion - Einstein's equation - Hall effect - Direct & indirect band gap semiconductors.

SUPERCONDUCTIVITY: General properties - Meissner effect - Penetration depth - Type I and Type II superconductors - Flux quantization - DC and AC Josephson effect - BCS Theory - Applications of superconductors.

UNIT VI

LASERS: Introduction - Characteristics of Lasers - Spontaneous and stimulated emission of radiation - Einstein's coefficients - Population inversion - Ruby laser - Helium-Neon Laser - CO₂ laser - Semiconductor Laser - Applications of lasers.

UNIT VII

FIBER OPTICS AND HOLOGRAPHY: Introduction - Principle of optical fiber - Acceptance angle and acceptance cone - Numerical aperture - Types of optical fibers and refractive index profiles - Attenuation in optical fibers - Application of optical fibers - Basic principles of holography - Construction and reconstruction of image on hologram - Applications of holography.

UNIT VIII

SCIENCE & TECHNOLOGY OF NANOMATERIALS: Introduction to Nano materials - Basic principles of Nanoscience & Technology - Fabrication of nano materials - Physical & chemical properties of nanomaterials - Carbon nanotubes - Applications of nanotechnology.

TEXTBOOKS:

1. Applied Physics 2nd edition by Dr. P. Appala Naidu & Dr. M. Chandra Shekar, V.G.S. Book links.
2. Introduction to Solid State Physics by C. Kittel ; Wiley Eastern Ltd.
3. Nanotechnology by Mark Ratner and Daniel Ratner; Pearson Education.

REFERENCES:

1. Materials Science and Engineering by V. Raghavan; Prentice-Hall India.
2. Materials Science by M. Arumugam; Anuradha Agencies.
3. Solid State Physics by N.W. Ashcroft & N. David Merwin; Thomson Learning.
4. Materials Science by M.S.Vijaya & G. Rangarajan; Tata McGraw Hill.
5. Solid State Physics by P.K. Palanisamy; Scitech Publications (India) Pvt. Ltd.
6. Nano Materials by A.K. Bandyopadhyay, New Age International Publishers.
7. Applied Physics by P.K.Mittal; I.K. International.
8. Applied Physics by K. Vijay Kumar & T. Sreekanth; S. Chand & Company Ltd.

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(07A1EC01) C PROGRAMMING AND DATA STRUCTURES

UNIT - I

Algorithm / pseudo code, flowchart, program development steps, structure of C program, A Simple C program, identifiers, basic data types and sizes, Constants, variables, arithmetic, relational and logical operators, increment and decrement operators, conditional operator, bit-wise operators, assignment operators, expressions, type conversions, conditional expressions, precedence and order of evaluation.

Input-output statements, statements and blocks, if and switch statements, loops- while, do-while and for statements, break, continue, goto and labels, programming examples.

UNIT - II

Designing structured programs, Functions, basics, parameter passing, storage classes- extern, auto, register, static, scope rules, block structure, user defined functions, standard library functions, recursive functions, header files, C preprocessor, example c programs.

UNIT - III

Arrays- concepts, declaration, definition, accessing elements, storing elements, arrays and functions, two-dimensional and multi-dimensional arrays, applications of arrays, pointers- concepts, initialization of pointer variables, pointers and function arguments, address arithmetic, Character pointers and functions, pointers to pointers, pointers and multidimensional arrays, dynamic memory managements functions, command line arguments, c program examples.

UNIT - IV

Derived types- structures- declaration, definition and initialization of structures, accessing structures, nested structures, arrays of structures, structures and functions, pointers to structures, self referential structures, unions, typedef, bitfields, C program examples.

UNIT - V

Input and output – concept of a file, text files and binary files, streams, standard I/o, Formatted I/o, file I/o operations, error handling, C program examples.

UNIT - VI

Searching – Linear and binary search methods, sorting – Bubble sort, selection sort, Insertion sort, Quick sort, merge sort.

UNIT - VII

Introduction to data structures, singly linked lists, doubly linked lists, circular list, representing stacks and queues in C using arrays and linked lists, infix to post fix conversion, postfix expression evaluation.

UNIT - VIII

Trees- Binary tree, terminology, representation, traversals, graphs- terminology, representation, graph traversals (dfs & bfs)

TEXT BOOKS :

1. Computer science, A structured programming approach using C, B.A. Forouzan and R.F. Gilberg, Third edition, Thomson.
2. Data Structures Using C – A.S.Tanenbaum, Y. Langsam, and M.J. Augenstein, PHI/Pearson education.

REFERENCES :

1. C& Data structures – P. Padmanabham, B.S. Publications.
2. The C Programming Language, B.W. Kernighan, Dennis M. Ritchie, PHI/Pearson Education
3. C Programming with problem solving, J.A. Jones & K. Harrow, dreamtech Press
4. Programming in C – Stephen G. Kochan, III Edition, Pearson Education.
5. Data Structures and Program Design in C, R.Kruse, C.L. Tondo, BP Leung, Shashi M, Second Edition, Pearson Education.

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(07A1EC07) BASIC ELECTRICAL ENGINEERING

UNIT - I

Introduction to Electrical Engineering : Essence of electricity, Conductors, semiconductors and insulators (elementary treatment only); Electric field; electric current, potential and potential difference, electromotive force, electric power, ohm's law, basic circuit components, electromagnetism related laws, Magnetic field due to electric current flow, force on a current carrying conductor placed in a magnetic field, Faradays laws of electromagnetic induction. Types of induced EMF's, Kirchhoff's laws. Simple problems.

UNIT-II

Network Analysis : Basic definitions, types of elements, types of sources, resistive networks, inductive networks, capacitive networks, series parallel circuits, star delta and delta star transformation, Network theorems- Superposition, Thevenin's, Maximum power transfer theorems and simple problems.

UNIT-III

Magnetic Circuits : Basic definitions, analogy between electric and magnetic circuits, magnetization characteristics of Ferro magnetic materials, self inductance and mutual inductance, energy in linear magnetic systems, coils connected in series, attracting force of electromagnets.

UNIT-IV

Alternating Quantities : Principle of ac voltages, waveforms and basic definitions, relationship between frequency, speed and number of poles, root mean square and average values of alternating currents and voltage, form factor and peak factor, phasor representation of alternating quantities, the J operator and phasor algebra, analysis of ac circuits with single basic network element, single phase series circuits, single phase parallel circuits, single phase series parallel circuits, power in ac circuits.

UNIT-V

Transformers : Principles of operation, Constructional Details, Ideal Transformer and Practical Transformer, Losses, Transformer Test, Efficiency and Regulation Calculations (All the above topics are only elementary treatment and simple problems).

UNIT-VI

Direct current machines : Principle of operation of dc machines, armature windings, e.m.f equation in a dc machine, Torque production in a dc machine, Operation of a dc machine as a generator, operation of a dc machine as a motor.

UNIT-VII

A.C Machines : Three phase induction motor, principle of operation, slip and rotor frequency, torque (simple problems). Synchronous Machines: Principle of operation, EMF equation (Simple problems on EMF), Synchronous motor principle and operation (Elementary treatment only)

UNIT VIII

Basic Instruments : Introduction, classification of instruments, operating principles, essential features of measuring instruments, Moving coil permanent magnet (PMMC) instruments, Moving Iron of Ammeters and Voltmeters (elementary Treatment only)

TEXT BOOKS :

1. Basic Electrical Engineering - By M.S.Naidu and S. Kamakshiah - TMH.
2. Basic Electrical Engineering -By T.K.Nagasarkar and M.S. Sukhija Oxford University Press.

REFERENCES :

1. Theory and Problems of Basic Electrical Engineering by D.P.Kothari & I.J. Nagrath PHI.
2. Principles of Electrical Engineering by V.K Mehta, S.Chand Publications.
3. Essentials of Electrical and Computer Engineering by David V. Kerns, JR. J. David Irwin Pearson.

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(07A1EC06) ELECTRONIC DEVICES AND CIRCUITS

UNIT-I

ELECTRON DYNAMICS AND CRO: Motion of charged particles in electric and magnetic fields. Simple problems involving electric and magnetic fields only. Electrostatic and magnetic focusing. Principles of CRT, deflection sensitivity (Electrostatic and magnetic deflection), Parallel Electric and Magnetic fields, Perpendicular Electric and Magnetic fields.

UNIT- II

JUNCTION DIODE CHARACTERISTICS : Review of semi conductor Physics – n and p –type semi conductors, Mass Action Law, Continuity Equation, Hall Effect, Fermi level in intrinsic and extrinsic semiconductors, Open-circuited p-n junction, The p-n junction Energy band diagram of PN diode, PN diode as a rectifier (forward bias and reverse bias), The current components in p-n diode, Law of junction, Diode equation, Volt-ampere characteristics of p-n diode, Temperature dependence of VI characteristic, Transition and Diffusion capacitances, Step graded junction, Breakdown Mechanisms in Semi Conductor (Avalanche and Zener breakdown) Diodes, Zener diode characteristics, Characteristics of Tunnel Diode with the help of energy band diagrams, Varactor Diode, LED, LCD. And photo diode

UNIT- III

RECTIFIERS, FILTERS AND REGULATORS : Half wave rectifier, ripple factor, full wave rectifier, Harmonic components in a rectifier circuit, Inductor filter, Capacitor filter, L- section filter, Π - section filter, Multiple L- section and Multiple Π section filter, and comparison of various filter circuits? in terms of ripple factors, Simple circuit of a regulator using zener diode, Series and Shunt voltage regulators

UNIT- IV

TRANSISTOR and FET CHARACTERISTICS : Junction transistor, Transistor current components, Transistor as an amplifier, Transistor construction, Detailed study of currents in a transistor, Transistor alpha, Input and Output characteristics of transistor in Common Base, Common Emitter, and Common collector configurations, Relation between Alpha and Beta, typical transistor junction voltage values, JFET characteristics (Qualitative and Quantitative discussion), Small signal model of JFET, MOSFET characteristics (Enhancement and depletion mode), Symbols of MOSFET, Comparison of Transistors, Introduction to SCR and UJT.

UNIT-V

BIASING AND STABILISATION : BJT biasing, DC equivalent model, criteria for fixing operating point, Fixed bias, Collector to base bias, Self bias techniques for

stabilization, Stabilization factors, (S, S', S'') , Compensation techniques, (Compensation against variation in V_{BE}, I_{CO}) Thermal run away, Thermal stability,

UNIT- VI

AMPLIFIERS : Small signal low frequency transistor amplifier circuits: h-parameter representation of a transistor, Analysis of single stage transistor amplifier using h-parameters: voltage gain, current gain, Input impedance and Output impedance. Comparison of transistor configurations in terms of A_v, R_i, A_v, R_o .

UNIT- VII

FEEDBACK AMPLIFIERS : Concept of feedback, Classification of feedback amplifiers, General characteristics of negative feedback amplifiers, Effect of Feedback on input and output characteristics, Voltage series, voltage shunt, current series, and current shunt feedback amplifiers with discrete components and their analysis

UNIT-VIII

OSCILLATORS : Condition for oscillations. RC-phase shift oscillators with Transistor and FET, Hartley and Colpitts oscillators, Wein bridge oscillator, Crystal oscillators, Frequency and amplitude stability of oscillators,

TEXT BOOKS :

1. Electronic Devices and Circuits – J.Millman, C.C.Halkias, and Satyabratha Jit Tata McGraw Hill, 2nd Ed., 2007.
2. Electronic Devices and Circuits – R.L. Boylestad and Louis Nashelsky, Pearson/Prentice Hall, 9th Edition, 2006.

REFERENCES :

1. Electronic Devices and Circuits – T.F. Bogart Jr., J.S.Beasley and G.Rico, Pearson Education, 6th edition, 2004.
2. Principles of Electronic Circuits – S.G.Burns and P.R.Bond, Galgotia Publications, 2nd Edn., 1998.
3. Microelectronics – Millman and Grabel, Tata McGraw Hill, 1988.
4. Electronic Devices and Circuits – Dr. K. Lal Kishore, B.S. Publications, 2nd Edition, 2005.
5. Electronic Devices and Circuits- Prof GS N Raju I K International Publishing House Pvt .Ltd 2006

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(07A11591) ENGINEERING DRAWING

UNIT – I

Introduction to engineering graphics – construction of ellipse, parabola and hyperbola – cylindrical curves.

UNIT – II

Orthographic projections of points, lines and planes – axis inclined to one planes and inclined to both the planes.

UNIT – III**Orthographic projections of solids :**

Cylinder, cone, prism, pyramid and sphere positions and axis inclined to both the planes.

UNIT – IV

Isomeric projections of lines, planes and simple solids

UNIT – V

Conversion of orthographic views into isometric views and vice-versa.

TEXT BOOKS :

1. Engineering drawings By N.D.Bhatt
2. Engineering graphics By K.L. Narayana & P.Kannayya

REFERENCES:-

1. Engineering drawing and graphics: Venugopal/ New age
2. Engineering drawing : Johle / TMH

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(07A11592) COMPUTER PROGRAMMING LAB

Objectives:

- To make the student learn a programming language.
- To teach the student to write programs in C solve the problems
- To introduce the student to simple linear and non linear data structures such as lists, stacks, queues, trees and graphs.

Recommended Systems/Software Requirements:

- Intel based desktop PC
- ANSI C Compiler with Supporting Editors

Week 1.

- a) Write a C program to find the sum of individual digits of a positive integer.
- b) A Fibonacci Sequence is defined as follows: the first and second terms in the sequence are 0 and 1. Subsequent terms are found by adding the preceding two terms in the sequence. Write a C program to generate the first n terms of the sequence.
- c) Write a C program to generate all the prime numbers between 1 and n, where n is a value supplied by the user.

Week 2.

- a) Write a C program to calculate the following Sum:
Sum = $1 - x^2/2! + x^4/4! - x^6/6! + x^8/8! - x^{10}/10!$
- b) Write a C program to find the roots of a quadratic equation.

Week 3

- a) Write C programs that use both recursive and non-recursive functions
 - i) To find the factorial of a given integer.
 - ii) To find the GCD (greatest common divisor) of two given integers.
 - iii) To solve Towers of Hanoi problem.

Week 4

- a) The total distance travelled by vehicle in 't' seconds is given by distance = $ut + 1/2at^2$ where 'u' and 'a' are the initial velocity (m/sec.) and acceleration (m/sec²). Write C program to find the distance travelled at regular intervals of time given the values of 'u' and 'a'. The program should provide the flexibility to the user to select his own time intervals and repeat the calculations for different values of 'u' and 'a'.

b) Write a C program, which takes two integer operands and one operator from the user, performs the operation and then prints the result. (Consider the operators +, -, *, /, % and use Switch Statement)

Week 5

a) Write a C program to find both the largest and smallest number in a list of integers.

b) Write a C program that uses functions to perform the following:

- i) Addition of Two Matrices
- ii) Multiplication of Two Matrices

Week 6

a) Write a C program that uses functions to perform the following operations:

- i) To insert a sub-string in to given main string from a given position.
- ii) To delete n Characters from a given position in a given string.

b) Write a C program to determine if the given string is a palindrome or not

Week 7

a) Write a C program that displays the position or index in the string S where the string T begins, or - 1 if S doesn't contain T.

b) Write a C program to count the lines, words and characters in a given text.

Week 8

a) Write a C program to generate Pascal's triangle.

b) Write a C program to construct a pyramid of numbers.

Week 9

Write a C program to read in two numbers, x and n, and then compute the sum of this geometric progression:

$$1+x+x^2+x^3+\dots+x^n$$

For example: if n is 3 and x is 5, then the program computes $1+5+25+125$.

Print x, n, the sum

Perform error checking. For example, the formula does not make sense for negative exponents – if n is less than 0. Have your program print an error message if $n < 0$, then go back and read in the next pair of numbers of without computing the sum.

Are any values of x also illegal? If so, test for them too.

Week 10

a) 2's complement of a number is obtained by scanning it from right to left and complementing all the bits after the first appearance of a 1. Thus 2's complement of 11100 is 00100. Write a C program to find the 2's complement of a binary number.

b) Write a C program to convert a Roman numeral to its decimal equivalent.

Week 11

Write a C program that uses functions to perform the following operations:

- i) Reading a complex number
- ii) Writing a complex number
- iii) Addition of two complex numbers
- iv) Multiplication of two complex numbers

(Note: represent complex number using a structure.)

Week 12

a) Write a C program which copies one file to another.

b) Write a C program to reverse the first n characters in a file.

(Note: The file name and n are specified on the command line.)

Week 13

Write a C program that uses functions to perform the following operations on singly linked list:

- i) Creation ii) Insertion iii) Deletion iv) Traversal

Week 14

Write a C program that uses functions to perform the following operations on doubly linked list:

- i) Creation ii) Insertion iii) Deletion iv) Traversal in both ways

Week 15

Write C programs that implement stack (its operations) using

- i) Arrays ii) Pointers

Week 16

Write C programs that implement Queue (its operations) using

- i) Arrays ii) Pointers

Week 17

Write a C program that uses Stack operations to perform the following:

- i) Converting infix expression into postfix expression
- ii) Evaluating the postfix expression

Week 18

Write a C program that uses functions to perform the following:

- i) Creating a Binary Tree of integers
- ii) Traversing the above binary tree in preorder, inorder and postorder.

Week 19

Write C programs that use both recursive and non recursive functions to perform the following searching operations for a Key value in a given list of integers :

- i) Linear search ii) Binary search

Week 20

Write C programs that implement the following sorting methods to sort a given list of integers in ascending order:

- i) Bubble sort ii) Quick sort

Week 21

Write C programs that implement the following sorting methods to sort a given list of integers in ascending order:

- i) Insertion sort ii) Merge sort

Week 22

Write C programs to implement the Lagrange interpolation and Newton- Gregory forward interpolation.

Week 23

Write C programs to implement the linear regression and polynomial regression algorithms.

Week 24

Write C programs to implement Trapezoidal and Simpson methods.

Text Books

1. C programming and Data Structures, P. Padmanabham, Third Edition, BS Publications
2. Data Structures: A pseudo code approach with C, second edition R.F. Gilberg and B.A. Forouzan
3. Programming in C, P.Dey & M. Ghosh, Oxford Univ.Press.
4. C and Data Structures, E Balaguruswamy, TMH publications.

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(07A11593) ELECTRICAL AND ELECTRONICS LAB

PART - A

1. Serial and Parallel Resonance – Timing, Resonant frequency, Bandwidth and Q-factor determination for RLC network.
2. Time response of first order RC/RL network for periodic non-sinusoidal inputs – time constant and steady state error determination.
3. Two port network parameters – Z-Y Parameters, chain matrix and analytical verification.
4. Verification of Superposition and Reciprocity theorems.
5. Verification of maximum power transfer theorem. Verification on DC, verification on AC with Resistive and Reactive loads.
6. Experimental determination of Thevenin's and Norton's equivalent circuits and verification by direct test.
7. Magnetization characteristics of D.C. Shunt generator. Determination of critical field resistance.
8. Swinburne's Test on DC shunt machine (Predetermination of efficiency of a given DC Shunt machine working as motor and generator).
9. Brake test on DC shunt motor. Determination of performance Characteristics.
10. OC & SC tests on Single-phase transformer (Predetermination of efficiency and regulation at given power factors and determination of equivalent circuit).
11. Brake test on 3-phase Induction motor (performance characteristics).
12. Regulation of alternator by synchronous impedance method

PART - B

1. Identification, Specifications and Testing of R, L, C Components (colour codes), Potentiometers, Switches (SPDT, DPDT and DIP), Coils, Gang Condensers, Relays, Bread Boards. Identification and Specifications of active devices, Diodes, BJTs, Lowpower JFETs, MOSFETs, LEDs, LCDs, SCR, UJT, Linear and Digital ICs.
2. PN Junction Diode Characteristics (Forward bias, Reverse bias)
3. Zener Diode Characteristics
4. Transistor CE Characteristics (Input and Output)
5. Rectifier without Filters (Full wave & Half wave)
6. Rectifier with Filters (Full wave & half wave)
7. SCR Characteristics
8. FET Characteristics
9. CE and CC Amplifier
10. Feedback Amplifier (Voltage Series/Current series)
11. RC Phase Shift Oscillator
12. Hartely/Colpitts Oscillator

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(07A11594) ENGLISH LANGUAGE COMMUNICATION SKILLS LAB

The **Language Lab** focuses on the production and practice of sounds of language and familiarises the students with the use of English in everyday situations and contexts.

Objectives:

1. To expose the students to a variety of self-instructional, learner-friendly modes of language learning.
2. To help the students cultivate the habit of reading passages from the computer monitor, thus providing them with the required facility to face computer-based competitive exams such GRE, TOEFL, GMAT etc.
3. To enable them to learn better pronunciation through stress on word accent, intonation, and rhythm.
4. To train them to use language effectively to face interviews, group discussions, public speaking.
5. To initiate them into greater use of the computer in resume preparation, report writing, format-making etc.

SYLLABUS :

The following course content is prescribed for the **English Language Laboratory** sessions:

1. Introduction to the Sounds of English- Vowels, Diphthongs & Consonants.
2. Introduction to Stress and Intonation.
3. Situational Dialogues / Role Play.
4. Oral Presentations- Prepared and Extempore.
5. 'Just A Minute' Sessions (JAM).
6. Describing Objects / Situations / People.
7. Information Transfer
8. Debate
9. Telephoning Skills.
10. Giving Directions.

Minimum Requirement:

The **English Language Lab** shall have two parts:

- i) **The Computer aided Language Lab** for 60 students with 60 systems, one master console, LAN facility and English language software for self- study by learners.
- ii) **The Communication Skills Lab** with movable chairs and audio-visual aids with a P.A System, a T. V., a digital stereo -audio & video system and camcorder etc.

System Requirement (Hardware component):

Computer network with Lan with minimum 60 multimedia systems with the following specifications:

- i) P – IV Processor

- a) Speed – 2.8 GHZ
 - b) RAM – 512 MB Minimum
 - c) Hard Disk – 80 GB
- ii) Headphones of High quality

Suggested Software:

- Cambridge Advanced Learners' English Dictionary with CD.
- The Rosetta Stone English Library
- Clarity Pronunciation Power – Part I
- Mastering English in Vocabulary, Grammar, Spellings, Composition
- Dorling Kindersley series of Grammar, Punctuation, Composition etc.
- Language in Use, Foundation Books Pvt Ltd with CD.
- Oxford Advanced Learner's Compass, 7th Edition
- Learning to Speak English - 4 CDs
- Microsoft Encarta with CD
- Murphy's English Grammar, Cambridge with CD
- English in Mind, Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge

Books Suggested for English Language Lab Library (to be located within the lab in addition to the CDs of the text book which are loaded on the systems):

1. **Spoken English** (CIEFL) in 3 volumes with 6 cassettes, OUP.
2. **English Pronouncing Dictionary** Daniel Jones Current Edition with CD.
3. **Spoken English-** R. K. Bansal and J. B. Harrison, Orient Longman 2006 Edn.
4. **English Language Communication : A Reader cum Lab Manual** Dr A Ramakrishna Rao, Dr G Natanam & Prof SA Sankaranarayanan, Anuradha Publications, Chennai
5. **Speaking English Effectively** by Krishna Mohan & NP Singh (Macmillan)
6. **A Practical Course in English Pronunciation**, (with two Audio cassettes) by J. Sethi, Kamlesh Sadanand & D.V. Jindal, Prentice-Hall of India Pvt. Ltd., New Delhi.
7. **A text book of English Phonetics for Indian Students** by T. Balasubramanian (Macmillan)
8. **English Skills for Technical Students**, WBSCTE with British Council, OL

DISTRIBUTION AND WEIGHTAGE OF MARKS**English Language Laboratory Practical Paper:**

1. The practical examinations for the English Language Laboratory shall be conducted as per the University norms prescribed for the core engineering practical sessions.
2. For the Language lab sessions, there shall be a continuous evaluation during the year for 25 sessional marks and 50 year-end Examination marks. Of the 25 marks, 15 marks shall be awarded for day-to-day work and 10 marks to be awarded by conducting Internal Lab Test(s). The year- end Examination shall be conducted by the teacher concerned with the help of another member of the staff of the same department of the same institution.

**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY
HYDERABAD**

I Year B. Tech CSSE

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(07A11595) IT WORKSHOP

Objectives :

The IT Workshop for engineers is a 6 training lab course spread over 90 hours. The modules include training on PC Hardware, Internet & World Wide Web and Productivity tools including Word, Excel, Power Point and Publisher.

PC Hardware introduces the students to a personal computer and its basic peripherals, the process of assembling a personal computer, installation of system software like MS Windows, Linux and the required device drivers. In addition hardware and software level troubleshooting process, tips and tricks would be covered.

Internet & World Wide Web module introduces the different ways of hooking the PC on to the internet from home and workplace and effectively usage of the internet. Usage of web browsers, email, newsgroups and discussion forums would be covered. In addition, awareness of cyber hygiene, i.e., protecting the personal computer from getting infected with the viruses, worms and other cyber attacks would be introduced.

Productivity tools module would enable the students in crafting professional word documents, excel spread sheets, power point presentations and personal web sites using the Microsoft suite of office tools and LaTeX.

PC Hardware

Week 1 – Task 1 : Identify the peripherals of a computer, components in a CPU and its functions. Draw the block diagram of the CPU along with the configuration of each peripheral and submit to your instructor.

Week 2 – Task 2 : Every student should disassemble and assemble the PC back to working condition. Lab instructors should verify the work and follow it up with a Viva. Also students need to go through the video which shows the process of assembling a PC. A video would be given as part of the course content.

Week 3 – Task 3 : Every student should individually install MS windows on the personal computer. Lab instructor should verify the installation and follow it up with a Viva.

Week 4 – Task 4 : Every student should install Linux on the computer. This computer should have windows installed. The system should be configured as dual boot with both windows and Linux. Lab instructors should verify the installation and follow it up with a Viva.

Week 5 – Task 5 : Several mini tasks would be that covers Basic commands in Linux and Basic system administration in Linux which includes: Basic Linux

commands in bash, Create hard and symbolic links, Text processing, Using wildcards

Week 6 – Task 6 : Hardware Troubleshooting : Students have to be given a PC which does not boot due to improper assembly or defective peripherals. They should identify the problem and fix it to get the computer back to working condition. The work done should be verified by the instructor and followed up with a Viva

Week 7 – Task 7 : Software Troubleshooting : Students have to be given a malfunctioning CPU due to system software problems. They should identify the problem and fix it to get the computer back to working condition. The work done should be verified by the instructor and followed up with a Viva.

Week 8 – Task 8 : The test consists of various systems with Hardware / Software related troubles, Formatted disks without operating systems.

Internet & World Wide Web

Week 9 - Task 1 : Orientation & Connectivity Boot Camp : Students should get connected to their Local Area Network and access the Internet. In the process they configure the TCP/IP setting. Finally students should demonstrate, to the instructor, how to access the websites and email. If there is no internet connectivity preparations need to be made by the instructors to simulate the WWW on the LAN.

Week 10 - Task 2 : Web Browsers, Surfing the Web : Students customize their web browsers with the LAN proxy settings, bookmarks, search toolbars and pop up blockers. Also, plug-ins like Macromedia Flash and JRE for applets should be configured.

Week 11 - Task 3 : Search Engines & Netiquette : Students should know what search engines are and how to use the search engines. A few topics would be given to the students for which they need to search on Google. This should be demonstrated to the instructors.

Week 12 - Task 4 : Cyber Hygiene : Students would be exposed to the various threats on the internet and would be asked to configure their computer to be safe on the internet. They need to first install an anti virus software, configure their personal firewall and windows update on their computer. Then they need to customize their browsers to block pop ups, block active x downloads to avoid viruses and/or worms.

Week 13 Module Test A test which simulates all of the above tasks would be crafted and given to the students.

LaTeX and Word

Week 14 – Word Orientation : The mentor needs to give an overview of LaTeX and Microsoft/ equivalent (FOSS) tool word : Importance of LaTeX and MS/ equivalent (FOSS) tool Word as word Processors, Details of the four tasks and features that would be covered in each, Using LaTeX and word – Accessing, overview of toolbars, saving files, Using help and resources, rulers, format painter in word.

Task 1 : Using LaTeX and word to create project certificate. Features to be covered:-Formatting Fonts in word, Drop Cap in word, Applying Text effects, Using Character Spacing, Borders and Colors, Inserting Header and Footer, Using Date and Time option in both LaTeX and Word.

Week 15 - Task 2 : Creating project abstract Features to be covered:-Formatting Styles, Inserting table, Bullets and Numbering, Changing Text Direction, Cell alignment, Footnote, Hyperlink, Symbols, Spell Check , Track Changes.

Week 16 - Task 3 : Creating a Newsletter : Features to be covered:- Table of Content, Newspaper columns, Images from files and clipart, Drawing toolbar and Word Art, Formatting Images, Textboxes and Paragraphs

Week 17 - Task 4 : Creating a Feedback form - Features to be covered- Forms, Text Fields, Inserting objects, Mail Merge in Word.

Week 18 - LaTeX and Word Module Test - Replicate the given document inclusive of all features

Excel

Week 19 - Excel Orientation : The mentor needs to tell the importance of MS/ equivalent (FOSS) tool Excel as a Spreadsheet tool, give the details of the four tasks and features that would be covered in each. Using Excel –

Accessing, overview of toolbars, saving excel files, Using help and resources

Task 1 : Creating a Scheduler - Features to be covered:- Gridlines, Format Cells, Summation, auto fill, Formatting Text

Week 20 - Task 2 : Calculating GPA - .Features to be covered:- Cell Referencing, Formulae in excel – average, std.deviation, Charts, Renaming and Inserting worksheets, Hyper linking, Count function, LOOKUP/VLOOKUP

Week 21 - Task 3 : Performance Analysis - Features to be covered:- Split cells, freeze panes, group and outline, Sorting, Boolean and logical operators, Conditional formatting

Week 22 - Task 4 : Cricket Score Card - Features to be covered:-Pivot Tables, Interactive Buttons, Importing Data, Data Protection, Data Validation

Week 23 – Excel Module Test - Replicate the given document inclusive of all features

LaTeX and MS/equivalent (FOSS) tool Power Point

Week 24 - Task1 : Students will be working on basic power point utilities and tools which help them create basic power point presentation. Topic covered during this week includes :- PPT Orientation, Slide Layouts, Inserting Text, Word Art, Formatting Text, Bullets and Numbering, Auto Shapes, Lines and Arrows in both LaTeX and Powerpoint.

Week 25 - Task 2 : Second week helps students in making their presentations interactive. Topic covered during this week includes : Hyperlinks, Inserting –Images, Clip Art, Audio, Video, Objects, Tables and Charts

Week 26 - Task 3 : Concentrating on the in and out of Microsoft power point and presentations in LaTeX. Helps them learn best practices in designing and preparing power point presentation. Topic covered during this week includes :- Master Layouts (slide, template, and notes), Types of views (basic, presentation, slide slotter, notes etc), Inserting – Background, textures, Design Templates, Hidden slides.

Week 27 - Task 4 : Entire week concentrates on presentation part of LaTeX and power point. Topic covered during this week includes -Using Auto content wizard, Slide Transition, Custom Animation, Auto Rehearsing

Week 28 - Task 5 : Power point test would be conducted. Students will be given model power point presentation which needs to be replicated (exactly how it's asked).

Publisher

Week 29 : Help students in preparing their personal website using Microsoft/ equivalent (FOSS) tool publisher. Topic covered during this week includes - Publisher Orientation, Using Templates, Layouts, Inserting text objects, Editing text objects, Inserting Tables, Working with menu objects, Inserting pages, Hyper linking, Renaming, deleting, modifying pages, Hosting website.

REFERENCES :

1. Comdex Information Technology course tool kit Vikas Gupta, WILEY Dreamtech
2. The Complete Computer upgrade and repair book, 3rd edition Cheryl A Schmidt, WILEY Dreamtech
3. Introduction to Information Technology, ITL Education Solutions limited, Pearson Education.
4. PC Hardware and A+Handbook – Kate J. Chase PHI (Microsoft)
5. LaTeX Companion – Leslie Lamport, PHI/Pearson.
6. All LaTeX and others related material is available at
(a) www.sssolutions.in and
(b) www.sontisoftsolutions.org

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II Year B. Tech CSSE I-SEM

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(07A3BS03) PROBABILITY AND STATISTICS

UNIT-I

Probability: Sample space and events – Probability – The axioms of probability – Some Elementary theorems - Conditional probability – Baye's theorem.

UNIT-II

Random variables – Discrete and continuous – Distribution – Distribution function. Distribution

UNIT-III

Binomial and poisson distributions Normal distribution – related properties.

UNIT-IV

Sampling distribution: Populations and samples - Sampling distributions of mean (known and unknown) proportions, sums and differences.

UNIT-V

Estimation: Point estimation – interval estimation - Bayesian estimation.

UNIT-VI

Test of Hypothesis – Means– Hypothesis concerning one and two means– Type I and Type II errors. One tail, two-tail tests.

UNIT-VII

Tests of significance – Student's t-test, F-test, test. Estimation of proportions.

UNIT-VIII

Queueing Theory: Pure Birth and Death Process M/M/1 Model and Simple Problems.

Text Books:

1. Probability & Statistics, T. K. V. Iyengar, B. Krishna Gandhi and Others, S. Chand & Company.
2. A text book of Probability & Statistics, Shahnaz Bathul, V. G. S. Book Links.

References:

1. Probability & Statistics, Arnold O. Allen, Academic Press.
2. Probability & Statistics for Engineers, Miller and John E. Freund, Prentice Hall of India.
3. Probability & Statistics, Mendan Hall, Beaver Thomson Publishers.
4. Probability & Statistics, D. K. Murugeson & P. Guru Swamy, Anuradha Publishers.

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(07A3BS04) MATHEMATICAL FOUNDATION OF COMPUTER SCIENCE

UNIT-I

Mathematical Logic : Statements and notations, Connectives, Well formed formulas, Truth Tables, tautology, equivalence implication, Normal forms.

UNIT-II

Predicates : Predicative logic, Free & Bound variables, Rules of inference, Consistency, proof of contradiction, Automatic Theorem Proving.

UNIT-III

Set Theory : Properties of binary Relations, equivalence, compatibility and partial ordering relations, Hasse diagram. Functions: Inverse Function Comports of functions, recursive Functions, Lattice and its Properties, Pigeon hole principles and its application.

UNIT-IV

Algebraic structures : Algebraic systems Examples and general properties, Semi groups and monads, groups sub groups' homomorphism, Isomorphism.

UNIT-V

Elementary Combinatorics: Basis of counting, Combinations & Permutations, with repetitions, Constrained repetitions, Binomial Coefficients, Binomial Multinomial theorems, the principles of Inclusion – Exclusion.

UNIT-VI

Recurrence Relation : Generating Functions, Function of Sequences Calculating Coefficient of generating function, Recurrence relations, Solving recurrence relation by substitution and Generating funds. Characteristics roots solution of In homogeneous Recurrence Relation.

UNIT-VII

Graph Theory : Representation of Graph, DFS, BFS, Spanning Trees, planar Graphs

UNIT-VIII

Graph Theory and Applications, Basic Concepts Isomorphism and Sub graphs, Multi graphs and Euler circuits, Hamiltonian graphs, Chromatic Numbers

TEXT BOOKS :

1. Discrete and Combinational Mathematics- An Applied Introduction-5th Edition – Ralph. P.Grimaldi.Pearson Education
2. Discrete Mathematical Structures with applications to computer science Trembly J.P. & Manohar .P, TMH
3. Discrete Mathematics and its Applications, Kenneth H. Rosen, Fifth Edition.TMH.

REFERENCES :

1. Discrete Mathematics with Applications, Thomas Koshy, Elsevier
2. Discrete Mathematical Structures, Bernard Kolman, Robert C. Busby, Sharn Cutler, Pearson Education/PHI.
3. Discrete Mathematical structures Theory and application-Malik & Sen
4. Discrete Mathematics for Computer science, Garry Haggard and others, Thomson.
5. Discrete Mathematics for Computer Scientists & Mathematicians, J.L. Mott, A. Kandel, T.P. Baker Prentice Hall.
6. Logic and Discrete Mathematics, Grass Man & Trembley, Person Education.

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(07A3EC20) ADVANCED DATA STRUCTURES AND ALGORITHMS

Unit I:-

C++ Class Overview- Class Definition, Objects, Class Members, Access Control, Class Scope, Constructors and destructors, parameter passing methods, Inline functions, static class members, this pointer, friend functions, dynamic memory allocation and deallocation (new and delete), exception handling.

Unit II:-

Function Overloading, Operator Overloading, Generic Programming- Function and class templates, Inheritance basics, base and derived classes, inheritance types, base class access control, runtime polymorphism using virtual functions, abstract classes, streams I/O.

Unit III:-

Algorithms, performance analysis-time complexity and space complexity, O-notation, Omega notation and Theta notation, Review of basic data structures - the list ADT, stack ADT, queue ADT, implementation using template classes in C++, sparse matrix representation.

Unit IV:-

Dictionaries, linear list representation, skip list representation, operations- insertion, deletion and searching, hash table representation, hash functions, collision resolution-separate chaining, open addressing-linear probing, quadratic probing, double hashing, rehashing, extendible hashing, comparison of hashing and skip lists.

Unit V:-

Priority Queues – Definition, ADT, Realizing a Priority Queue using Heaps, Definition, insertion, Deletion, Application-Heap Sort, External Sorting- Model for external sorting, Multiway merge, Polyphase merge.

Unit VI:-

Search trees (part I) : Binary search trees, definition, ADT, implementation, operations-searching, insertion and deletion, Balanced search trees- AVL trees, definition, height of an AVL tree, representation, operations-insertion, deletion and searching.

Search trees (part II) : Introduction to Red -Black trees and Splay Trees, B-Trees-B-Tree of order m, height of a B-Tree, insertion, deletion and searching, Comparison of Search Trees.

Unit VII:-

Divide and Conquer- General method, applications – Binary search, merge sort, quick sort, Strassen's matrix multiplication
Efficient non recursive tree traversal algorithms, Biconnected components. Disjoint set operations, union and find algorithms.

Unit VIII:-

Greedy method and Dynamic programming : General method (Greedy), Minimum cost spanning trees, Job sequencing with deadlines, General method (Dynamic Programming), Optimal binary search trees, 0/1 knapsack problem, Ordering Matrix Multiplications

TEXT BOOKS :

1. Data Structures and Algorithm Analysis in C++, Mark Allen Weiss, Pearson Education, second edition.
2. Data structures, Algorithms and Applications in C++, S.Sahni, University press (India)
pvt ltd, 2nd edition, Orient Longman pvt.ltd.

REFERENCE :

1. Data structures and Algorithms in C++, Michael T.Goodrich, R.Tamassia and D.Mount, Seventh Edition Wiley student edition, John Wiley and Sons.
2. Data Structures and Algorithms in C++, Third Edition, Adam Drozdek, Thomson
3. Problem solving with C++, The OOP, Fourth edition, W.Savitch, Pearson education.
4. C++, The Complete Reference, 4th Edition, Herbert Schildt, TMH.
5. Data structures using C and C++, Langsam, Augenstein and Tanenbaum, PHI/ Pearson Education.

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(07A3EC16) DIGITAL LOGIC DESIGN

UNIT-I:

BINARY SYSTEMS: Digital Systems, Binary Numbers, Number base conversions, Octal and Hexadecimal Numbers, complements, Signed binary numbers, Binary codes, Binary Storage and Registers, Binary logic.

UNIT-II:

BOOLEAN ALGEBRA AND LOGIC GATES: Basic Definitions, Axiomatic definition of Boolean Algebra, Basic theorems and properties of Boolean algebra, Boolean functions canonical and standard forms, other logic operations, Digital logic gates, integrated circuits.

UNIT-III:

GATE – LEVEL MINIMIZATION: The map method, Four-variable map, Five-Variable map, product of sums simplification Don't-care conditions, NAND and NOR implementation other Two-level implementations, Exclusive – Or function, Hardware Description language (HDL)

UNIT-IV:

COMBINATIONAL LOGIC: Combinational Circuits, Analysis procedure Design procedure, Binary Adder-Subtractor Decimal Adder, Binary multiplier, magnitude comparator, Decoders, Encoders, Multiplexers, HDL for combinational circuits.

UNIT-V:

SYNCHRONOUS SEQUENTIAL LOGIC: Sequential circuits, latches, Flip-Flops Analysis of clocked sequential circuits, HDL for sequential circuits, State Reduction and Assignment, Design Procedure.

UNIT-VI

Registers, shift Registers, Ripple counters synchronous counters, other counters, HDL for Registers and counters.

UNIT-VII

Introduction, Random-Access Memory, Memory Decoding, Error Detection and correction Read-only memory, Programmable logic Array programmable Array logic, Sequential Programmable Devices.

UNIT-VIII:

ASYNCHRONOUS SEQUENTIAL LOGIC: Introduction, Analysis Procedure, Circuits with Latches, Design Procedure, Reduction of state and Flow Tables, Race- Free state Assignment Hazards, Design Example.

TEXT BOOKS:

1. DIGITAL DESIGN – Third Edition , M.Morris Mano, Pearson Education/PHI.
2. FUNDAMENTALS OF LOGIC DESIGN, Roth, 5th Edition, Thomson.

REFERENCES:

1. Switching and Finite Automata Theory by Zvi. Kohavi, Tata McGraw Hill.
2. Switching and Logic Design, C.V.S. Rao, Pearson Education
3. Digital Principles and Design – Donald D.Givone, Tata McGraw Hill, Edition.
4. Fundamentals of Digital Logic and Micro Computer Design , 5TH Edition, M. Rafiquzzaman John Wiley

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II Year B. Tech CSSE I-SEM

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(07A3HS01) MANAGERIAL ECONOMICS AND FINANCIAL ANALYSIS

Unit I Introduction to Managerial Economics:

Definition, Nature and Scope of Managerial Economics–Demand Analysis: Demand Determinants, Law of Demand and its exceptions.

Unit II Elasticity of Demand:

Definition, Types, Measurement and Significance of Elasticity of Demand. Demand Forecasting, Factors governing demand forecasting, methods of demand forecasting (survey methods, statistical methods, expert opinion method, test marketing, controlled experiments, judgmental approach to demand forecasting)

Unit III Theory of Production and Cost Analysis:

Production Function – Isoquants and Isocosts, MRTS, Least Cost Combination of Inputs, Cobb-Douglas Production function, Laws of Returns, Internal and External Economies of Scale.

Cost Analysis: Cost concepts, Opportunity cost, Fixed vs. Variable costs, Explicit costs Vs. Implicit costs, Out of pocket costs vs. Imputed costs. Break-even Analysis (BEA)-Determination of Break-Even Point (simple problems)- Managerial Significance and limitations of BEA.

Unit IV Introduction to Markets & Pricing Policies:

Market structures: Types of competition, Features of Perfect competition, Monopoly and Monopolistic Competition. Price-Output Determination in case of Perfect Competition and Monopoly.

Objectives and Policies of Pricing- Methods of Pricing: Cost Plus Pricing, Marginal Cost Pricing, Sealed Bid Pricing, Going Rate Pricing, Limit Pricing, Market Skimming Pricing, Penetration Pricing, Two-Part Pricing, Block Pricing, Bundling Pricing, Peak Load Pricing, Cross Subsidization.

Unit V Business & New Economic Environment:

Characteristic features of Business, Features and evaluation of Sole Proprietorship, Partnership, Joint Stock Company, Public Enterprises and their types, Changing Business Environment in Post-liberalization scenario.

Unit VI Capital and Capital Budgeting:

Capital and its significance, Types of Capital, Estimation of Fixed and Working capital requirements, Methods and sources of raising finance. Nature and scope of capital budgeting; features of capital budgeting proposals, Methods of Capital Budgeting: Payback Method, Accounting Rate of Return (ARR) and Net Present Value Method (simple problems)

Unit VII Introduction to Financial Accounting:

Double-Entry Book Keeping, Journal, Ledger, Trial Balance- Final Accounts (Trading Account, Profit and Loss Account and Balance Sheet with simple adjustments).

Unit VIII Financial Analysis through ratios:

Computation, Analysis and Interpretation of Liquidity Ratios (Current Ratio and quick ratio), Activity Ratios (Inventory turnover ratio and Debtor Turnover ratio), Capital structure Ratios (Debt- Equity ratio, Interest Coverage ratio), and Profitability ratios (Gross Profit Ratio, Net Profit ratio, Operating Ratio, P/E Ratio and EPS).

TEXT BOOKS:

1. Aryasri: Managerial Economics and Financial Analysis, 2/e, TMH, 2005.
2. Varshney & Maheswari: Managerial Economics, Sultan Chand, 2003.

REFERENCES:

1. Ambrish Gupta, Financial Accounting for Management, Pearson Education, New Delhi.
2. H. Craig Peterson & W. Cris Lewis, Managerial Economics, PHI, 4th Ed.
3. Suma Damodaran, Managerial Economics, Oxford University Press.
4. Lipsey & Chrystel, Economics, Oxford University Press.
5. S. A. Siddiqui & A. S. Siddiqui, Managerial Economics & Financial Analysis, New age International Space Publications.
6. Domnick Salvatore: Managerial Economics In a Global Economy, 4th Edition, Thomson.
7. Narayanaswamy: Financial Accounting—A Managerial Perspective, PHI.
8. Raghunatha Reddy & Narasimhachary: Managerial Economics & Financial Analysis, Scitech.
9. S.N.Maheswari & S.K. Maheswari, Financial Accounting, Vikas.
10. Truet and Truet: Managerial Economics: Analysis, Problems and Cases, Wiley.
11. Dwivedi: Managerial Economics, 6th Ed., Vikas.

Prerequisites: Nil

Objective: To explain the basic principles of managerial economics, accounting and current business environment underlying business decision making.

Codes/Tables: Present Value Tables need to be permitted into the examinations Hall.

Question Paper Pattern: 5 Questions to be answered out of 8 questions.

Each question should not have more than 3 bits.

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(07A3EC22) DATA BASE MANAGEMENT SYSTEMS

UNIT I :

Data base System Applications, data base System VS file System – View of Data – Data Abstraction – Instances and Schemas – data Models – the ER Model – Relational Model – Other Models – Database Languages – DDL – DML – database Access for applications Programs – data base Users and Administrator – Transaction Management – data base System Structure – Storage Manager – the Query Processor

UNIT II :

History of Data base Systems, Data base design and ER diagrams – Beyond ER Design Entities, Attributes and Entity sets – Relationships and Relationship sets – Additional features of ER Model – Concept Design with the ER Model – Conceptual Design for Large enterprises.

UNIT III :

Introduction to the Relational Model – Integrity Constraint Over relations – Enforcing Integrity constraints – Querying relational data – Logical data base Design – Introduction to Views – Destroying /altering Tables and Views. Relational Algebra – Selection and projection set operations – renaming – Joins – Division – Examples of Algebra overviews – Relational calculus – Tuple relational Calculus – Domain relational calculus – Expressive Power of Algebra and calculus.

UNIT IV :

Form of Basic SQL Query – Examples of Basic SQL Queries – Introduction to Nested Queries – Correlated Nested Queries Set – Comparison Operators – Aggregative Operators – NULL values – Comparison using Null values – Logical connectivity's – AND, OR and NOT – Impact on SQL Constructs – Outer Joins – Disallowing NULL values – Complex Integrity Constraints in SQL Triggers and Active Data bases.

UNIT V :

Schema refinement – Problems Caused by redundancy – Decompositions – Problem related to decomposition – reasoning about FDS – FIRST, SECOND, THIRD Normal forms – BCNF – Lossless join Decomposition – Dependency preserving Decomposition – Schema refinement in Data base Design – Multi valued Dependencies – FORTH Normal Form.

UNIT VI :

Transaction Concept- Transaction State- Implementation of Atomicity and Durability – Concurrent – Executions – Serializability- Recoverability – Implementation of

Isolation – Testing for serializability- Lock –Based Protocols – Timestamp Based Protocols- Validation- Based Protocols – Multiple Granularity.

UNIT VII :

Recovery and Atomicity – Log – Based Recovery – Recovery with Concurrent Transactions – Buffer Management – Failure with loss of nonvolatile storage- Advance Recovery systems- Remote Backup systems.

UNIT VIII :

Data on External Storage – File Organization and Indexing – Cluster Indexes, Primary and Secondary Indexes – Index data Structures – Hash Based Indexing – Tree base Indexing – Comparison of File Organizations – Indexes and Performance Tuning- Intuitions for tree Indexes – Indexed Sequential Access Methods (ISAM) – B+ Trees: A Dynamic Index Structure.

TEXT BOOKS :

1. Data base Management Systems, Raghurama Krishnan, Johannes Gehrke, TATA McGrawHill 3rd Edition
2. Data base System Concepts, Silberschatz, Korth, McGraw hill, V edition.

REFERENCES :

1. Data base Systems design, Implementation, and Management, Peter Rob & Carlos Coronel 7th Edition.
2. Fundamentals of Database Systems, Elmasri Navrate Pearson Education
3. Introduction to Database Systems, C.J.Date Pearson Education

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II Year B. Tech CSSE I-SEM

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**(07A31591) ADVANCED DATA STRUCTURES
AND ALGORITHMS LAB**

Objectives:

- To make the student learn a object oriented way of solving problems.
- To make the student write ADTS for all data structures.
- To make the student learn different algorithm design techniques.

Recommended Systems/Software Requirements:

- Intel based desktop PC with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space
- C++ compiler and STL Recommended

Week1- C++ programs to implement the following using an array.

a) Stack ADT b) Queue ADT

Week2- Write C++ programs to implement the following using a singly linked list.

a) Stack ADT b) Queue ADT

Week3- Write C++ programs to implement the deque (double ended queue) ADT using a doubly linked list and an array.

Week 4- Write a C++ program to perform the following operations:

- a) Insert an element into a binary search tree.
- b) Delete an element from a binary search tree.
- c) Search for a key element in a binary search tree.

Week5- Write C++ programs that use non-recursive functions to traverse the given binary tree in

a) Preorder b) inorder and c) postorder.

Week6- Write C++ programs for the implementation of bfs and dfs for a given graph.

Week7- Write C++ programs for implementing the following sorting methods:

a) Merge sort b) Heap sort

Week8- Write a C++ program to perform the following operations

a) Insertion into a B-tree b) Deletion from a B-tree

Week9- Write a C++ program to perform the following operations

a) Insertion into an AVL-tree b) Deletion from an AVL-tree

Week10- Write a C++ program to implement Kruskal's algorithm to generate a minimum cost spanning tree.

Week11- Write a C++ program to implement Prim's algorithm to generate a minimum cost spanning tree.

Week12- Write a C++ program to implement all the functions of a dictionary (ADT) using hashing.

(Note: Use Class Templates In the above Programs)

TEXT BOOKS :

1. Data Structures and Algorithms in C++, Third Edition, Adam Drozdek, Thomson.
2. Data Structures using C++, D.S. Malik, Thomson

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(07A31592) DATA BASE MANAGEMENT SYSTEMS LAB

Objectives:

- To teach the student database design and query and PL/SQL.

Recommended Systems/Software Requirements:

- Intel based desktop PC
- Mysql /Oracle latest version Recommended

- 1) Creation, altering and dropping of tables and inserting rows into a table (use constraints while creating tables) examples using SELECT command.
- 2) Queries (along with sub Queries) using ANY, ALL, IN, EXISTS, NOT EXISTS, UNION, INTERSET, Constraints.
Example:- Select the roll number and name of the student who secured fourth rank in the class.
- 3) Queries using Aggregate functions (COUNT, SUM, AVG, MAX and MIN), GROUP BY, HAVING and Creation and dropping of Views.
- 4) Queries using Conversion functions (to_char, to_number and to_date), string functions (Concatenation, lpad, rpad, ltrim, rtrim, lower, upper, initcap, length, substr and instr), date functions (Sysdate, next_day, add_months, last_day, months_between, least, greatest, trunc, round, to_char, to_date)
- 5) i) Creation of simple PL/SQL program which includes declaration section, executable section and exception - Handling section (Ex. Student marks can be selected from the table and printed for those who secured first class and an exception can be raised if no records were found)
ii) Insert data into student table and use COMMIT, ROLLBACK and SAVEPOINT in PL/SQL block.
- 6) Develop a program that includes the features NESTED IF, CASE and CASE expression. The program can be extended using the NULLIF and COALESCE functions.
- 7) Program development using WHILE LOOPS, numeric FOR LOOPS, nested loops using ERROR Handling, BUILT -IN Exceptions, USE defined Exceptions, RAISE- APPLICATION ERROR.
- 8) Programs development using creation of procedures, passing parameters IN and OUT of PROCEDURES.
- 9) Program development using creation of stored functions, invoke functions in SQL Statements and write complex functions.

- 10) Program development using creation of package specification, package bodies, private objects, package variables and cursors and calling stored packages.
- 11) Develop programs using features parameters in a CURSOR, FOR UPDATE CURSOR, WHERE CURRENT of clause and CURSOR variables.
- 12) Develop Programs using BEFORE and AFTER Triggers, Row and Statement Triggers and INSTEAD OF Triggers

TEXT BOOKS :

- 1)ORACLE PL/SQL by example. Benjamin Rosenzweig, Elena Silvestrova, Pearson Education 3rd Edition
- 2)ORACLE DATA BASE LOG PL/SQL Programming SCOTT URMAN, Tata McGraw Hill.
- 3)SQL & PL/SQL for Oracle 10g, Black Book, Dr.P.S. Deshpande.

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(07A4EC08) OBJECT ORIENTED PROGRAMMING**UNIT I :**

Object oriented thinking :- Need for oop paradigm, A way of viewing world – Agents, responsibility, messages, methods, classes and instances, class hierarchies (Inheritance), method binding, overriding and exceptions, summary of oop concepts, coping with complexity, abstraction mechanisms.

UNIT II :

Java Basics History of Java, Java buzzwords, datatypes, variables, scope and life time of variables, arrays, operators, expressions, control statements, type conversion and casting, simple java program, classes and objects – concepts of classes, objects, constructors, methods, access control, this keyword, garbage collection, overloading methods and constructors, parameter passing, recursion, string handling.

UNIT III :

Inheritance – Hierarchical abstractions, Base class object, subclass, subtype, substitutability, forms of inheritance- specialization, specification, construction, extension, limitation, combination, benefits of inheritance, costs of inheritance. Member access rules, super uses, using final with inheritance, polymorphism- method overriding, abstract classes.

UNIT IV :

Packages and Interfaces : Defining, Creating and Accessing a Package, Understanding CLASSPATH, importing packages, differences between classes and interfaces, defining an interface, implementing interface, applying interfaces, variables in interface and extending interfaces.
Exploring packages – Java.io, java.util.

UNIT V :

Exception handling and multithreading - Concepts of exception handling, benefits of exception handling, Termination or resumptive models, exception hierarchy, usage of try, catch, throw, throws and finally, built in exceptions, creating own exception sub classes. Differences between multi threading and multitasking, thread life cycle, creating threads, synchronizing threads, daemon threads, thread groups.

UNIT VI :

Event Handling : Events, Event sources, Event classes, Event Listeners, Delegation event model, handling mouse and keyboard events, Adapter classes, inner classes. The AWT class hierarchy, user interface components- labels, button,

canvas, scrollbars, text components, check box, check box groups, choices, lists panels – scrollpane, dialogs, menubar, graphics, layout manager – layout manager types – boarder, grid, flow, card and grib bag.

UNIT VII :

Applets – Concepts of Applets, differences between applets and applications, life cycle of an applet, types of applets, creating applets, passing parameters to applets.

Swing – Introduction, limitations of AWT, MVC architecture, components, containers, exploring swing- JApplet, JFrame and JComponent, Icons and Labels, text fields, buttons – The JButton class, Check boxes, Radio buttons, Combo boxes, Tabbed Panes, Scroll Panes, Trees, and Tables.

UNIT VIII :

Networking – Basics of network programming, addresses, ports, sockets, simple client server program, multiple clients, Java .net package

Packages – java.util,

TEXT BOOKS :

1. Java; the complete reference, 7th editon, Herbert schildt, TMH.
2. Understanding OOP with Java, updated edition, T. Budd, pearson eduction.

REFERENCES :

1. An Introduction to programming and OO design using Java, J.Nino and F.A. Hosch, Jr in wilay & sons.
2. An Introduction to OOP, second edition, T. Budd, pearson education.
3. Introduction to Java programming 6th edition, Y. Daniel Liang, pearson education.
4. An introduction to Java programming and object oriented application development, R.A. Johnson- Thomson.
5. Core Java 2, Vol 1, Fundamentals, Cay.S.Horstmann and Gary Cornell, seventh Edition, Pearson Education.
6. Core Java 2, Vol 2, Advanced Features, Cay.S.Horstmann and Gary Cornell, Seventh Edition, Pearson Education
7. Object Oriented Programming through Java, P. Radha Krishna, University Press.

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(07A4EC13) COMPUTER ORGANIZATION

UNIT I :

BASIC STRUCTURE OF COMPUTERS : Computer Types, Functional unit, Basic OPERATIONAL concepts, Bus structures, Software, Performance, multiprocessors and multi computers. Data Representation. Fixed Point Representation. Floating – Point Representation. Error Detection codes.

UNIT II :

REGISTER TRANSFER LANGUAGE AND MICROOPERATIONS : Register Transfer language. Register Transfer Bus and memory transfers, Arithmetic Microoperatians, logic micro operations, shift micro operations, Arithmetic logic shift unit. Instruction codes. Computer Registers Computer instructions – Instruction cycle.

Memory – Reference Instructions. Input – Output and Interrupt. STACK organization. Instruction formats. Addressing modes. DATA Transfer and manipulation. Program control. Reduced Instruction set computer.

UNIT III :

MICRO PROGRAMMED CONTROL : Control memory, Address sequencing, microprogram example, design of control unit Hard wired control. Microprogrammed control

UNIT IV :

COMPUTER ARITHMETIC : Addition and subtraction, multiplication Algorithms, Division Algorithms, Floating – point Arithmetic operations. Decimal Arithmetic unit Decimal Arithmetic operations.

UNIT V :

THE MEMORY SYSTEM : Basic concepts semiconductor RAM memories. Read-only memories Cache memories performance considerations, Virtual memories secondary storage. Introduction to RAID.

UNIT-VI

INPUT-OUTPUT ORGANIZATION : Peripheral Devices, Input-Output Interface, Asynchronous data transfer Modes of Transfer, Priority Interrupt Direct memory Access, Input –Output Processor (IOP) Serial communication; Introduction to peripheral component, Interconnect (PCI) bus. Introduction to standard serial communication protocols like RS232, USB, IEEE1394.

UNIT VII :

PIPELINE AND VECTOR PROCESSING : Parallel Processing, Pipelining, Arithmetic Pipeline, Instruction Pipeline, RISC Pipeline Vector Processing, Array Processors.

UNIT VIII :

MULTI PROCESSORS : Characteristics of Multiprocessors, Interconnection Structures, Interprocessor Arbitration. InterProcessor Communication and Synchronization Cache Coherence. Shared Memory Multiprocessors.

TEXT BOOKS :

1. Computer Organization – Carl Hamacher, Zvonks Vranesic, SafeaZaky, Vth Edition, McGraw Hill.
2. Computer Systems Architecture – M.Moris Mano, Illrd Edition, Pearson/PHI

REFERENCES :

1. Computer Organization and Architecture – William Stallings Sixth Edition, Pearson/PHI
2. Structured Computer Organization – Andrew S. Tanenbaum, 4th Edition PHI/ Pearson
3. Fundamentals of Computer Organization and Design, - Sivaraama Dandamudi Springer Int. Edition.
4. Computer Architecture a quantitative approach, John L. Hennessy and David A. Patterson, Fourth Edition Elsevier
5. Computer Architecture: Fundamentals and principles of Computer Design, Joseph D. Dumas II, BS Publication.

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(07A40404) DATA COMMUNICATION SYSTEMS**Unit I :**

INTRODUCTION TO DATA COMMUNICATIONS AND NETWORKING: Standards Organizations for Data Communications, Layered Network Architecture, Open Systems Interconnection, Data Communications Circuits, Serial and parallel Data Transmission, Data communications Circuit Arrangements, Data communications Networks, Alternate Protocol Suites.

SIGNALS, NOISE, MODULATION, AND DEMODULATION :

Signal Analysis, Electrical Noise and Signal-to-Noise Ratio, Analog Modulation Systems, Information Capacity, Bits, Bit Rate, Baud, and M-ary Encoding, Digital Modulation.

Unit II :**METALLIC CABLE TRANSMISSION MEDIA :**

Metallic Transmission Lines, Transverse Electromagnetic Waves, Characteristics of Electromagnetic Waves, Transmission Line Classifications, Metallic Transmission Line Types, Metallic Transmission Line Equivalent Circuit, Wave Propagation on Metallic Transmission Lines, Metallic Transmission Line Losses.

OPTICAL FIBER TRANSMISSION MEDIA :

Advantages of Optical Fiber Cables, Disadvantages of Optical Fiber Cables, Electromagnetic spectrum, Optical Fiber Communications System Block Diagram, Optical Fiber construction, The Physics of Light, Velocity of Propagation, Propagation of Light Through an Optical fiber Cable, Optical Fiber Modes and Classifications, Optical Fiber Comparison, Losses in Optical Fiber Cables, Light sources, Light Detectors, Lasers.

Unit III :**DIGITAL TRANSMISSION :**

Pulse Modulation, Pulse code Modulation, Dynamic Range, Signal Voltage –to-Quantization Noise Voltage Ration, Linear Versus Nonlinear PCM Codes, Companding, PCM Line Speed, Delta Modulation PCM and Differential PCM.

MULTIPLEXING AND T CARRIERS :

Time- Division Multiplexing, T1 Digital Carrier System, North American Digital Multiplexing Hierarchy, Digital Line Encoding, T Carrier systems, European Time-Division Multiplexing, Statistical Time – Division Multiplexing, Frame Synchronization, Frequency- Division Multiplexing, Wavelength- Division Multiplexing, Synchronous Optical Network

Unit IV :**WIRELESS COMMUNICATIONS SYSTEMS :**

Electromagnetic Polarization, Rays and Wavefronts, Electromagnetic Radiation, Spherical Wavefront and the Inverse Square Law, wave Attenuation and Absorption, Optical Properties of Radio Waves, Terrestrial Propagation of Electromagnetic Waves, Skip Distance, Free-Space Path Loss, Microwave Communications Systems, Satellite Communications Systems.

Unit V :**TELEPHONE INSTRUMENTS AND SIGNALS:**

The Subscriber Loop, Standard Telephone Set, Basic Telephone Call Procedures, Call Progress Tones and Signals, Cordless Telephones, Caller ID, Electronic Telephones, Paging systems.

THE TELEPHONE CIRCUIT:

The Local Subscriber Loop, Telephone Message- Channel Noise and Noise Weighting, Units of Powers Measurement, Transmission Parameters and Private-Line Circuits, Voice-Frequency Circuit Arrangements, Crosstalk.

Unit VI :**CELLULAR TELEPHONE SYSTEMS:**

First- Generation Analog Cellular Telephone, Personal Communications system, Second-Generation Cellular Telephone Systems, N-AMPS, Digital Cellular Telephone, Interim Standard, North American Cellular and PCS Summary, Global system for Mobile Communications, Personal Communications Satellite System.

Unit VII:**DATA COMMUNICATIONS CODES, ERROR CONTROL, AND DATA FORMATS:**

Data Communications Character Codes, Bar Codes, Error Control, Error Detection, Error Correction, Character Synchronization.

DATA COMMUNICATIONS EQUIPMENT:

Digital Service Unit and Channel Service Unit, Voice- Band Data Communication Modems, Bell Systems- Compatible Voice- Band Modems, Voice- Band Modem Block Diagram, Voice- Band Modem Classifications, Asynchronous Voice-Band Modems, Synchronous Voice-Band Modems, Modem Synchronization, ITU-T Voice- Band Modem Specifications, 56K Modems, Modem Control: The AT Command Set, Cable Modems, Probability of Error and Bit Error Rate.

Unit VIII:**DATA -LINK PROTOCOLS:**

Data -Link Protocol Functions, Character -and Bit- Oriented Protocols, Data Transmission Modes, Asynchronous Data - Link Protocols, Synchronous Data - Link Protocols, Synchronous Data - Link Control, High - Level Data - Link Control.

TEXT BOOKS:

1. Introduction to Data Communications and Networking, Wayne Tomasi, Pearson Education.

Reference Books

1. Data Communications and Networking, Behrouz A Forouzan, Fourth Edition. TMH.
2. Computer Communications and Networking Technologies, Gallow, Second Edition Thomson
3. Computer Networking and Internet, Fred Halsli, Lingana Gouda Kulkarni, Fifth Edition, Pearson Education

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(07A4EC18) MICROPROCESSORS AND INTERFACING

UNIT-I

An over view of 8085, Architecture of 8086 Microprocessor. Special functions of General purpose registers. 8086 flag register and function of 8086 Flags. Addressing modes of 8086. Instruction set of 8086. Assembler directives, simple programs, procedures, and macros.

UNIT-II

Assembly language programs involving logical, Branch & Call instructions, sorting, evaluation of arithmetic expressions, string manipulation.

UNIT-III

Pin diagram of 8086-Minimum mode and maximum mode of operation. Timing diagram. Memory interfacing to 8086 (Static RAM & EPROM). Need for DMA. DMA data transfer Method. Interfacing with 8237/8257.

UNIT-IV

8255 PPI - various modes of operation and interfacing to 8086. Interfacing Keyboard, Displays, 8279 Stepper Motor and actuators. D/A and A/D converter interfacing.

UNIT-V

Interrupt structure of 8086. Vector interrupt table. Interrupt service routines. Introduction to DOS and BIOS interrupts. 8259 PIC Architecture and interfacing cascading of interrupt controller and its importance.

UNIT-VI

Serial data transfer schemes. Asynchronous and Synchronous data transfer schemes. 8251 USART architecture and interfacing. TTL to RS 232C and RS232C to TTL conversion. Sample program of serial data transfer. Introduction to High-speed serial communications standards, USB.

UNIT-VII

Advanced Micro Processors - Introduction to 80286, Salient Features of 80386, Real and Protected Mode Segmentation & Paging, Salient Features of Pentium, Branch Prediction, Overview of RISC Processors.

UNIT-VIII

8051 Microcontroller Architecture, Register set of 8051, Modes of timer operation, Serial port operation, Interrupt structure of 8051, Memory and I/O interfacing of 8051.

TEXT BOOKS :

1. Advanced microprocessor and Peripherals - A.K.Ray and K.M.Bhurchandi, TMH, 2000.
2. Micro Controllers - Deshmukh, Tata McGraw Hill Edition.

REFERENCES :

1. Micro Processors & Interfacing - Douglas U. Hall, 2007.
2. The 8088 and 8086 Micro Processors - PHI, 4th Edition, 2003.
3. Micro Computer System 8086/8088 Family Architecture, Programming and Design - By Liu and GA Gibson, PHI, 2nd Ed.,

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(07A4EC01) ENVIRONMENTAL STUDIES

UNIT - I

Multidisciplinary nature of Environmental Studies: Definition, Scope and Importance – Need for Public Awareness.

UNIT - II

Natural Resources : Renewable and non-renewable resources – Natural resources and associated problems – Forest resources – Use and over – exploitation, deforestation, case studies – Timber extraction – Mining, dams and other effects on forest and tribal people – Water resources – Use and over utilization of surface and ground water – Floods, drought, conflicts over water, dams – benefits and problems – Mineral resources: Use and exploitation, environmental effects of extracting and using mineral resources, case studies. – Food resources: World food problems, changes caused by agriculture and overgrazing, effects of modern agriculture, fertilizer-pesticide problems, water logging, salinity, case studies. – Energy resources: Growing energy needs, renewable and non-renewable energy sources use of alternate energy sources. Case studies. Land resources: Land as a resource, land degradation, man induced landslides, soil erosion and desertification. Role of an individual in conservation of natural resources. Equitable use of resources for sustainable lifestyles.

UNIT - III

Ecosystems : Concept of an ecosystem. - Structure and function of an ecosystem. - Producers, consumers and decomposers. - Energy flow in the ecosystem - Ecological succession. - Food chains, food webs and ecological pyramids. - Introduction, types, characteristic features, structure and function of the following ecosystem:

- Forest ecosystem
- Grassland ecosystem
- Desert ecosystem
- Aquatic ecosystems (ponds, streams, lakes, rivers, oceans, estuaries)

UNIT - IV

Biodiversity and its conservation : Introduction - Definition: genetic, species and ecosystem diversity. - Bio-geographical classification of India - Value of biodiversity: consumptive use, productive use, social, ethical, aesthetic and option values - Biodiversity at global, National and local levels. - India as a megadiversity nation - Hot-spots of biodiversity - Threats to biodiversity: habitat loss, poaching of wildlife, man-wildlife conflicts. - Endangered and endemic species of India - Conservation of biodiversity: In-situ and Ex-situ conservation of biodiversity.

UNIT - V

Environmental Pollution : Definition, Cause, effects and control measures of

- Air pollution
- Water pollution
- Soil pollution
- Marine pollution
- Noise pollution
- Thermal pollution
- Nuclear hazards

Solid waste Management : Causes, effects and control measures of urban and industrial wastes. - Role of an individual in prevention of pollution. - Pollution case studies. - Disaster management: floods, earthquake, cyclone and landslides.

UNIT - VI

Social Issues and the Environment : From Unsustainable to Sustainable development - Urban problems related to energy - Water conservation, rain water harvesting, watershed management - Resettlement and rehabilitation of people; its problems and concerns. Case Studies - Environmental ethics: Issues and possible solutions. - Climate change, global warming, acid rain, ozone layer depletion, nuclear accidents and holocaust. Case Studies. - Wasteland reclamation. - Consumerism and waste products. - Environment Protection Act. - Air (Prevention and Control of Pollution) Act. - Water (Prevention and control of Pollution) Act - Wildlife Protection Act - Forest Conservation Act - Issues involved in enforcement of environmental legislation. - Public awareness.

UNIT - VII

Human Population and the Environment : Population growth, variation among nations. Population explosion - Family Welfare Programme. - Environment and human health. - Human Rights. - Value Education. - HIV/AIDS. - Women and Child Welfare. - Role of information Technology in Environment and human health. - Case Studies.

UNIT - VIII

Field work : Visit to a local area to document environmental assets River /forest grassland/hill/mountain - Visit to a local polluted site - Urban/Rural/industrial/ Agricultural Study of common plants, insects, birds. - Study of simple ecosystems pond, river, hill slopes, etc.

TEXT BOOK:

- Textbook of Environmental Studies for Undergraduate Courses by Erach Bharucha for University Grants Commission.
- Environmental Studies by R. Rajagopalan, Oxford University Press.

REFERENCE:

- Textbook of Environmental Sciences and Technology by M. Anji Reddy, BS Publication.

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(07A4EC12) SOFTWARE ENGINEERING

UNIT-I:

Introduction to Software Engineering: The evolving role of software, Changing Nature of Software, Software myths.

A Generic view of process: Software engineering- A layered technology, a process framework, The Capability Maturity Model Integration (CMMI), Process patterns, process assessment, personal and team process models.

UNIT-II:

Process models: The waterfall model, Incremental process models, Evolutionary process models, The Unified process.

Software Requirements: Functional and non-functional requirements, User requirements, System requirements, Interface specification, the software requirements document.

UNIT-III:

Requirements engineering process: Feasibility studies, Requirements elicitation and analysis, Requirements validation, Requirements management.

System models: Context Models, Behavioral models, Data models, Object models, structured methods.

UNIT-IV:

Design Engineering: Design process and Design quality, Design concepts, the design model.

Creating an architectural design: software architecture, Data design, Architectural styles and patterns, Architectural Design.

UNIT-V:

Object-Oriented Design: Objects and object classes, An Object-Oriented design process, Design evolution.

Performing User interface design: Golden rules, User interface analysis and design, interface analysis, interface design steps, Design evaluation.

UNIT-VI:

Testing Strategies: A strategic approach to software testing, test strategies for conventional software, Black-Box and White-Box testing, Validation testing, System testing, the art of Debugging.

Product metrics: Software Quality, Metrics for Analysis Model, Metrics for Design Model, Metrics for source code, Metrics for testing, Metrics for maintenance.

UNIT-VII:

Metrics for Process and Products: Software Measurement, Metrics for software quality.

Risk management: Reactive vs Proactive Risk strategies, software risks, Risk identification, Risk projection, Risk refinement, RMMM, RMMM Plan.

UNIT-VIII:

Quality Management: Quality concepts, Software quality assurance, Software Reviews, Formal technical reviews, Statistical Software quality Assurance, Software reliability, The ISO 9000 quality standards.

TEXT BOOKS:

1. Software Engineering, A practitioner's Approach- Roger S. Pressman, 6th edition. McGrawHill International Edition.
2. Software Engineering- Sommerville, 7th edition, Pearson education.

REFERENCES:

1. Software Engineering- K.K. Aggarwal & Yogesh Singh, New Age International Publishers
2. Software Engineering, an Engineering approach- James F. Peters, Witold Pedrycz John Wiley
3. Systems Analysis and Design- Shelly Cashman Rosenblatt, 4th edition, Thomson Publications.
4. Software Engineering principles and practice- Waman S Jawadekar, The McGraw-Hill Companies.

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(07A41591) OBJECT ORIENTED PROGRAMMING LAB

Objectives:

- To make the student learn a object oriented way of solving problems.
- To teach the student to write programs in Java to solve the problems

Recommended Systems/Software Requirements:

- Intel based desktop PC with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space
- JDK Kit. Recommended

Week1 :

- a) Write a Java program that prints all real solutions to the quadratic equation $ax^2 + bx + c = 0$. Read in a, b, c and use the quadratic formula. If the discriminant $b^2 - 4ac$ is negative, display a message stating that there are no real solutions.
- b) The Fibonacci sequence is defined by the following rule:
The first two values in the sequence are 1 and 1. Every subsequent value is the sum of the two values preceding it. Write a Java program that uses both recursive and non-recursive functions to print the nth value in the Fibonacci sequence.

Week 2 :

- a) Write a Java program that prompts the user for an integer and then prints out all prime numbers up to that integer.
- b) Write a Java program to multiply two given matrices.
- c) Write a Java Program that reads a line of integers, and then displays each integer, and the sum of all the integers (Use StringTokenizer class of java.util)

Week 3 :

- a) Write a Java program that checks whether a given string is a palindrome or not. Ex: MADAM is a palindrome.
- b) Write a Java program for sorting a given list of names in ascending order.
- c) Write a Java program to make frequency count of words in a given text.

Week 4 :

- a) Write a Java program that reads a file name from the user, then displays information about whether the file exists, whether the file is readable, whether the file is writable, the type of file and the length of the file in bytes.
- b) Write a Java program that reads a file and displays the file on the screen, with a line number before each line.
- c) Write a Java program that displays the number of characters, lines and words in a text file.

Week 5 :

- a) Write a Java program that:
 - i) Implements stack ADT.
 - ii) Converts infix expression into Postfix form
 - iii) Evaluates the postfix expression

Week 6 :

- a) Develop an applet that displays a simple message.
- b) Develop an applet that receives an integer in one text field, and computes its factorial Value and returns it in another text field, when the button named "Compute" is clicked.

Week 7 :

Write a Java program that works as a simple calculator. Use a grid layout to arrange buttons for the digits and for the +, -, *, % operations. Add a text field to display the result.

Week 8 :

- a) Write a Java program for handling mouse events.

Week 9 :

- a) Write a Java program that creates three threads. First thread displays "Good Morning" every one second, the second thread displays "Hello" every two seconds and the third thread displays "Welcome" every three seconds.
- b) Write a Java program that correctly implements producer consumer problem using the concept of inter thread communication.

Week 10 :

Write a program that creates a user interface to perform integer divisions. The user enters two numbers in the textfields, Num1 and Num2. The division of Num1 and Num2 is displayed in the Result field when the Divide button is clicked. If Num1 or Num2 were not an integer, the program would throw a NumberFormatException. If Num2 were Zero, the program would throw an ArithmeticException Display the exception in a message dialog box.

Week 11 :

Write a Java program that implements a simple client/server application. The client sends data to a server. The server receives the data, uses it to produce a result, and then sends the result back to the client. The client displays the result on the console. For ex: The data sent from the client is the radius of a circle, and the result produced by the server is the area of the circle. (Use java.net)

Week 12 :

- a) Write a java program that simulates a traffic light. The program lets the user select one of three lights: red, yellow, or green. When a radio button is selected,

the light is turned on, and only one light can be on at a time No light is on when the program starts.

b) Write a Java program that allows the user to draw lines, rectangles and ovals.

Week 13 :

a) Write a java program to create an abstract class named Shape that contains an empty method named numberOfSides (). Provide three classes named Trapezoid, Triangle and Hexagon such that each one of the classes extends the class Shape. Each one of the classes contains only the method numberOfSides () that shows the number of sides in the given geometrical figures.

b) Suppose that a table named Table.txt is stored in a text file. The first line in the file is the header, and the remaining lines correspond to rows in the table. The elements are separated by commas. Write a java program to display the table using JTable component.

TEXT BOOKS :

1. Java How to Program, Sixth Edition, H.M.Dietel and P.J.Dietel, Pearson Education/PHI
2. Introduction to Java programming, Sixth edition, Y.Daniel Liang, Pearson Education
3. Big Java, 2nd edition, Cay Horstmann, Wiley Student Edition, Wiley India Private Limited.

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(07A41592) MICROPROCESSORS AND INTERFACING LAB

I. Microprocessor 8086 :

1. Introduction to MASM/TASM.
2. Arithmetic operation – Multi byte Addition and Subtraction, Multiplication and Division – Signed and unsigned Arithmetic operation, ASCII – arithmetic operation.
3. Logic operations – Shift and rotate – Converting packed BCD to unpacked BCD, BCD to ASCII conversion.
4. By using string operation and Instruction prefix: Move Block, Reverse string, Sorting, Inserting, Deleting, Length of the string, String comparison.
5. DOS/BIOS programming: Reading keyboard (Buffered with and without echo) – Display characters, Strings.

II. Interfacing :

1. 8259 – Interrupt Controller : Generate an interrupt using 8259 timer.
2. 8279 – Keyboard Display : Write a small program to display a string of characters.
3. 8255 – PPI : Write ALP to generate sinusoidal wave using PPI.
4. 8251 – USART : Write a program in ALP to establish Communication between two processors.

III. Microcontroller 8051

1. Reading and Writing on a parallel port.
2. Timer in different modes.
3. Serial communication implementation.

Equipment required for Laboratories:

1. 8086 μ P Kits
2. 8051 Micro Controller kits
3. Interfaces/peripheral subsystems
 - i) 8259 PIC
 - ii) 8279-KB/Display
 - iii) 8255 PPI
 - iv) 8251 USART
4. ADC Interface
5. DAC Interface
6. Traffic Controller Interface
7. Elevator Interface

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III Year B. Tech CSSE I-SEM

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(07A50207) OPTIMIZATION TECHNIQUES

UNIT – I Introduction and Classical Optimization Techniques:

Statement of an Optimization problem – design vector – design constraints – constraint surface – objective function – objective function surfaces – classification of Optimization problems.

UNIT – II Classical Optimization Techniques

Single variable Optimization – multi variable Optimization without constraints – necessary and sufficient conditions for minimum/maximum – multivariable Optimization with equality constraints.

Solution by method of Lagrange multipliers – multivariable Optimization with inequality constraints – Kuhn – Tucker conditions.

UNIT – III Linear Programming

Standard form of a linear programming problem – geometry of linear programming problems – definitions and theorems – solution of a system of linear simultaneous equations – pivotal reduction of a general system of equations – motivation to the simplex method – simplex algorithm.

UNIT – IV Transportation Problem

Finding initial basic feasible solution by north – west corner rule, least cost method and Vogel's approximation method – testing for optimality of balanced transportation problems.

UNIT – V Unconstrained Nonlinear Programming:

One – dimensional minimization methods: Classification, Fibonacci method and Quadratic interpolation method

UNIT – VI Unconstrained Optimization Techniques

Univariate method, Powell's method and steepest descent method.

UNIT – VII Constrained Nonlinear Programming:

Characteristics of a constrained problem, Classification, Basic approach of Penalty Function method; Basic approaches of Interior and Exterior penalty function methods. Introduction to convex Programming Problem.

UNIT – VIII Dynamic Programming:

Dynamic programming multistage decision processes – types – concept of sub optimization and the principle of optimality – computational procedure in dynamic programming – examples illustrating the calculus method of solution - examples illustrating the tabular method of solution.

TEXT BOOKS:

1. "Engineering optimization: Theory and practice"-by S. S.Rao, New Age International (P) Limited, 3rd edition, 1998.
2. "Introductory Operations Research" by H.S. Kasene & K.D. Kumar, Springer(India), Pvt .Ltd.

REFERENCE BOOKS:

1. "Optimization Methods in Operations Research and systems Analysis" – by K.V. Mital and C. Mohan, New Age International (P) Limited, Publishers, 3rd edition, 1996.
2. Operations Research – by Dr. S.D.Sharma.
3. "Operations Research : An Introduction" – by H.A. Taha, PHI Pvt. Ltd., 6th edition
4. Linear Programming – by G. Hadley

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III Year B. Tech CSSE I-SEM

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(07A5EC20) AUTOMATA AND COMPILER DESIGN

UNIT-I:

Formal Language and Regular Expressions: Languages, Definition Languages regular expressions, Finite Automata – DFA, NFA. Conversion of regular expression to NFA, NFA to DFA. Applications of Finite Automata to lexical analysis, lex tools.

UNIT-II:

Context Free grammars and parsing: Context free grammars, derivation, parse trees, ambiguity LL(K) grammars and LL(1) parsing

UNIT-III:

Bottom up parsing handle pruning LR Grammar Parsing, LALR parsing, parsing ambiguous grammars, YACC programming specification.

UNIT-IV:

Semantics: Syntax directed translation, S-attributed and L-attributed grammars, Intermediate code – abstract syntax tree, translation of simple statements and control flow statements.

UNIT-V:

Context Sensitive features – Chomsky hierarchy of languages and recognizers. Type checking, type conversions, equivalence of type expressions, overloading of functions and operations.

UNIT-VI:

Run time storage: Storage organization, storage allocation strategies scope access to now local names, parameters, language facilities for dynamics storage allocation.

UNIT-VII:

Code optimization: Principal sources of optimization, optimization of basic blocks, peephole optimization, flow graphs, Data flow analysis of flow graphs.

UNIT-VIII:

Code generation: Machine dependent code generation, object code forms, generic code generation algorithm, Register allocation and assignment. Using DAG representation of Block.

TEXT BOOKS :

1. Introduction to Theory of computation. Sipser, 2nd Edition, Thomson.
2. Compilers Principles, Techniques and Tools Aho, Ullman, Raviseti, Pearson Education.

REFERENCES :

1. Modern Compiler Construction in C , Andrew W.Appel Cambridge University Press.
2. Compiler Construction, LOUDEN, Thomson.

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III Year B. Tech CSSE I-SEM

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(07A50504) UNIX PROGRAMMING

UNIT-I:

Unix utilities –1: Introduction to unix file system, vi editor, file handling utilities, security by file permissions process utilities, disk utilities, networking commands,

cp, mv, ln, rm, unlink, mkdir, rmdir, du, df, mount, umount, find, unmask, ulimit, ps, who, w, finger, arp, ftp, telnet, rlogin.

UNIT-II:

Unix utilities –2: Text processing utilities and backup utilities , detailed commands to be covered are cat, tail, head , sort, nl, uniq, grep, egrep, fgrep, cut, paste, join, tee, pg, comm, cmp, diff, tr, awk, tar, cpio.

UNIT-III:

Problem solving approaches in Unix: Using single commands, using compound Commands, shell scripts, C programs, building own command library of programs.

UNIT-IV:

Working with the Bourne shell: what is a shell, shell responsibilities, pipes and input Redirection, output redirection, here documents, the shell as a programming language, shell meta characters, shell variables, shell commands, the environment, control structures, shell script examples.

UNIT-V:

Unix Internals-1 : Unix file structure, directories, files and devices, System calls, library functions, low level file access, usage of open, creat, read, write, close, lseek, stat, fstat, ioctl, umask, dup and dup2, the standard i/o (fopen, fopen, fclose, fflush, fseek, fgetc, getc, getchar, fputc, putc, putchar, fgets, gets), formatted I/O, stream errors, streams and file descriptors, file and directory maintenance (chmod, chown, unlink, link, symlink, mkdir, rmdir, chdir, getcwd), Directory handling system calls (opendir, readdir, closedir, rewinddir, seekdir, telldir)

UNIT-VI:

Unix Internals –2: Process and Signals: What is process, process structure, starting new process, Waiting for a process, zombie process, process control, process identifiers, fork, Vfork, exit, wait, exec, Signal functions, unreliable signals, interrupted system Calls, kill and raise functions, alarm, pause functions, abort, system, sleep functions.

UNIT-VII:

Unix Internals –3:Data Management: Management of memory (malloc, free, realloc, calloc), File Locking (creating lock files, Locking regions, use of read/write locking, competing locks, other commands, deadlocks)

UNIT-VIII:

Unix Internals – 4:Inter-Process communication: Pipe, Process Pipes, the pipe call, parent-child process, named pipes: FIFOs, Semaphores, message queues and shared memory and applications of IPC.

TEXT BOOKS:

1. Unix the ultimate guide, Sumitabha Das, TMH.
2. Unix Network Programming, W.R.Stevens Pearson/PHI.

REFERENCES:

1. Advanced programming in the Unix environment, W.R.Stevens, Pearson education.
2. Unix system programming using C++, T.Chan, PHI.
3. Unix programming environment, Kernighan and Pike, PHI. / Pearson Education
4. Unix Internals The New Frontiers, U.Vahalia, Pearson Education.
5. Unix for programmers and users, 3rd edition, Graham Glass, King Ables, Pearson Education

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(07A51501) SYSTEMS PROGRAMMING

UNIT I

INTRODUCTION TO PC ARCHITECTURE (Intel Pentium) PC Hardware, Segments and Addressing, Registers. ASSEMBLY LANGUAGE BASICS Machine Addressing, Special DEBUG features, Data Definition Directives, Addressing Formats, COM Programs.

UNIT II

PROGRAM LOGIC AND CONTROL JMP, LOOP and Conditional Jump Instructions, Boolean operations, Shifting, Rotating.

UNIT III

KEYBOARD AND SCREEN PROCESSING String Operations, Arithmetic Operations and Table Processing, Searching, Sorting.

UNIT IV

MACRO WORKING AND LINKING, Macro definition, The LOCAL Directive, Reception Directives, Conditional Directives, Intra-segment and Inter-segment Calls, Passing Parameters.

UNIT V

ADVANCED SCREEN AND KEYBOARD PROCESSING, BIOS Interrupt 10H for graphics and text, DOS Interrupt 21H and BIOS Interrupt 16H for Keyboard input, Extended Function Keys.

UNIT VI

DISK PROCESSING Disk Organization, File Allocation Table, File Control Block, Sequential Reading of a Disk File, Random Processing, Miscellaneous Disk Processing Features, File Handlers and Extended DOS functions,

UNIT VII

BIOS Disk Operations. DOS MEMORY MANAGEMENT Program Segment Prefix, DOS Memory Control, Program Loader, Program Overlays, Resident Programs.

UNIT VIII

ASSEMBLERS AND MACROPROCESSOR, Design of Assembler, Data, Structure, format of Databases, Algorithm, Macro instructions, Features of a Macro facility, A two-pass algorithm and a single pass algorithm. LOADERS, Compile-and-go Loaders, General Loader Schemes, Absolute, Relocating and Direct-linking loaders.

TEXT BOOKS

1. Peter Abel, IBM PC Assembly Language and Programming, Second Edition, PHI. (Chapters I to 18, 20 to 22).
2. John J Donovan, Systems Programming, McGraw Hill.(Chapters 3,4,and 5).

REFERENCE:

- 1) Operating Systems and Systems Programming, Dhamdhare, PHI.

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(07A5EC11) COMPUTER GRAPHICS

UNIT-I:

Introduction, Application areas of Computer Graphics, overview of graphics systems, video-display devices, raster-scan systems, random scan systems, graphics monitors and work stations and input devices (p.nos 22-90 of text book-1).

UNIT-II:

Output primitives: Points and lines, line drawing algorithms, mid-point circle and ellipse algorithms. **Filled area primitives:** Scan line polygon fill algorithm, boundaryfill and flood-fill algorithms (p.nos 103-123, 137-145, 147-150, 164-171 of text book-1, p.nos. 72-99 of text book-2).

UNIT-III:

2-D geometrical transforms: Translation, scaling, rotation, reflection and shear transformations, matrix representations and homogeneous coordinates, composite transforms, transformations between coordinate systems (p.nos 204-227 of text book-1).

UNIT-IV:

2-D viewing : The viewing pipeline, viewing coordinate reference frame, window to view-port coordinate transformation, viewing functions, Cohen-Sutherland and Cyrus-beck line clipping algorithms, Sutherland -Hodgeman polygon clipping algorithm (p.nos 237-249, 257-261 of text book -1, p.nos. 111-126 of text book-2).

UNIT-V:

3-D object representation : Polygon surfaces, quadric surfaces, spline representation, Hermite curve, Bezier curve and B-Spline curves, Bezier and BSpline surfaces. Basic illumination models, polygon rendering methods. (p.nos 324-331, 340-342, 347-364, 516-531, 542-546 of text book-1, p.nos 473-529, 721-739 of text book-2).

UNIT-VI:

3-D Geometric transformations: Translation, rotation, scaling, reflection and shear transformations, composite transformations. **3-D viewing :** Viewing pipeline, viewing coordinates, view volume and general projection transforms and clipping (p.nos 427-443, 452-481 of text book -1).

UNIT-VII:

Visible surface detection methods : Classification, back-face detection, depthbuffer, scan-line, depth sorting, BSP-tree methods, area sub-division and octree methods (p.nos 489-505 of text book -1, Chapter 15 of of text book-2).

UNIT-VIII:

Computer animation: Design of animation sequence, general computer animation functions, raster animation, computer animation languages, key frame systems, motion specifications. (p.nos 604-616 of text book -1, chapter 21 of text book-2)

TEXT BOOKS:

1. "Computer Graphics *C version*", Donald Hearn and M.Pauline Baker, Pearson Education
2. "Computer Graphics Principles & practice", second edition in C, Foley, VanDam, Feiner and Hughes, Pearson Education.

REFERENCES:

1. "Computer Graphics", second Edition, Donald Hearn and M.Pauline Baker, PHI/Pearson Education.
2. "Computer Graphics Second edition", Zhigang xiang, Roy Plastrock, Schaum's outlines, Tata Mc-Graw hill edition.
3. Procedural elements for Computer Graphics, David F Rogers, Tata Mc Graw hill, 2nd edition.
4. "Principles of Interactive Computer Graphics", Neuman and Sproul, TMH.
5. Principles of Computer Graphics, Shalini Govil, Pai, 2005, Springer.
6. Computer Graphics, Steven Harrington, TMH

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(07A5EC21) OPERATING SYSTEMS

UNIT I :

Computer System and Operating System Overview: Overview of computer operating systems operating systems functions protection and security distributed systems special purpose systems operating systems structures and systems calls operating systems generation

UNIT II :

Process Management – Process concepts threads, scheduling-criteria algorithms, their evaluation,
Thread scheduling, case studies UNIX, Linux, Windows

UNIT III :

Concurrency : Process synchronization, the critical- section problem, Peterson's Solution, synchronization Hardware, semaphores, classic problems of synchronization, monitors, Synchronization examples, atomic transactions. Case studies UNIX, Linux, Windows

UNIT IV :

Memory Management : Swapping, contiguous memory allocation, paging, structure of the page table , segmentation, virtual memory, demand paging, page- Replacement, algorithms, case studies UNIX, Linux, Windows

UNIT V :

Principles of deadlock – system model, deadlock characterization, deadlock prevention, detection and avoidance, recovery from deadlock,
I/O systems, Hardware, application interface, kernel I/O subsystem, Transforming I/O requests Hardware operation, STREAMS, performance.

UNIT VI :

File system Interface- the concept of a file, Access Methods, Directory structure, File system mounting, file sharing, protection.

File System implementation- File system structure, file system implementation, directory implementation, directory implementation, allocation methods, free-space management, efficiency and performance, case studies. UNIX, Linux, Windows

UNIT VII :

Mass-storage structure overview of Mass-storage structure, Disk structure, disk attachment disk scheduling, swap-space management, RAID structure, stable-storage implementation, Tertiary storage structure.

UNIT VIII :

Protection : Protection, Goals of Protection, Principles of Protection, Domain of protection Access Matrix, Implementation of Access Matrix, Access control, Revocation of Access Rights, Capability- Based systems, Language – Based Protection,

Security- The Security problem, program threats, system and network threats cryptography as a security tool, user authentication, implementing security defenses, firewalling to protect systems and networks, computer –security classifications, case studies UNIX, Linux, Windows

TEXT BOOKS :

1. Operating System Concepts- Abraham Silberchatz, Peter B. Galvin, Greg Gagne 7th Edition, John Wiley.
2. Operating systems- A Concept based Approach-D.M.Dhamdhare, 2nd Edition, TMH

REFERENCES :

1. Operating Systems' – Internal and Design Principles Stallings, Fifth Edition–2005, Pearson education/PHI
2. Operating System A Design Approach-Crowley, TMH.
3. Modern Operating Systems, Andrew S Tanenbaum 2nd edition Pearson/PHI.

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(07A51591) ADVANCED ENGLISH COMMUNICATION SKILLS LAB

1. Introduction

The introduction of the English Language Lab is considered essential at 3rd year level. At this stage the students need to prepare themselves for their careers which may require them to listen to, read, speak and write in English both for their professional and interpersonal communication in the globalised context.

The proposed course should be an integrated theory and lab course to enable students to use 'good' English and perform the following:

- Gather ideas and information, to organise ideas relevantly and coherently.
- Engage in debates.
- Participate in group discussions.
- Face interviews.
- Write project/research reports/technical reports.
- Make oral presentations.
- Write formal letters.
- Transfer information from non-verbal to verbal texts and vice versa.
- To take part in social and professional communication.

2. Objectives:

This Lab focuses on using computer-aided multimedia instruction for language development to meet the following targets:

- To improve the students' fluency in English, through a well-developed vocabulary and enable them to listen to English spoken at normal conversational speed by educated English speakers and respond appropriately in different socio-cultural and professional contexts.
- Further, they would be required to communicate their ideas relevantly and coherently in writing.

3. Syllabus:

The following course content is prescribed for the Advanced Communication Skills Lab:

- Functional English - starting a conversation – responding appropriately and relevantly – using the right body language – role play in different situations.
- Vocabulary building – synonyms and antonyms, word roots, one-word substitutes, prefixes and suffixes, study of word origin, analogy, idioms and phrases.
- Group Discussion – dynamics of group discussion, intervention, summarizing, modulation of voice, body language, relevance, fluency and coherence.

- Interview Skills – concept and process, pre-interview planning, opening strategies, answering strategies, interview through tele and video-conferencing.
- Resume' writing – structure and presentation, planning, defining the career objective, projecting ones strengths and skill-sets, summary, formats and styles, letter-writing.
- Reading comprehension – reading for facts, guessing meanings from context, scanning, skimming, inferring meaning, critical reading.
- Technical Report writing – Types of formats and styles, subject matter – organization, clarity, coherence and style, planning, data-collection, tools, analysis.

4. Minimum Requirement:

The English Language Lab shall have two parts:

- i) **The Computer aided Language Lab** for 60 students with 60 systems, one master console, LAN facility and English language software for self- study by learners.
- ii) **The Communication Skills Lab** with movable chairs and audio-visual aids with a P.A System, a T. V., a digital stereo –audio & video system and camcorder etc.

System Requirement (Hardware component):

Computer network with Lan with minimum 60 multimedia systems with the following specifications:

- iii) P – IV Processor
 - a) Speed – 2.8 GHZ
 - b) RAM – 512 MB Minimum
 - c) Hard Disk – 80 GB
- iv) Headphones of High quality

5. Suggested Software:

The software consisting of the prescribed topics elaborated above should be procured and used.

Suggested Software:

- **Clarity Pronunciation Power** – part II
- **Oxford Advanced Learner's Compass**, 7th Edition
- **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice.**
- **Lingua TOEFL CBT Insider**, by Dreamtech
- **TOEFL & GRE(KAPLAN, AARCO & BARRONS, USA, Cracking GRE by CLIFFS)**
- **The following software from 'train2success.com'**
 - **Preparing for being Interviewed,**
 - **Positive Thinking,**
 - **Interviewing Skills,**
 - **Telephone Skills,**
 - **Time Management**
 - **Team Building,**
 - **Decision making**

- **English in Mind**, Herbert Puchta and Jeff Stranks with Meredith Levy, Cambridge

6. Books Recommended:

1. **Effective Technical Communication**, M. Ashraf Rizvi, Tata Mc. Graw-Hill Publishing Company Ltd.
2. **A Course in English communication** by Madhavi Apte, Prentice-Hall of India, 2007.
3. **Communication Skills** by Leena Sen, Prentice-Hall of India, 2005.
4. **Academic Writing- A Practical guide for students** by Stephen Bailey, Rontledge Falmer, London & New York, 2004.
5. **English Language Communication : A Reader cum Lab Manual** Dr A Ramakrishna Rao, Dr G Natanam & Prof SA Sankaranarayanan, Anuradha Publications, Chennai
6. **Body Language- Your Success Mantra** by Dr. Shalini Verma, S. Chand, 2006.
7. **DELTA's key to the Next Generation TOEFL Test: Advanced Skill Practice**, New Age International (P) Ltd., Publishers, New Delhi.
8. Books on **TOEFL/GRE/GMAT/CAT** by Barron's/cup
9. **IELTS series with CDs** by Cambridge University Press.
10. **Technical Report Writing Today** by Daniel G. Riordan & Steven E. Pauley, Biztantra Publishers, 2005.
11. **Basic Communication Skills for Technology** by Andra J. Rutherford, 2nd Edition, Pearson Education, 2007.
12. **Communication Skills for Engineers** by Sunita Mishra & C. Muralikrishna, Pearson Education, 2007.
13. **Objective English** by Edgar Thorpe & Showick Thorpe, 2nd edition, Pearson Education, 2007.
14. **Cambridge Preparation for the TOEFL Test** by Jolene Gear & Robert Gear, 4th Edition.
15. **Technical Communication** by Meenakshi Raman & Sangeeta Sharma, Oxford University Press.

DISTRIBUTION AND WEIGHTAGE OF MARKS:

Advanced Communication Skills Lab Practicals:

1. The practical examinations for the English Language Laboratory practice shall be conducted as per the University norms prescribed for the core engineering practical sessions.
2. For the English Language lab sessions, there shall be a continuous evaluation during the year for 25 sessional marks and 50 End Examination marks. Of the 25 marks, 15 marks shall be awarded for day-to-day work and 10 marks to be awarded by conducting Internal Lab Test(s). The End Examination shall be conducted by the teacher concerned with the help of another member of the staff of the same department of the same institution.

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(07A51592) UNIX PROGRAMMING AND COMPILER DESIGN LAB

Objectives:

- To teach students various unix utilities and shell scripting
- To provide an understanding of the language translation peculiarities by designing a complete translator for a mini language.

Recommended Systems/Software Requirements:

- Intel based desktop PC with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space LAN Connected
- Any flavour of Unix / Linux
- C++ compiler and JDK kit

PART A

2. Write a shell script that copies multiple files to a directory.
3. Write a shell script which counts the number of lines and words present in a given file.
4. Write a shell script which displays the list of all files in the given directory.
5. Write a shell script (small calculator) that adds, subtracts, multiplies and divides the given two integers. There are two division options: one returns the quotient and the other returns remainder. The script requires 3 arguments: The operation to be used and two integer numbers. The options are add(-a), subtract(-s), multiply(-m), quotient(-c) and remainder(-r).
6. Write a shell script to reverse the rows and columns of a matrix.
7. Write a C program that counts the number of blanks in a text file.
 - a) using standard I/O
 - b) using system calls.
8. Implement in C the following Unix commands using system calls.
 - a) cat
 - b) ls
 - c) mv
9. Write a program that takes one or more file/directory names as command line input and reports the following information on the file:
 - c) File type.
 - d) Number of links.
 - e) Time of last access.
 - f) Read, Write and Execute permissions.
10. Write a C program that illustrates how to execute two commands concurrently with a command pipe.
11. Write a C program that illustrates the creation of child process using fork system call.
12. Write a C program that displays the real time of a day every 60 seconds.
13. Write a C program that illustrates file locking using semaphores.
14. Write a C program that implements a producer-consumer system with two processes. (using semaphores)
15. Write a C program that illustrates inter process communication using shared memory system calls.

16. Write a C program that illustrates the following.
 g) Creating a message queue.
 h) Writing to a message queue.
 i) Reading from a message queue.

PART-B

Objective: To provide an understanding of the language translation peculiarities by designing a complete translator for a mini language. Consider the following mini Language, a simple procedural high-level language, only operating on integer data, with a syntax looking vaguely like a simple C crossed with Pascal. The syntax of the language is defined by the following BNF grammar:

```
<program> ::= <block>
<block> ::= { <variabledefinition> <slst> }
| { <slst> }
<variabledefinition> ::= int <vardeflist> ;
<vardeflist> ::= <vardec> | <vardec> , <vardeflist>
<vardec> ::= <identifier> | <identifier> [ <constant> ]
<slst> ::= <statement> | <statement> ; <slst>
<statement> ::= <assignment> | <ifstatement> | <whilestatement>
| <block> | <printstatement> | <empty>
<assignment> ::= <identifier> = <expression>
| <identifier> [ <expression> ] = <expression>
<ifstatement> ::= if <bexpression> then <slst> else <slst> endif
| if <bexpression> then <slst> endif
<whilestatement> ::= while <bexpression> do <slst> enddo
<printstatement> ::= print ( <expression> )
<expression> ::= <expression> <addingop> <term> | <term> | <addingop> <term>
<bexpression> ::= <expression> <relop> <expression>
<relop> ::= < | <= | == | >= | > | !=
<addingop> ::= + | -
<term> ::= <term> <multop> <factor> | <factor>
<multop> ::= * | /
<factor> ::= <constant> | <identifier> | <identifier> [ <expression> ]
| ( <expression> )
<constant> ::= <digit> | <digit> <constant>
<identifier> ::= <identifier> <letterordigit> | <letter>
<letterordigit> ::= <letter> | <digit>
<letter> ::= a|b|c|d|e|f|g|h|i|j|k|l|m|n|o|p|q|r|s|t|u|v|w|x|y|z
<digit> ::= 0|1|2|3|4|5|6|7|8|9
<empty> has the obvious meaning
```

Comments (zero or more characters enclosed between the standard C/Java-style comment

brackets _____) can be inserted. The language has rudimentary support for 1-dimensional arrays. The declaration _____ declares an array of three elements, referenced

as _____ and _____. Note also that you should worry about the scoping of names.

A simple program written in this language is:

```
{ int a[3],t1,t2;
  t1=2;
```

```
a[0]=1; a[1]=2; a[t1]=3;
t2=-(a[2]+t1*6)/(a[2]-t1);
if t2>5 then
  print(t2);
else {
  int t3;
  t3=99;
  t2=-25;
  print(-t1+t2*t3); /* this is a comment
on 2 lines */
}
endif
}
```

1. Design a Lexical analyzer for the above language. The lexical analyzer should ignore redundant spaces, tabs and newlines. It should also ignore comments. Although the syntax specification states that identifiers can be arbitrarily long, you may restrict the length to some reasonable value.
2. Implement the lexical analyzer using JLex, flex or lex or other lexical analyzer generating tools.
3. Design Predictive parser for the given language
4. Design LALR bottom up parser for the above language.
5. Convert the BNF rules into Yacc form and write code to generate abstract syntax tree.
6. Write program to generate machine code from the abstract syntax tree generated by the parser. The following instruction set may be considered as target code. The following is a simple register-based machine, supporting a total of 17 instructions. It has three distinct internal storage areas. The first is the set of 8 registers, used by the individual instructions as detailed below, the second is an area used for the storage of variables and the third is an area used for the storage of program. The instructions can be preceded by a label. This consists of an integer in the range 1 to 9999 and the label is followed by a colon to separate it from the rest of the instruction. The numerical label can be used as the argument to a jump instruction, as detailed below. In the description of the individual instructions below, instruction argument types are specified as follows:

R

specifies a register in the form R0, R1, R2, R3, R4, R5, R6 or R7 (or r0, r1, etc.).

L

specifies a numerical label (in the range 1 to 9999).

V

specifies a "variable location" (a variable number, or a variable location pointed to by a register - see below).

A

specifies a constant value, a variable location, a register or a variable location pointed to by a register (an indirect address). Constant values are specified as an integer value, optionally preceded by a minus sign, preceded by a _ symbol. An indirect address is specified by an _ followed by a register. So, for example, an A-type argument could have the form 4 (variable number 4), #4 (the constant value 4), r4 (register 4) or @r4 (the contents of

register 4 identifies the variable location to be accessed).

The instruction set is defined as follows:

LOAD A,R

loads the integer value specified by _ into register _.

STORE R,V

stores the value in register _ to variable _.

OUT R

outputs the value in register _.

NEG R

negates the value in register _.

ADD A,R

adds the value specified by _ to register _, leaving the result in register _.

SUB A,R

subtracts the value specified by _ from register _, leaving the result in register _.

MUL A,R

multiplies the value specified by _ by register _, leaving the result in register _.

DIV A,R

divides register _ by the value specified by _, leaving the result in register _.

JMP L

causes an unconditional jump to the instruction with the label _.

JEQ R,L

jumps to the instruction with the label _ if the value in register _ is zero.

JNE R,L

jumps to the instruction with the label _ if the value in register _ is not zero.

JGE R,L

jumps to the instruction with the label _ if the value in register _ is greater than or equal to zero.

JGT R,L

jumps to the instruction with the label _ if the value in register _ is greater than zero.

JLE R,L

jumps to the instruction with the label _ if the value in register _ is less than or equal to zero.

JLT R,L

jumps to the instruction with the label _ if the value in register _ is less than zero.

NOP

is an instruction with no effect. It can be tagged by a label.

STOP

stops execution of the machine. All programs should terminate by executing a ____ instruction.

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(07A6EC08) COMPUTER NETWORKS

UNIT -I:

Introduction: OSI, TCP/IP and other networks models, Examples of Networks: Novell Networks, Arpanet, Internet, Network Topologies WAN, LAN, MAN.

UNIT-II:

Physical Layer: Transmission media copper, twisted pair wireless, switching and encoding asynchronous communications; Narrow band, broad band ISDN and ATM.

UNIT-III:

Data link layer: Design issues, framing, error detection and correction, CRC, Elementary Protocol-stop and wait, Sliding Window, Slip, Data link layer in HDLC, Internet, ATM.

UNIT-IV:

Medium Access sub layer: ALOHA, MAC addresses, Carrier sense multiple access. IEEE 802.X Standard Ethernet, wireless LANS. Bridges,

UNIT-V:

Network Layer: Virtual circuit and Datagram subnets-Routing algorithm shortest path routing, Flooding, Hierarchical routing, Broad cast, Multi cast, distance vector routing.

UNIT -VI:

Dynamic routing – Broadcast routing. Rotary for mobility. Congestion, Control Algorithms – General Principles – of Congestion prevention policies. Internet working: The Network layer in the internet and in the ATM Networks.

UNIT -VII:

Transport Layer: Transport Services, Connection management, TCP and UDP protocols; ATM AAL Layer Protocol.

UNIT -VIII

Application Layer – Network Security, Domain name system, SNMP, Electronic Mail; the World WEB, Multi Media.

TEXT BOOKS:

1. Computer Networks — Andrew S Tanenbaum, 4th Edition. Pearson Education/ PHI
2. Data Communications and Networking – Behrouz A. Forouzan. Third Edition TMH.

REFERENCES:

1. An Engineering Approach to Computer Networks-S.Keshav, 2nd Edition, Pearson Education
2. Understanding communications and Networks, 3rd Edition, W.A. Shay, Thomson

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(07A60507) MATHEMATICAL MODELING AND SIMULATION

UNIT I

Art of Modeling, Types of models, mathematical models – solution methods – analytical, Numerical and Heuristic. L.P.P. – Formulation – Graphical Method, simplex method, dual simplex method and application.

UNIT II

Transportation models – Assignment models, Integer programming, Non-linear programming.

UNIT III

Deterministic Inventory models – General Inventory model, Static E.O.Q. Models, Dynamic Inventory model, Probabilistic Inventory models, continuous Review models, single period model and multiple period model.

UNIT IV

Selective Inventory control – ABC, VED, FSN Analysis. Inventory systems – Fixed order quantity system, two bin system, periodic review systems, Optional Replenishment system and M R P.

UNIT V

Queuing Theory – Basic Structure of Queuing Models, Role of Exponential Distribution, Birth-and-Death Process, Queuing Models Based on the Birth-and-Death Process, Queuing Models involving Non-exponential Distributions, Priority-Discipline Queuing Models and Queuing Networks. Applications of Queuing Theory – Decision Making, Formulation of Waiting – Cost Function and Decision Models.

UNIT VI

CPM and PERT – Network Representation, Critical path calculation, construction of Time schedule.

UNIT VII

Simulation – Introduction, General principles, Random-Number Generation, Random-Variate Generation, Simulation Software.

UNIT VIII

Input modeling, verification and validation of simulation models, Output Analysis for a single model, Comparison and Evaluation of Alternative System Designs, Simulation of Computer Systems.

TEXT BOOKS:

1. Introduction to Operations Research, Frederick S Hiller and Gerald J Liberman, 7th edition, TMH.
2. Discrete Event System Simulation, 3rd edition, Jerry Banks, John S Carson II, Barry L. Nelson and David M. Nicol. Pearson Education/PHI.

REFERENCES:

1. Operations Research- Natarajan, Pearson.
2. Operations Research: Applications and Algorithms, Winston, Thomson.
3. Operations Research – An Introduction, TAHA, 6th edition, PHI/Pearson.

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(07A6EC09) OBJECT ORIENTED ANALYSIS AND DESIGN

UNIT-I:

Introduction to UML: Importance of modeling, principles of modeling, object oriented modeling, conceptual model of the UML, Architecture, Software Development Life Cycle.

UNIT-II:

Basic Structural Modeling: Classes, Relationships, common Mechanisms, and diagrams.

Advanced Structural Modeling: Advanced classes, advanced relationships, Interfaces, Types and Roles, Packages.

UNIT-III

Class & Object Diagrams: Terms, concepts, modeling techniques for Class & Object Diagrams.

UNIT- IV:

Basic Behavioral Modeling-I: Interactions, Interaction diagrams.

UNIT-V:

Basic Behavioral Modeling-II: Use cases, Use case Diagrams, Activity Diagrams.

UNIT-VI:

Advanced Behavioral Modeling: Events and signals, state machines, processes and Threads, time and space, state chart diagrams.

UNIT-VII:

Architectural Modeling: Component, Deployment, Component diagrams and Deployment diagrams.

UNIT-VIII:

Case Study: The Unified Library application

TEXT BOOKS:

1. Grady Booch, James Rumbaugh, Ivar Jacobson : The Unified Modeling Language User Guide, Pearson Education.
2. Hans-Erik Eriksson, Magnus Penker, Brian Lyons, David Fado: UML 2 Toolkit, WILEY-Dreamtech India Pvt. Ltd.

REFERENCES:

1. Meilir Page-Jones: Fundamentals of Object Oriented Design in UML, Pearson Education.
2. Pascal Roques: Modeling Software Systems Using UML2, WILEY-Dreamtech India Pvt. Ltd.
3. Atul Kahate: Object Oriented Analysis & Design, The McGraw-Hill Companies.
4. Mark Priestley: Practical Object-Oriented Design with UML, TATA McGrawHill
5. Craig Larman Applying UML and Patterns: An introduction to Object – Oriented Analysis and Design and Unified Process, Pearson Education.

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(07A60508) ADVANCED COMPUTER ARCHITECTURE

Unit I:

Fundamentals of Computer design- Technology trends- cost- measuring and reporting performance quantitative principles of computer design.

Unit II:

Instruction set principles and examples- classifying instruction set- memory addressing- type and size of operands- addressing modes for signal processing operations in the instruction set- instructions for control flow- encoding an instruction set.-the role of compiler

Unit III:

Instruction level parallelism (ILP)- over coming data hazards- reducing branch costs -high performance instruction delivery- hardware based speculation- limitation of ILP

Unit IV:

ILP software approach- compiler techniques- static branch protection- VLIW approach- H.W support for more ILP at compile time- H.W verses S.W solutions

Unit V:

Memory hierarchy design- cache performance- reducing cache misses penalty and miss rate – virtual memory- protection and examples of VM.

Unit VI:

Multiprocessors and thread level parallelism- symmetric shared memory architectures- distributed shared memory- Synchronization- multi threading.

Unit VII:

Storage systems- Types – Buses - RAID- errors and failures- bench marking a storage device- designing a I/O system.

Unit VIII:

Inter connection networks and clusters- interconnection network media – practical issues in interconnecting networks- examples – clusters- designing a cluster

TEXT BOOK:

1. Computer Architecture A quantitative approach 3rd edition John L. Hennessy & David A. Patterson Morgan Kufmann (An Imprint of Elsevier)

REFERENCE:

1. "Computer Architecture and parallel Processing" Kai Hwang and A.Briggs International Edition McGraw-Hill.
2. Advanced Computer Architectures, Dezso Sima, Terence Fountain, Peter Kacsuk, Pearson.

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(07A6EC13) WEB TECHNOLOGIES

Objectives:

This course demonstrate an in-depth understanding of the tools and Web technologies necessary for business application design and development. The course covers client side scripting like HTML, JavaScript and server side scripting like servlets, JSPs. And also XML and web servers and database interfacing.

Syllabus:

UNIT-I:

HTML Common tags- List, Tables, images, forms, Frames; Cascading Style sheets;

UNIT-II:

Introduction to Java Scripts, Objects in Java Script, Dynamic HTML with Java Script

UNIT-III:

XML: Document type definition, XML Schemas, Document Object model, Presenting XML, Using XML Processors: DOM and SAX

UNIT-IV:

Java Beans: Introduction to Java Beans, Advantages of Java Beans, BDK Introspection, Using Bound properties, Bean Info Interface, Constrained properties Persistence, Customizes, Java Beans API, Introduction to EJB's

UNIT-V:

Web Servers and Servlets: Tomcat web server, Introduction to Servlets: Lifecycle of a Servlet, JSDK, The Servlet API, The javax.servelet Package, Reading Servlet parameters, Reading Initialization parameters. The javax.servelet HTTP package, Handling Http Request & Responses, Using Cookies-Session Tracking, Security Issues,

UNIT-VI:

Introduction to JSP: The Problem with Servlet. The Anatomy of a JSP Page, JSP Processing. JSP Application Design with MVC Setting Up and JSP Environment: Installing the Java Software Development Kit, Tomcat Server & Testing Tomcat

UNIT-VII:

JSP Application Development: Generating Dynamic Content, Using Scripting Elements Implicit JSP Objects, Conditional Processing – Displaying Values Using an Expression to Set an Attribute, Declaring Variables and Methods Error Handling and Debugging Sharing Data Between JSP pages, Requests, and Users Passing Control and Data between Pages – Sharing Session and Application Data – Memory Usage Considerations

UNIT VIII:

Database Access : Database Programming using JDBC, Studying Javax.sql.* package, Accessing a Database from a JSP Page, Application – Specific Database Actions, Deploying JAVA Beans in a JSP Page, Introduction to struts framework..

TEXT BOOKS:

1. Web Programming, building internet applications, Chris Bates 2nd edition, WILEY Dreamtech (UNIT s 1,2,3)
2. The complete Reference Java 2 Fifth Edition by Patrick Naughton and Herbert Schildt. TMH (Chapters: 25) (UNIT 4)
3. Java Server Pages –Hans Bergsten, SPD O'Reilly (UNITs 5,6,7,8)

REFERENCE BOOKS:

1. Programming world wide web-Sebesta, Pearson
2. Core SERVLETS AND JAVASERVER PAGES VOLUME 1: CORE TECHNOLOGIES By Marty Hall and Larry Brown Pearson
3. Internet and World Wide Web – How to program by Dietel and Nieto PHI/ Pearson Education Asia.
4. Jakarta Struts Cookbook , Bill Siggelkow, S P D O'Reilly for chap 8.
5. Murach's beginning JAVA JDK 5, Murach, SPD
6. An Introduction to web Design and Programming –Wang-Thomson
7. Web Applications Technologies Concepts-Knuckles, John Wiley
8. Programming world wide web-Sebesta, Pearson
9. Web Warrior Guide to Web Programmimg-Bai/Ekedaw-Thomas
10. Beginning Web Programming-Jon Duckett WROX.
11. Java Server Pages, Pekowsky, Pearson.

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(07A60509) ARTIFICIAL INTELLIGENCE

UNIT-I

Introduction : AI problems, foundation of AI and history of AI intelligent agents: Agents and Environments, the concept of rationality, the nature of environments, structure of agents, problem solving agents, problem formulation.

UNIT-II

Searching: Searching for solutions, uniformed search strategies – Breadth first search, depth first search, Depth limited search, Iterative deepening depth first search bi-direction search - comparison. Search with partial information (Heuristic search) Greedy best first search, A* search, Memory bounded heuristic search, Heuristic functions.

UNIT-III

Local search Algorithms, Hill climbing, simulated, annealing search, local beam search, genetical algorithms. Constrain satisfaction problems: Backtracking search for CSPs local search for constraint satisfaction problems.

UNIT-IV

Game Playing: Adversial search, Games, minimax, algorithm, optimal decisions in multiplayer games, Alpha-Beta pruning, Evaluation functions, cutting of search.

UNIT-V

Knowledge Representation & Reasons logical Agents, Knowledge – Based Agents, the Wumpus world, logic, propositional logic, Resolution patterns in propositional logic, Resolution, Forward & Backward. Chaining.

UNIT-VI

First order logic. Inference in first order logic, propositional Vs. first order inference, unification & lifts forward chaining, Backward chaining, Resolution.

UNIT-VII

Planning – Classical planning problem, Language of planning problems, Expressiveness and extension, planning with state – space search, Forward states spare search, Backward states space search, Heuristics for stats space search. Planning search, planning with state space search, partial order planning Graphs.

UNIT-VIII

Learning – Forms of learning, Induction learning, Learning Decision Tree, Statistical learning methods, learning with complex data, learning with Hidden variables – The EM Algorithm, Instance Based learning, Neural Networks.

TEXT BOOKS:

1. Artificial Intelligence – A Modern Approach. Second Edition, Stuart Russel, Peter Norvig, PHI/Pearson Education.
2. Artificial Intelligence, 3rd Edition, Patrick Henry Winston., Pearson Edition,

Reference:

1. Artificial Intelligence , 2nd Edition, E.Rich and K.Knight (TMH).
2. Artificial Intelligence and Expert Systems – Patterson PHI
3. Expert Systems: Principles and Programming- Fourth Edn, Giarrantana/ Riley, Thomson
4. PROLOG Programming for Artificial Intelligence. Ivan Bratka- Third Edition – Pearson Education.

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(07A61591) COMPUTER NETWORKS AND UML LAB**Objective:**

- To Understand the functionalities of various layers of OSI model
- To provide an understanding of the design aspects of operating system

System/ Software Requirement

- Intel based desktop PCs LAN CONNECTED with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space

Part A

1. Implement the data link layer framing methods such as character, character stuffing and bit stuffing.
2. Implement on a data set of characters the three CRC polynomials – CRC 12 , CRC 16 and CRC CCIP .
3. Implement Dijkstra 's algorithm to compute the Shortest path thru a graph.
4. Take an example subnet graph with weights indicating delay between nodes. Now obtain Routing table at each node using distance vector routing algorithm
5. Take an example subnet of hosts . Obtain broadcast tree for it.
6. Take a 64 bit playing text and encrypt the same using DES algorithm .
7. Write a program to break the above DES coding
8. Using RSA algorithm Encrypt a text data and Decrypt the same .

Part B**UML Lab****Objective:**

- To inculcate object oriented software design

System/ Software Requirement

- Intel based desktop PCs LAN CONNECTED with minimum of 166 MHZ or faster processor with atleast 64 MB RAM and 100 MB free disk space
- Tools Such as Rational Rose

Syllabus:

1. The student should take up the case study of Unified Library application which is mentioned in the theory, and Model it in different views i.e Use case view, logical view, component view, Deployment view, Database design, forward and Reverse Engineering, and Generation of documentation of the project.
2. Student has to take up another case study of his/her own interest and do the same what ever mentioned in first problem. Some of the ideas regarding case studies are given in reference books which were mentioned in theory Syllabus can be referred for some idea.

NOTE: The analysis, design, coding, documentation, database design of mini project should be carried out in 4th year using object-oriented approach through UML.

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(07A61592) WEB TECHNOLOGIES LAB

Objective :

To create a fully functional website with mvc architecture. To Develop an online Book store using we can sell books (Ex amazon .com),

Hardware and Software required :

1. A working computer system with either Windows or Linux
2. A web browser either IE or firefox
3. Tomcat web server and Apache web server
4. XML editor like Altova Xml-spy [www.Altova.com/XMLSpy – free] , Stylusstudio , etc.,
5. A database either Mysql or Oracle
6. JVM(Java virtual machine) must be installed on your system
7. BDk(Bean development kit) must be also be installed

Week-1:

Design the following static web pages required for an online book store web site.

1) HOME PAGE:

The static home page must contain three frames.

Top frame : Logo and the college name and links to Home page, Login page, Registration page, Catalogue page and Cart page (the description of these pages will be given below).

Left frame : At least four links for navigation, which will display the catalogue of respective links. For e.g.: When you click the link "CSE" the catalogue for CSE Books should be displayed in the Right frame.

Right frame: The pages to the links in the left frame must be loaded here. Initially this page contains description of the web site.

Logo	Web Site Name
Home	Login
Registration	Catalogue
Cart	
CSECEEEECIVIL	Description of the Web Site

Fig 1.1

2) LOGIN PAGE:

This page looks like below:

Logo	Web Site Name
Home	Login
Registration	Catalogue
Cart	

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(07A7EC22) MULTIMEDIA AND APPLICATION DEVELOPMENT

UNIT-I

Fundamental concepts in Text and Image: Multimedia and hypermedia, world wide web, overview of multimedia software tools: Graphics and image data representation graphics/image data types, file formats, Color in image and video: color science, color models in images, color models in video.

UNIT-II

Fundamental concepts in video and digital audio: Types of video signals, analog video, digital video, digitization of sound, MIDI, quantization and transmission of audio.

UNIT-III

Action Script I: ActionScript Features, Object-Oriented ActionScript, Datatypes and Type Checking, Classes, Authoring an ActionScript Class

UNIT-IV

Action Script II: Inheritance, Authoring an ActionScript 2.0 Subclass, Interfaces, Packages, Exceptions

UNIT-V

Application Development: An OOP Application Frame work, Using mponents with ActionScript MovieClip Subclasses.

UNIT VI

Multimedia data compression: Lossless compression algorithm: Run-Length Coding, Variable Length Coding, Dictionary Based Coding, Arithmetic Coding, Lossless Image Compression, Lossy compression algorithm: Quantization, Transform Coding, Wavelet-Based Coding, Embedded Zerotree of Wavelet Coefficients Set Partitioning in Hierarchical Trees (SPIHT).

UNIT VII

Basic Video Compression Techniques: Introduction to video compression, video compression based on motion compensation, search for motion vectors, MPEG, Basic Audio Compression Techniques.

UNIT-VIII

Multimedia Networks: Basics of Multimedia Networks, Multimedia Network Communications and Applications : Quality of Multimedia Data Transmission,

Multimedia over IP, Multimedia over ATM Networks, Transport of MPEG-4, Mediaon- Demand(MOD).

TEXT BOOKS:

- 1) Fundamentals of Multimedia by Ze-Nian Li and Mark S. Drew PHI/Pearson Education
- 2) Essentials ActionScript 2.0, Colin Mook, SPD O'REILLY.

REFERENCES:

- 1) Digital Multimedia, Nigel chapman and jenny chapman, Wiley-Dreamtech
- 2) Macromedia Flash MX Professional 2004 Unleashed, Pearson.
- 3) Multimedia and communications Technology, Steve Heath, Elsevier(Focal Press)
- 4) Multimedia Applications, Steinmetz, Nahrstedt, Springer.
- 5) Multimedia Basics by Weixel Thomson
- 6) Multimedia Technology and Applications, David Hilman , Galgotia

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(07A7EC20) EMBEDDED SYSTEMS

Unit I

Embedded Computing: Introduction, Complex Systems and Microprocessor, The Embedded System Design Process, Formalisms for System Design, Design Examples. (Chapter I from Text Book 1, Wolf)

Unit II

The 8051 Architecture : Introduction, 8051 Micro controller Hardware, Input/Output Ports and Circuits, External Memory, Counter and Timers, Serial data Input/Output, Interrupts. (Chapter 3 from Text Book 2, Ayala)

Unit III

Basic Assembly Language Programming Concepts : The Assembly Language Programming Process, Programming Tools and Techniques, Programming the 8051. Data Transfer and Logical Instructions. (Chapters 4,5 and 6 from Text Book 2, Ayala)

Unit IV

Arithmetic Operations, Decimal Arithmetic. Jump and Call Instructions, Further Details on Interrupts. (Chapter 7 and 8 from Text Book 2, Ayala)

Unit-V

Applications: Interfacing with Keyboards, Displays, D/A and A/D Conversions, Multiple Interrupts, Serial Data Communication. (Chapter 10 and 11 from Text Book 2, Ayala)

Unit VI

Introduction to Real – Time Operating Systems: Tasks and Task States, Tasks and Data, Semaphores, and Shared Data; Message Queues, Mailboxes and Pipes, Timer Functions, Events, Memory Management, Interrupt Routines in an RTOS Environment (Chapter 6 and 7 from Text Book 3, Simon)

Unit VII

Basic Design Using a Real-Time Operating System: Principles, Semaphores and Queues, Hard Real-Time Scheduling Considerations, Saving Memory and Power, An example RTOS like uC-OS (Open Source); Embedded Software Development Tools: Host and Target machines, Linker/Locators for Embedded Software, Getting Embedded Software into the Target System; Debugging

Techniques: Testing on Host Machine, Using Laboratory Tools, An Example System.

(Chapter 8,9,10 and 11 from Text Book 3, Simon)

Unit VIII

Introduction to advanced architectures: ARM and SHARC, Processor and memory organization and Instruction level parallelism; Networked embedded systems: Bus protocols, I2C bus and CAN bus; Internet-Enabled Systems, Design Example- Elevator Controller.

(Chapter 8 from Text Book 1, Wolf)

TEXT BOOKS:

1. Computers and Components, Wayne Wolf, Elsevier.
2. The 8051 Microcontroller, Third Edition, Kenneth J. Ayala, Thomson.

REFERENCES :

1. Embedding system building blocks, Labrosse, via CMP publishers.
2. Embedded Systems, Raj Kamal, TMH.
3. Micro Controllers, Ajay V Deshmukhi, TMH.
4. Embedded System Design, Frank Vahid, Tony Givargis, John Wiley.
5. Microcontrollers, Raj kamal, Pearson Education.
6. An Embedded Software Primer, David E. Simon, Pearson Education.

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(07A71501) PERFORMANCE EVALUATION OF COMPUTER SYSTEMS

UNIT I

Introduction: Motivation, Probability Models; Sample Space, Events, Algebra Of Events, Graphical Methods of Representing Events, Probability Axioms, Discrete Random Variables, Random Variables and their Event Spaces, Distribution Functions, Special Discrete Distributions, Analysis of Program MAX, The probability generating function, Discrete Random Vectors, Independent Random variables, Continuous random variables, The Exponential Distribution, The reliability, failure density and hazard function, Some Important Distributions, Functions of a Random variable, Jointly distributed random variables, Order statistics

UNIT -II

Expectation: Moments, Expectation of Functions of More Than one Random Variable, Transform Methods, Moments and Transforms of some Important Distributions, Computation of Mean Time to Failure, Inequalities and Limit Theorems, Conditional distribution and conditional Expectation: Mixture distributions, Conditional Expectation, Imperfect Fault coverage and reliability. Stochastic Processes: Classification of Stochastic Processes, The Bernoulli Process, The Poisson Process, Renewal Processes, Availability Analysis, Random Incidence, Renewal Model of Program Behavior.

UNIT -III

Discrete- Parameter Markov Chains: Computation of n-step Transition Probabilities, State Classification and Limiting Distributions, Distribution of Times between state changes, Irreducible finite chains with Aperiodic states, The M/G/1 Queuing system, Discrete Parameter Birth-Death Processes, Finite Markov chains with absorbing states, Continuous-Parameter Markov chains: The Birth and Death Process, Other special cases of the Birth-Death Model, Non-Birth-Death Processes, Markov chains with absorbing states, Networks of Queues: Open Queuing Networks, Closed Queuing Networks, Non exponential Service-Time Distributions and Multiple Job Types, Non-Product-Form Networks.

UNIT -IV

Structures and Algorithms for Array Processors, SIMD Array Processors, AIMD Interconnection Networks, Parallel Algorithms for Array Processors, Associative Array Processing

UNIT -V

SMID Computers and performance Enhancement: The space of SMID computers, Array and Associative Processors, SIMD Computer Perspectives, The Illiac IV and the BSP Systems, The Illiac IV System Architecture, Applications of Illiac IV, The Massively Parallel Processor, The MPP System Architecture, Performance Enhancement Methods: Parallel Memory Allocation, Array Processing Languages, Performance Analysis of Array Processor4s, Multiple-SMID Computer Organization.

UNIT -VI

Multiprocessor Architecture and Programming, Functional Structures, Interconnection Networks, Parallel Memory Organizations, Multiprocessor Operation Systems, Exploiting Concurrency for Multiprocessing

UNIT -VII

Multiprocessing Control and Algorithm, Inter-process Communication Mechanisms, System Deadlocks and Protection, Multiprocessor Scheduling Strategies, Parallel Algorithms for Multiprocessors

UNIT -VIII

Example Multiprocessor Systems, The Space of Multiprocessor Systems, The C.mmp Multiprocessor System, The S-1 Multiprocessor, The HEP Multiprocessor, Mainframe Multiprocessor Systems, The Cray X-map and Cray 2

TEXT BOOKS:

1. Probability and Statistics with Reliability, Queuing, and Computer Science Applications, Kishore Trivedi, PHI.
2. Computer Architecture and Parallel Processing, Kai Hwang and Faye A. Briggs, MGH.

REFERENCE:

1. Computer Networks and Systems: Queuing Theory and Performance Evaluation, Thomas G. Robertazzi, Springer.

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(07A71502) COMPUTER SYSTEMS MAINTENANCE**UNIT -I**

An Inside Look at a Contemporary PC, The Contemporary PC, Disassembly/ Reassembly Notes, Standardized Form Factors, An Inside Look at Operating Systems and the Boot Process, The PC Hierarchy, Understanding Popular OS Features, The Boot Process, Mastering MSDOS.SYS, Creating a Boot Diskette, Multi-Boot Setup

UNIT -II

BIOS Typical Motherboard BIOS, BIOS Features, BIOS and Boot Sequence, BIOS Shortcomings and Compatibility issues, BIOS Troubleshooting, BIOS Upgrades. Buses Industry Standard Architecture (ISA), Peripheral Component Interconnect (PCI), Accelerated Graphics Port (AGP), Communication and Networking Riser (CNR). CD Drives: The CD-ROM Drive, The CD-R Drive, The CD-RW Drive, Troubleshooting CD Drives.

UNIT -III

CPU Identification and Troubleshooting CPU Essentials, Modern CPU Concepts, The Intel CPUs, The AMD CPUs, The VIA Cyrix CPUs, CPU Over clocking, Troubleshooting CPU Problems.

UNIT -IV

Data Recovery Techniques: Understanding Data Loss, Protecting Drives and Data Recovering Files and Folders, Recovering FAT and Directory Damage Recovering the MBR, Data Recovery Tips, Data Recovery Troubleshooting Drive Adapters and RAID Basics, Understanding the IDE Family, Controller Installation, RAID Primer, Troubleshooting a Drive Adapter.

UNIT -V

DVD Drives The Potential of DVD, Specifications and Standards, Content Protection, DVD Media, The DVD Drive and Decoder, DVD Drive Installation and Replacement, Troubleshooting DVD Drives, Enhancing System Performance Checking System Performance, System Monitor and Performance, Graphics Performance, Memory Performance, Drive Performance, Managing the Registry More Windows Performance Tips

UNIT -VI

Floppy Drives: The Floppy Drive, Floppy Troubleshooting Hard Drives: Basic Drive Concepts, IDE Drive Standards and Features, Drive Construction, Drive Preparing

Concepts, Drive Capacity Limits, Drive Installation/ Replacement Guidelines, Drive Testing and Troubleshooting.

UNIT -VII

Keyboards: Keyboard Basics, Keyboard Maintenance and Troubleshooting, Memory Troubleshooting, Essential Memory Concepts, Memory Packages and Structures, Logical Memory Organization, Memory Considerations, Parity and ECC, Selecting and Installing Memory, Memory Troubleshooting Modems: Dial-up Modem Construction and Operation, Understanding Signal Modulation, Signaling Standards, Installing an Analog Modem, Installing a Cable/ DSL Modem, Dial-up/ Cable/DSL Modem Troubleshooting

UNIT -VIII

Power Protection: Understanding Power Problems, Protection Devices, Backup, Backup, Backup Troubleshooting Power Protection Devices, Power Supplies and Power Management, Understanding Switching Supplies, Upgrading a Power Supply, Troubleshooting Switching Power Supplies, Understanding Power Management, Troubleshooting Power Management

TEXT BOOKS

1. PC Troubleshooting & Repair, The ultimate Reference, Stephen J. Bigelow, Dreamtech.
2. A+ Guide to managing and maintaining Your PC, Comprehensive, Fifth Edition, THOMSON.

REFERENCE:

1. PC Hardware Annoyances, Stephen J. Bigelow, SPD-Oreille.

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(07A7EC25) NETWORK MANAGEMENT SYSTEMS (ELECTIVE-I)

Unit - I :

Data communications and Network Management Overview: Analogy of Telephone Network Management, Communications protocols and Standards, Case Histories of Networking and Management, Challenges of Information Technology Managers, Network Management: Goals, Organization, and Functions, Network and System Management, Network Management System Platform, Current Status and future of Network Management.

UNIT-II:

SNMPV1 Network Management: Organization and Information and Information Models.

Managed network: Case Histories and Examples, The History of SNMP Management, The SNMP Model, The Organization Model, System Overview, The Information Model.

UNIT-III:

SNMPv1 Network Management: Communication and Functional Models The SNMP Communication Model, Functional model

UNIT-IV:

SNMP Management: SNMPv2 :Major Changes in SNMPv2, SNMPv2 System Architecture, SNMPv2 Structure of Management Information, The SNMPv2 Management Information Base, SNMPv2 Protocol, Compatibility With SNMPv1

UNIT-V:

SNMP Management: RMON:What is Remote Monitoring? , RMON SMI and MIB, RMON1, RMON2, ATM Remote Monitoring, A Case Study of Internet Traffic Using RMON

UNIT-VI:

Telecommunications Management Network:Why TMN? , Operations Systems, TMN Conceptual Model, TMN Standards, TMN Architecture, TMN Management Service Architecture, An Integrated View of TMN, Implementation Issues.

UNIT-VII:

Network Management Tools and Systems:Network Management Tools, Network Statistics Measurement Systems, History of Enterprise Management, Network

Management systems, Commercial Network management Systems, System Management, Enterprise Management Solutions.

UNIT-VIII:

Web-Based Management: NMS with Web Interface and Web-Based Management, Web Interface to SNMP Management, Embedded Web-Based Management, Desktop management Interface, Web-Based Enterprise Management, WBEM: Windows Management Instrumentation, Java management Extensions, Management of a Storage Area Network: , Future Directions

TEXT BOOK:

1. Network Management, Principles and Practice, Mani Subrahmanian, Pearson Education.

REFERENCES :

1. Network management, Morris, Pearson Education.
2. Principles of Network System Administration, Mark Burges, Wiley Dreamtech.
3. Distributed Network Management, Paul, John Wiley.

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**(07A7EC39) INFORMATION SECURITY
(ELECTIVE-I)**

UNIT-I:

Security Attacks (Interruption, Interception, Modification and Fabrication), Security Services (Confidentiality, Authentication, Integrity, Non-repudiation, access Control and Availability) and Mechanisms, A model for Internetwork security, Internet Standards and RFCs, Buffer overflow & format string vulnerabilities, TCP session hijacking, ARP attacks, route table modification, UDP hijacking, and man-in-the-middle attacks.

UNIT-II:

Conventional Encryption Principles, Conventional encryption algorithms, cipher block modes of operation, location of encryption devices, key distribution Approaches of Message Authentication, Secure Hash Functions and HMAC,

UNIT-III:

Public key cryptography principles, public key cryptography algorithms, digital signatures, digital Certificates, Certificate Authority and key management Kerberos, X.509 Directory Authentication Service

UNIT-IV:

Email privacy: Pretty Good Privacy (PGP) and S/MIME.

UNIT-V:

IP Security Overview, IP Security Architecture, Authentication Header, Encapsulating Security Payload, Combining Security Associations and Key Management

UNIT-VI:

Web Security Requirements, Secure Socket Layer (SSL) and Transport Layer Security (TLS), Secure Electronic Transaction (SET)

UNIT-VII:

Basic concepts of SNMP, SNMPv1 Community facility and SNMPv3 Intruders, Viruses and related threats

UNIT-VIII:

Firewall Design principles, Trusted Systems. Intrusion Detection Systems

TEXT BOOKS:

1. Network Security Essentials (Applications and Standards) by William Stallings Pearson Education.
2. Hack Proofing your network by Ryan Russell, Dan Kaminsky, Rain Forest Puppy, Joe Grand, David Ahmad, Hal Flynn Ido Dubrawsky, Steve W. Manzuik and Ryan Permeah, Wiley Dreamtech,

REFERENCES:

1. Fundamentals of Network Security by Eric Maiwald (Dreamtech press)
2. Network Security - Private Communication in a Public World by Charlie Kaufman, Radia Perlman and Mike Speciner, Pearson/PHI.
3. Cryptography and network Security, Third edition, Stallings, PHI/Pearson
4. Principles of Information Security, Whitman, Thomson.
5. Network Security: The complete reference, Robert Bragg, Mark Rhodes, TMH
6. Introduction to Cryptography, Buchmann, Springer.

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**(07A7EC40) HUMAN COMPUTER INTERFACE
(ELECTIVE-I)**

UNIT-I:

Introduction: Importance of user Interface – definition, importance of good design. Benefits of good design. A brief history of Screen design.

UNIT-II:

The graphical user interface – popularity of graphics, the concept of direct manipulation, graphical system, Characteristics, Web user – Interface popularity, characteristics- Principles of user interface.

UNIT-III:

Design process – Human interaction with computers, importance of human characteristics human consideration, Human interaction speeds, understanding business junctions.

UNIT-IV:

Screen Designing:- Design goals – Screen planning and purpose, organizing screen elements, ordering of screen data and content – screen navigation and flow – Visually pleasing composition – amount of information – focus and emphasis – presentation information simply and meaningfully – information retrieval on web – statistical graphics – Technological consideration in interface design.

UNIT-V

Windows – New and Navigation schemes selection of window, selection of devices based and screen based controls.

UNIT-VI

Components – text and messages, Icons and increases – Multimedia, colors, uses problems, choosing colors.

UNIT-VII

Software tools – Specification methods, interface – Building Tools.

UNIT-VIII

Interaction Devices – Keyboard and function keys – pointing devices – speech recognition digitization and generation – image and video displays – drivers.

TEXT BOOKS:

1. The essential guide to user interface design, Wilbert O Galitz, Wiley DreamaTech.
2. Designing the user interface. 3rd Edition Ben Shneidermann, Pearson Education Asia

REFERENCES:

1. Human – Computer Interaction. ALAN DIX, JANET FINCAY, GRE GORYD, ABOWD, RUSSELL BEALG, PEARSON.
2. Interaction Design, PRECE, ROGERS, SHARPS. Wiley Dreamtech,
3. User Interface Design, Soren Lauesen, Pearson Education.

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**(07A7EC24) ADVANCED COMPUTING CONCEPTS
(ELECTIVE-II)**

UNIT I

Grid Computing: Data & Computational Grids, Grid Architectures and its relations to various Distributed Technologies

UNIT II

Autonomic Computing, Examples of the Grid Computing Efforts (IBM).

UNIT III

Cluster setup & its Advantages, Performance Models & Simulations; Networking Protocols & I/O, Messaging systems.

UNIT IV

Process scheduling, Load sharing and Balancing; Distributed shared memory, parallel I/O

UNIT V

Example cluster System - Beowulf; Cluster Operating systems: COMPaS and NanOS

UNIT VI

Pervasive Computing concepts & Scenarios; Hardware & Software; Human - machine interface.

UNIT VII

Device connectivity; Java for Pervasive devices; Application examples

UNIT V III

Classical Vs Quantum logic gates; One, two & three Qubit Quantum gates; Fredkin & Toffoli gates; Quantum circuits; Quantum algorithms

TEXT BOOK:

1. J. Joseph & C. Fellenstein: 'Grid Computing', Pearson Education.
2. J. Burkhardt et al.: 'Pervasive computing' Pearson Education
3. Marivesar: 'Approaching quantum computing', Pearson Education.

REFERENCES:

1. Raj Kumar Buyya: 'High performance cluster computing', Pearson Education.
2. Neilsen & Chung L: 'Quantum computing and Quantum Information', Cambridge University Press.
3. A networking approach to Grid Computing, Minoli, Wiley.

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**(07A70509) DISTRIBUTED DATABASES
(ELECTIVE II)**

UNIT I

Features of Distributed versus Centralized Databases, Principles Of Distributed Databases, Levels Of Distribution Transparency, Reference Architecture for Distributed Databases, Types of Data Fragmentation, Integrity Constraints in Distributed Databases

UNIT -II

Translation of Global Queries to Fragment Queries, Equivalence Transformations for Queries, Transforming Global Queries into Fragment Queries, Distributed Grouping and Aggregate Function Evaluation, Parametric Queries

UNIT -III

Optimization of Access Strategies, A Framework for Query Optimization, Join Queries, General Queries

UNIT IV

The Management of Distributed Transactions, A Framework for Transaction Management, Supporting Atomicity of Distributed Transactions, Concurrency Control for Distributed Transactions, Architectural Aspects of Distributed Transactions

UNIT V

Concurrency Control, Foundation of Distributed Concurrency Control, Distributed Deadlocks, Concurrency Control based on Timestamps, Optimistic Methods for Distributed Concurrency Control.

UNIT -VI

Reliability, Basic Concepts, Nonblocking Commitment Protocols, Reliability and concurrency Control, Determining a Consistent View of the Network, Detection and Resolution of Inconsistency, Checkpoints and Cold Restart, Distributed Database Administration, Catalog Management in Distributed Databases, Authorization and Protection

UNIT -VII

Architectural Issues, Alternative Client/Server Architectures, Cache Consistency Object Management, Object Identifier Management, Pointer Swizzling, Object Migration, Distributed Object Storage, Object Query Processing, Object Query Processor Architectures, Query Processing Issues, Query Execution, Transaction

Management, Transaction Management in Object DBMSs , Transactions as Objects

UNIT -VIII

Database Integration, Scheme Translation, Scheme Integration, Query Processing Query Processing Layers in Distributed Multi-DBMSs, Query Optimization Issues Transaction Management Transaction and Computation Model Multidatabase Concurrency Control, Multidatabase Recovery, Object Orientation And Interoperability Object Management Architecture CORBA and Database Interoperability Distributed Component Model COM/OLE and Database Interoperability, PUSH-Based Technologies

TEXT BOOKS

1. Distributed Database Principles & Systems, Stefano Ceri, Giuseppe Pelagatti McGraw-Hill
2. Principles of Distributed Database Systems, M. Tamer Ozsu, Patrick Valduriez - Pearson Education

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(07A7EC44) NEURAL NETWORKS (ELECTIVE-II)

Objectives

Understanding the alternative paradigm of conventional computing by machines called neural computing. This subject covers from basic neuron model to complex models, their learning algorithms and their applications

UNIT I

INTRODUCTION - what is a neural network? Human Brain, Models of a Neuron, Neural networks viewed as Directed Graphs; Network Architectures, Knowledge Representation, Artificial Intelligence and Neural Networks (p. no's 1 -49)

UNIT II

LEARNING PROCESS – Error Correction learning, Memory based learning, Hebbian learning, Competitive, Boltzmann learning, Credit Assignment Problem, Memory, Adaption, Statistical nature of the learning process, (p. no's 50 -116)

UNIT III

SINGLE LAYER PERCEPTRONS – Adaptive filtering problem, Unconstrained Organization Techniques, Linear least square filters, least mean square algorithm, learning curves, Learning rate annealing techniques, perceptron –convergence theorem, Relation between perceptron and Bayes classifier for a Gaussian Environment (p. no's 117 -155)

UNIT IV

MULTILAYER PERCEPTRON – Back propagation algorithm XOR problem, Heuristics, Output representation and decision rule, Computer experiment, feature detection, (p. no's 156 -201)

UNIT V

BACK PROPAGATION - back propagation and differentiation, Hessian matrix, Generalization, Cross validation, Network pruning Techniques, Virtues and limitations of back propagation learning, Accelerated convergence, supervised learning. (p. no's 202 -234)

UNIT VI

SELF ORGANIZATION MAPS – Two basic feature mapping models, Self organization map, SOM algorithm, properties of feature map, computer simulations, learning vector quantization, Adaptive pattern classification (p. no's 443 -469, 9.1 -9.8)

UNIT VII

NEURO DYNAMICS – Dynamical systems, stability of equilibrium states, attractors, neurodynamical models, manipulation of attractors as a recurrent network paradigm (p. no's 664 –680, 14.1 –14.6)

UNIT VIII

HOPFIELD MODELS – Hopfield models, computer experiment I (p. no's 680-701, 14.7 –14.8)

TEXT BOOK:

1. Neural networks A comprehensive foundations, Simon Haykin, Pearson Education 2nd edition 2004

REFERENCES

1. Artificial neural networks - B.Vegnanarayana Prentice Hall of India P Ltd 2005
2. Neural networks in Computer intelligence, Li Min Fu TMH 2003
3. Neural networks James A Freeman David M S kapura pearson education

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(07A71591) EMBEDDED SYSTEMS LAB

1. Write a program to
 - a) Read inputs from switches
 - b) To make LEDs blink
 2. Write a Program for serial communication
 3. Write a program for encryption/decryption
 4. Develop necessary interfacing circuit to read data from a sensor and process using the 8051 board. The data has to be displayed on a PC monitor.
 5. Port RTOs (ICOS) on to 89CS1 board and Verify.
 6. Simulate on elevator movement using RTOs on 89CSI board
- Ref: KVKKF Prasad: 'Embedded/Real-Time Systems', Dreamtech. Press.

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(07A71592) MULTIMEDIA AND APPLICATION DEVELOPMENT LAB

1. Assigning Actions to an Object, and a Button
2. Creating Loops
3. Generation Random Numbers
4. Creating a Function, Calling a Function
5. Detecting the Player Version
6. Detecting the Operating System
7. Checking the System language
8. Detecting Display Settings
9. Tinting a Movie Clip's Color
10. Controlling a Movie Clip's Color with Sliders
11. Drawing a Circle
12. Drawing a Rectangle
13. Filling a Shape with a Gradient
14. Scripting Masks
15. Converting Angle Measurements
16. Calculating the Distance Between the Two Points
17. Formatting Currency Amount
18. Converting Between Units of Measurement
19. Determining Points Along a Circle
20. Sorting or Reversing an Array
21. Implementing a Custom Sort
22. Creating a Text Field
23. Making a Password Inputfield All the above programs are to be done in Flash MX 2004.

REFERENCES:

1. Action Script Cookbook, Joey Lott, SPD-Oreilly.
2. Flash MX Action Script for designers, Doug Sahlin, Dreamtech Wiley.
3. Flash MX Professional 2004 Unleashed, David Voegelers and Matthew Pizzi, Pearson Education.

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(07A8HS02) MANAGEMENT SCIENCE

Unit I

Introduction to Management: Concepts of Management and organization-nature, importance and Functions of Management, Taylor's Scientific Management Theory, Fayol's Principles of Management, Mayo's Hawthorne Experiments, Maslow's Theory of Human Needs, Douglas McGregor's Theory X and Theory Y, Herzberg's Two-Factor Theory of Motivation, Systems Approach to Management, Leadership Styles, Social responsibilities of Management.

Unit II

Designing Organisational Structures: Basic concepts related to Organisation - Departmentation and Decentralisation, Types of mechanistic and organic structures of organisation (Line organization, Line and staff organization, functional organization, Committee organization, matrix organization, Virtual Organisation, Cellular Organisation, team structure, boundaryless organization, inverted pyramid structure, lean and flat organization structure) and their merits, demerits and suitability.

Unit III

Operations Management: Principles and Types of Plant Layout-Methods of production (Job, batch and Mass Production), Work Study -Basic procedure involved in Method Study and Work Measurement-Statistical Quality Control: \bar{X} chart, \bar{R} chart, c chart, p chart, (simple Problems), Acceptance Sampling, Deming's contribution to quality.

Unit VI

a) **Materials Management:** Objectives, Need for Inventory control, EOQ, ABC Analysis, Purchase Procedure, Stores Management and Stores Records.

b) **Marketing:** Functions of Marketing, Marketing Mix, Marketing Strategies based on Product Life Cycle, Channels of distribution

Unit V

Human Resources Management (HRM): Concepts of HRM, HRD and Personnel Management and Industrial Relations (PMIR), HRM vs. PMIR, Basic functions of HR Manager: Manpower planning, Recruitment, Selection, Training and Development, Placement, Wage and Salary Administration, Promotion, Transfer, Separation, Performance Appraisal, Grievance Handling and Welfare Administration, Job Evaluation and Merit Rating.

Unit VI

Project Management (PERT/CPM): Network Analysis, Programme Evaluation and Review Technique (PERT), Critical Path Method (CPM), Identifying critical path, Probability of Completing the project within given time, Project Cost Analysis, Project Crashing. (simple problems)

Unit VII

Strategic Management: Mission, Goals, Objectives, Policy, Strategy, Programmes, Elements of Corporate Planning Process, Environmental Scanning, Value Chain Analysis, SWOT Analysis, Steps in Strategy Formulation and Implementation, Generic Strategy alternatives.

Unit VIII

Contemporary Management Practices: Basic concepts of MIS, End User Computing, Materials Requirement Planning (MRP), Just-In-Time (JIT) System, Total Quality Management (TQM), Six sigma and Capability Maturity Model (CMM) Levels, Supply Chain Management, Enterprise Resource Planning (ERP), Performance Management, Business Process outsourcing (BPO), Business Process Re-engineering and Bench Marking, Balanced Score Card.

TEXT BOOKS:

1. Aryasri: *Management Science*, TMH, 2004.
2. Stoner, Freeman, Gilbert, *Management*, 6th Ed, Pearson Education, New Delhi, 2004.

REFERENCES :

1. Kotler Philip & Keller Kevin Lane: *Marketing Mangement* 12/e, PHI, 2005
2. Koontz & Weihrich: *Essentials of Management*, 6/e, TMH, 2005
3. Thomas N.Duening & John M.Ivancevich *Management—Principles and Guidelines*, Biztantra, 2003.
4. Kanishka Bedi, *Production and Operations Management*, Oxford University Press, 2004.
5. Memoria & S.V.Gauker, *Personnel Management*, Himalaya, 25/e, 2005
6. Samuel C.Certo: *Modern Management*, 9/e, PHI, 2005
7. Schermerhorn, Capling, Poole & Wiesner: *Management*, Wiley, 2002.
8. Parnell: *Strategic Management*, Biztantra, 2003.
9. Lawrence R Jauch, R.Gupta & William F.Glueck: *Business Policy and Strategic Management*, Frank Bros. 2005.
10. L.S.Srinath: *PERT/CPM*, Affiliated East-West Press, 2005.

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**(07A81202) MOBILE COMPUTING
(ELECTIVE-III)**

Unit I:

Introduction to Mobile Communications and Computing: Mobile Computing (MC): Introduction to MC, novel applications, limitations, and architecture GSM: Mobile services, System architecture, Radio interface, Protocols, Localization and calling, Handover, Security, and New data services.

Unit II:

(Wireless) Medium Access Control: Motivation for a specialized MAC (Hidden and exposed terminals, Near and far terminals), SDMA, FDMA, TDMA, CDMA.

Unit III:

Mobile Network Layer: Mobile IP (Goals, assumptions, entities and terminology, IP packet delivery, agent advertisement and discovery, registration, tunneling and encapsulation, optimizations), Dynamic Host Configuration Protocol (DHCP).

Unit IV:

Mobile Transport Layer: Traditional TCP, Indirect TCP, Snooping TCP, Mobile TCP, Fast retransmit/fast recovery, Transmission /time-out freezing, Selective retransmission, Transaction oriented TCP.

Unit V:

Database Issues: Hoarding techniques, caching invalidation mechanisms, client server computing with adaptation, power-aware and context-aware computing, transactional models, query processing, recovery, and quality of service issues.

Unit VI:

Data Dissemination: Communications asymmetry, classification of new data delivery mechanisms, push-based mechanisms, pull-based mechanisms, hybrid mechanisms, selective tuning (indexing) techniques.

Unit VII:

Mobile Ad hoc Networks (MANETs): Overview, Properties of a MANET, spectrum of MANET applications, routing and various routing algorithms, security in MANETs.

Unit VIII:

Protocols and Tools: Wireless Application Protocol-WAP. (Introduction, protocol architecture, and treatment of protocols of all layers), Bluetooth (User scenarios, physical layer, MAC layer, networking, security, link management) and J2ME.

TEXT BOOKS:

- 1) Jochen Schiller, "Mobile Communications", Addison-Wesley. (Chapters 4, 7, 9, 10, 11), second edition, 2004.
- 2) Stojmenovic and Cacute, "Handbook of Wireless Networks and Mobile Computing", Wiley, 2002, ISBN 0471419028. (Chapters 11, 15, 17, 26 and 27)

REFERENCES:

- 1) Reza Behravanfar, "Mobile Computing Principles: Designing and Developing Mobile Applications with UML and XML", ISBN: 0521817331, Cambridge University Press, October 2004.
- 2) Adelstein, Frank, Gupta, Sandeep KS, Richard III, Golden, Schwiebert, Loren, "Fundamentals of Mobile and Pervasive Computing", ISBN: 0071412379, McGraw-Hill Professional, 2005.
- 3) Hansmann, Merk, Nicklous, Stober, "Principles of Mobile Computing", Springer, second edition, 2003.
- 4) Martyn Mallick, "Mobile and Wireless Design Essentials", Wiley DreamTech, 2003

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**(07A8EC25) VLSI DESIGN
(ELECTIVE-III)**

UNIT I - INTRODUCTION

Introduction to IC Technology – MOS, PMOS, NMOS, CMOS & BiCMOS technologies- Oxidation, Lithography, Diffusion, Ion implantation, Metallisation, Encapsulation, Probe testing, Integrated Resistors and Capacitors.

UNIT II - BASIC ELECTRICAL PROPERTIES

Basic Electrical Properties of MOS and BiCMOS Circuits: I_{ds} - V_{ds} relationships, MOS transistor threshold Voltage, g_m , g_{ds} , figure of merit μ_0 ; Pass transistor, NMOS Inverter, Various pull ups, CMOS Inverter analysis and design, Bi-CMOS Inverters.

UNIT III - VLSI CIRCUIT DESIGN PROCESSES

VLSI Design Flow, MOS Layers, Stick Diagrams, Design Rules and Layout, 2 μm CMOS Design rules for wires, Contacts and Transistors Layout Diagrams for NMOS and CMOS Inverters and Gates, Scaling of MOS circuits, Limitations of Scaling.

UNIT IV - GATE LEVEL DESIGN

Logic Gates and Other complex gates, Switch logic, Alternate gate circuits. Basic circuit concepts, Sheet Resistance R_s and its concept to MOS, Area Capacitance Units, Calculations - t - Delays, Driving large Capacitive Loads, Wiring Capacitances, Fan-in and fan-out, Choice of layers

UNIT V - SUBSYSTEM DESIGN

Subsystem Design, Shifters, Adders, ALUs, Multipliers, Parity generators, Comparators, Zero/One Detectors, Counters, High Density Memory Elements.

UNIT VI - SEMICONDUCTOR INTEGRATED CIRCUIT DESIGN

PLAs, FPGAs, CPLDs, Standard Cells, Programmable Array Logic, Design Approach.

UNIT VII - VHDL SYNTHESIS

VHDL Synthesis, Circuit Design Flow, Circuit Synthesis, Simulation, Layout, Design capture tools, Design Verification Tools, Test Principles.

UNIT VIII CMOS TESTING

CMOS Testing, Need for testing, Test Principles, Design Strategies for test, Chip-level Test Techniques, System-level Test Techniques, Layout Design for improved Testability.

TEXTBOOKS :

1. Essentials of VLSI circuits and systems – Kamran Eshraghian, Eshraghian Douglas and A. Pucknell, PHI, 2005 Edition.
2. Principles of CMOS VLSI Design - Weste and Eshraghian, Pearson Education, 1999.

REFERENCES :

1. Chip Design for Submicron VLSI: CMOS Layout & Simulation, - John P. Uyemura, Thomson Learning.
2. Introduction to VLSI Circuits and Systems - John .P. Uyemura, JohnWiley, 2003.
3. Digital Integrated Circuits - John M. Rabaey, PHI, EEE, 1997.
4. Modern VLSI Design - Wayne Wolf, Pearson Education, 3rd Edition, 1997.

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**(07A8EC26) DATA WAREHOUSING AND DATA MINING
(ELECTIVE-III)**

UNIT - I

Introduction : Fundamentals of data mining, Data Mining Functionalities, Classification of Data Mining systems, Major issues in Data Mining.

Data Preprocessing : Needs Preprocessing the Data, Data Cleaning, Data Integration and Transformation, Data Reduction, Discretization and Concept Hierarchy Generation.

UNIT - II

Data Warehouse and OLAP Technology for Data Mining Data Warehouse, Multidimensional Data Model, Data Warehouse Architecture, Data Warehouse Implementation, Further Development of Data Cube Technology, From Data Warehousing to Data Mining.

UNIT - III

Data Mining Primitives, Languages, and System Architectures : Data Mining Primitives, Data Mining Query Languages, Designing Graphical User Interfaces Based on a Data Mining Query Language Architectures of Data Mining Systems.

UNIT - IV

Concepts Description : Characterization and Comparison : Data Generalization and Summarization- Based Characterization, Analytical Characterization: Analysis of Attribute Relevance, Mining Class Comparisons: Discriminating between Different Classes, Mining Descriptive Statistical Measures in Large Databases.

UNIT - V

Mining Association Rules in Large Databases : Association Rule Mining, Mining Single-Dimensional Boolean Association Rules from Transactional Databases, Mining Multilevel Association Rules from Transaction Databases, Mining Multidimensional Association Rules from Relational Databases and Data Warehouses, From Association Mining to Correlation Analysis, Constraint-Based Association Mining.

UNIT - VI

Classification and Prediction : Issues Regarding Classification and Prediction, Classification by Decision Tree Induction, Bayesian Classification, Classification by Backpropagation, Classification Based on Concepts from Association Rule Mining, Other Classification Methods, Prediction, Classifier Accuracy.

UNIT - VII

Cluster Analysis Introduction : Types of Data in Cluster Analysis, A Categorization of Major Clustering Methods, Partitioning Methods, Density-Based Methods, Grid-Based Methods, Model-Based Clustering Methods, Outlier Analysis.

UNIT - VIII

Mining Complex Types of Data : Multidimensional Analysis and Descriptive Mining of Complex, Data Objects, Mining Spatial Databases, Mining Multimedia Databases, Mining Time-Series and Sequence Data, Mining Text Databases, Mining the World Wide Web.

TEXT BOOKS :

1. Data Mining – Concepts and Techniques - JIAWEI HAN & MICHELINE KAMBER Harcourt India.

REFERENCES :

1. Data Mining Introductory and advanced topics –MARGARET H DUNHAM, PEARSON EDUCATION
2. Data Mining Techniques – ARUN K PUJARI, University Press.
3. Data Warehousing in the Real World – SAM ANAHORY & DENNIS MURRAY. Pearson Edn Asia.
4. Data Warehousing Fundamentals – PAULRAJ PONNAIAH WILEY STUDENT EDITION.
5. The Data Warehouse Life cycle Tool kit – RALPH KIMBALL WILEY STUDENT EDITION.

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**(07A8EC13) DESIGN PATTERNS
(ELECTIVE-IV)**

UNIT -I:

Introduction: What Is a Design Pattern?, Design Patterns in Smalltalk MVC, Describing Design Patterns, The Catalog of Design Patterns, Organizing the Catalog, How Design Patterns Solve Design Problems, How to Select a Design Pattern, How to Use a Design Pattern.

UNIT-II:

A Case Study: Designing a Document Editor: Design Problems, Document Structure, Formatting, Embellishing the User Interface, Supporting Multiple Lookand- Feel Standards, Supporting Multiple Window Systems, User Operations Spelling Checking and Hyphenation, Summary .

UNIT-III:

Creational Patterns: Abstract Factory, Builder, Factory Method, Prototype, Singleton, Discussion of Creational Patterns.

UNIT-IV:

Structural Pattern Part-I: Adapter, Bridge, Composite.

UNIT-V:

Structural Pattern Part-II: Decorator, façade, Flyweight, Proxy.

UNIT-VI:

Behavioral Patterns Part-I: Chain of Responsibility, Command, Interpreter, Iterator.

UNIT-VII:

Behavioral Patterns Part-II: Mediator, Memento, Observer, State, Strategy, Template Method, Visitor, Discussion of Behavioral Patterns.

UNIT-VIII:

What to Expect from Design Patterns, A Brief History, The Pattern Community An Invitation, A Parting Thought.

TEXT BOOK:

1. Design Patterns By Erich Gamma, Pearson Education

REFERENCES:

1. Pattern's in JAVA Vol-I By Mark Grand ,Wiley DreamTech.
2. Pattern's in JAVA Vol-II By Mark Grand ,Wiley DreamTech.
3. JAVA Enterprise Design Patterns Vol-III By Mark Grand ,Wiley DreamTech.
4. Head First Design Patterns By Eric Freeman-Oreilly-spd
5. Design Patterns Explained By Alan Shalloway, Pearson Education.

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**(07A80514) SOFTWARE TESTING METHODOLOGIES
(ELECTIVE-IV)**

UNIT I :

Introduction : Purpose of testing, Dichotomies, model for testing, consequences of bugs, taxonomy of bugs

UNIT II :

Flow graphs and Path testing : Basics concepts of path testing, predicates, path predicates and achievable paths, path sensitizing, path instrumentation, application of path testing.

UNIT III :

Transaction Flow Testing : Transaction flows, transaction flow testing techniques. Dataflow testing:-Basics of dataflow testing, strategies in dataflow testing, application of dataflow testing.

UNIT IV :

Domain Testing: domains and paths, Nice & ugly domains, domain testing, domains and interfaces testing, domain and interface testing, domains and testability.

UNIT V :

Paths, Path products and Regular expressions : Path products & path expression, reduction procedure, applications, regular expressions & flow anomaly detection.

UNIT VI :

Logic Based Testing : Overview, decision tables, path expressions, kv charts, specifications.

UNIT VII :

State, State Graphs and Transition testing : State graphs, good & bad state graphs, state testing, Testability tips.

UNIT VIII :

Graph Matrices and Application : Motivational overview, matrix of graph, relations, power of a matrix, node reduction algorithm, building tools.

Usage of JMeter and Winrunner tools for functional / Regression testing, creation of test script for unattended testing, synchronization of test case, Rapid testing, Performance testing of a data base application and HTTP connection for website access.

TEXT BOOKS :

1. Software Testing techniques - Baris Beizer, Dreamtech, second edition.
2. Software Testing Tools - Dr.K.V.K.K.Prasad, Dreamtech.

REFERENCES :

1. The craft of software testing - Brian Marick, Pearson Education.
2. Software Testing Techniques - SPD(Oreille)
3. Software Testing in the Real World - Edward Kit, Pearson.
4. Effective methods of Software Testing, Perry, John Wiley.
5. Art of Software Testing - Meyers, John Wiley.

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**(07A8EC24) PATTERN RECOGNITION
(ELECTIVE IV)**

UNIT-I:

Introduction: Machine perception, pattern recognition example, pattern recognition systems, the design cycle, learning and adaptation (Text book-1, p.nos: 1-17).

UNIT-II:

Bayesian Decision Theory : Introduction, continuous features - two categories classifications, minimum error-rate classification- zero-one loss function, classifiers, discriminant functions, and decision surfaces (Text book-1, p.nos: 20-27, 29-31).

UNIT-III:

Normal density: Univariate and multivariate density, discriminant functions for the normal density-different cases, Bayes decision theory - discrete features, compound Bayesian decision theory and context (Text book-1, p.nos: 31-45, 51-54, 62-63).

UNIT-IV:

Maximum likelihood and Bayesian parameter estimation: Introduction, maximum likelihood estimation, Bayesian estimation, Bayesian parameter estimation-Gaussian case (Text book-1, p.nos: 84-97).

UNIT-V:

Un-supervised learning and clustering: Introduction, mixture densities and identifiability, maximum likelihood estimates, application to normal mixtures, Kmeans clustering. Data description and clustering - similarity measures, criteria function for clustering (Text book-1, p.nos: 517 - 526, 537 - 546).

UNIT-VI:

Component analyses: Principal component analysis, non-linear component analysis; Low dimensional representations and multi dimensional scaling (Text book-1, p.nos: 568-570, 573 - 576, 580-581).

UNIT-VII:

Discrete Hidden Markov Models:- Introduction, Discrete-time markov process, extensions to hidden Markov models, three basic problems for HMMs. (Text book -2, p.nos: 321 - 344)

UNIT-VIII:

Continuous hidden Markov models: Observation densities, training and testing with continuous HMMs, types of HMMs. (Text book-2, p.nos: 348 - 352)

TEXT BOOKS:

1. Pattern classifications, Richard O. Duda, Peter E. Hart, David G. Stroke. Wiley student edition, Second Edition.
2. Fundamentals of speech Recognition, Lawrence Rabiner, Biing - Hwang Juang Pearson education.

REFERENCE:

1. Pattern Recognition and Image Analysis - Earl Gose, Richard John baugh, Steve Jost PHI 2004

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07A81591	Industry Oriented Mini Project	-	-	2
07A81592	Seminar	-	-	2
07A81593	Project	-	-	10
07A81594	Comprehensi			