

Phone: Off: +91-40-23152216

Web: [www.jntuh.ac.in](http://www.jntuh.ac.in)

E Mail: [manzoorjntu@gmail.com](mailto:manzoorjntu@gmail.com)



**JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD**

(Established by Govt. Act No. 30 of 2008)

**Kukatpally, Hyderabad – 500085, Telangana, India**

Circular No. JNTUH/DAAF/A3/2499/IDP/2023

Date.31.07.2023

Sub: JNT University Hyderabad– DAAF –Approved the proposed guidelines and tuition fee share between JNTUH & VEDA IIT to conduct Integrated Dual Degree program (IDP) from the academic year 2023-24 at VEDA IIT, Hyderabad – Reg.

Ref: 1. 58<sup>th</sup> meeting of Standing Committee of the Academic Senate held on 23.05.2023.

2. University Lr. No. JNTUH/DAAF/A3/2499/2023, dated. 05.07.2023.

3. E-mail dated. 17.07.2023 of the Director, VEDA IIT, Hyderabad.

4. University Procds. No. JNTUH/DAAF/A3/2499/MoU/VEDA IIT/23, dated.26.07.2023

5. Committee report /minutes held on 27.07.2023

6. Note orders of the Vice-Chancellor dated.31.07.2023.

\*\_\*\_\*

In the reference (2) cited above, the University has approved the proposal for Integrated Dual Degree program in two disciplines in VLSI Engineering and Embedded System Design with a facility to transfer credits earned at VEDA IIT in Industry eco-system to JNTUH for the award of B.Tech & M.Tech degree for B.Tech students of JNTUH (University, Autonomous and Affiliated Colleges) who completed 3<sup>rd</sup> year (Regular/Full Time).

Directional insights given in NEP-2020 document of page No. 38, Clause 11.10 related connective leads in concerned to flexibility of program shall be implemented.

In the reference (5) cited above, based on the recommendations of the committee, the Hon'ble Vice-Chancellor is please to approve the following proposed guidelines and tuition fee share between JNTUH & VEDA IIT to conduct Integrated Dual Degree program (IDP) from the academic year 2023-24 at VEDA IIT, Hyderabad. The students of B. Tech. - ECE/EEE/EIE/CSE IV Year can opt for the courses offered by VEDA IIT, Hyderabad in collaboration with Consortium of Industries:

1. The Integrated Dual Degree Program will be offered in two disciplines in VLSI Engineering and Embedded System Design with a facility to transfer 108 credits earned at VEDA IIT for the award of B.Tech and M.Tech degree for B.Tech students of JNTUH (University, Autonomous and Affiliated Colleges) who completed 3<sup>rd</sup> Year B.Tech (Regular/Full) Time.
2. The IV and V Year (Four Semesters) are sponsored by the consortium of industries in collaboration with VEDA IIT to improve job opportunities in VLSI/ Embedded System Design as there is substantial demand for skilled postgraduates in these areas.
3. Selection of students for the IDP is based on merit tests and interviews conducted by VEDA IIT before the commencement of IV Year B.Tech.

4. The students have to pay their existing 4<sup>th</sup> Year Tuition fee to their respective colleges.
5. The students have to pay their 5<sup>th</sup> Year Tuition fee of Rs. 60,000/- per semester to JNTUH (30%) & VEDA IIT (70%)
6. On successful completion of 4<sup>th</sup> year, IDP students will be provided an internship with a stipend at the consortium / associated companies until successful completion of M. Tech (5<sup>th</sup> year).
7. The students have to follow the academic regulations & attendance requirements of both JNTUH & VEDA IIT.
8. VEDA IIT has to send monthly attendance & internal examination marks to JNTUH and the colleges concerned during 4<sup>th</sup> year and to JNTUH in 5<sup>th</sup> year
9. The students have to attend the course classes at VEDA Educational Society (VEDA IIT), Aydiv IT Park, Hyderabad.
10. The parent colleges have to submit the Expression of Interest (EoI) permitting their interested students to attend the classes and labs in 4<sup>th</sup> year at VEDA IIT and additionally the student pursues 5<sup>th</sup> year at VEDA IIT & its consortium under an MoU between JNTUH & VEDA IIT and both the degrees (B.Tech & M.Tech) will be awarded after completion of 5<sup>th</sup> year.
11. JNTUH agrees to transfer credits and marks earned at VEDA IIT to the concerned colleges (University & Autonomous) for the 4<sup>th</sup> year and to JNTUH for the 4<sup>th</sup> & 5<sup>th</sup> years for the award of B.Tech and M.Tech degrees.
12. For students who opted for this program, there is no exit policy in the middle of this program, either in IV or V Year and request to re-join in their parent colleges
13. VEDA IIT has to facilitate the placements for all the trained & successfully completed students, as per the terms and conditions of the companies.
14. As the Consortium of Industries sponsors this program in collaboration with VEDA IIT, the students are expected to submit, before joining the VEDA Program, a commitment agreement for a minimum period of 2 years on successfully completing (5<sup>th</sup> year) M. Tech. program. The clause for the exit policy commitment agreement should also be well-defined by the consortium at the time of admission.

Hence, the Principals of University Constituent, Autonomous, and Affiliated Colleges are requested to inform the students and submit their Expression of Interest (EoI) indicating the list of students (with names & H.T Nos.) who opted for this program in the areas of VLSI Engineering & Embedded System Design offered at VEDA IIT, Aydiv IT Park, Hyderabad on or before 05.08.2023 to the Registrar, JNTUH with a copy to the Director, VEDA IIT, Hyderabad.

Encl: Approved admission notification & Course structure.

To

The Principals of the all University Colleges of Engineering, JNTUH.

The Principals of the all Autonomous and Affiliated Colleges of JNTUH.

  
31/7/23  
**REGISTRAR**