

## GOVERNMENT OF TELANGANA, TASK & PVC

Offer in Collaboration with

**VEDA IIT**

Semiconductor Exposure Training - An overview – Window to the VLSI Concepts  
July 26<sup>th</sup> to August 07<sup>th</sup>, 2021

**VLSI Awareness Program conducted jointly by VEDA IIT and Telangana State Government to meet the following objectives:**

- To provide a general overview of the VLSI Engineering
- To introduce different aspects of VLSI Engineering
- To make the participants appreciate the depth of VLSI Engineering and the efforts required to excel in the field
- To make the students understand the abundant job opportunities in VLSI

### VLSI Concepts

- Logic Design
- Logic Verification
- Implementation
- DFT
- Stick diagrams & Layout
- Physical Design

- No Course fee
- Duration: 2 Weeks
- 6 days per week
- 2 hour sessions/day
- On-line

**Program schedule:**  
**26<sup>th</sup> July to 07<sup>th</sup> August 2021**

### About VLSI Promotional program of Government of Telangana:

To promote ESDM in the state, a program to promote VLSI has been rolled out. The program constitutes – imparting VLSI skill sets, support start ups and develop an ecosystem. Photonics Valley Corporation and TASK are coordinating the skilling initiative with the intervention of VLSI industry. This awareness program is aimed at creating awareness among the Engineering students about the VLSI discipline and abundant job opportunities it offers. The industry partner for this program is VEDA IIT.

### About VEDA

VEDA IIT is the leading institute imparting VLSI skill sets among the Engineering graduates for more than two decades. It is an eminent institution instrumental in bringing the VLSI evolution in the state. It supported many a company by rendering quality Human Resources. It was listed among top five institutes in VLSI Engineering by Times of India. It was the leading organizer of International VLSI & Embedded Systems Design Conferences - VLSI 2006, 2008, 2012 and 2017

### ELIGIBILITY

- Streams of Engg: Electronics, Electrical, Computer sciences, Instrumentation.
- Passed out students (B.Tech, M.Tech, M.Sc) and students completed 3rd year
- Passed out students from relevant streams of Polytechnics

**APPLY AT [WWW.VEDAIIT.ORG](http://WWW.VEDAIIT.ORG)**

Last date for application: **25<sup>th</sup> July, 2021**

(Registration can be closed earlier than this date for logistic reasons)

**VEDA IIT, A Unit of The VEDA Educational Society**

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