ACCREDITED BY NAAC

A-GRADE

JAWAHARLAL NEHRU TECHNOLOGICAL UNIVERSITY HYDERABAD
(Established by Govt. Act No. 30 of 2008)
Kukatpally, Hyderabad – 500 085, Telangana (India).

Dr.K. VENKATESWARA RAO
M. Sc.,M.Tech.,Phd.,PDF(Johns Hopkins Medicine USA)
Professor of Nano Technology &
REGISTRAR

Lr. No. JNTUH/DAAF/A3/1510/2024   Date.06.07.2024

To
The Principals of the all JNTUH University Colleges,
The Principals of the all Autonomous and Affiliated Colleges of JNTUH.

        Sub: JNTUH-DAAF – Permission for offer Industry Ready Professional Training in
              VLSI Design/Embedded System Design course to B. Tech. – ECE/EEE/EIE/CSE IV Year
              I & II Semester for the Academic Year 2024-25.
        Ref: 1. Circular No. JNTUH/DAP/A3/1510/2022 Dt. 20.07.2022.
             2. Letter dated. 26.06.2024 of the Director & Head, VEDA IIT, Hyderabad.
             3. University Lr. No. JNTUH/DAP/A3/1510/MoU/VEDA IIT/22, dated.30.12.2022.
             4. Note orders of the Vice-Chancellor I/c., dated. 04.07.2024.

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With reference the letter and e-mail (1&2) cited above, it is to inform you that the Hon'ble Vice-Chancellor I/c., is pleased to accord permission to offer 'Industry Ready Professional Training' programme in VLSI Design / Embedded System Design to B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester. The students of B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester can opt for the courses offered by VEDA IIT, Hyderabad in collaboration with Consortium of Industries viz., M/s. SoCtronics Technologies Private Limited. The courses offered by the JNTUH University, Autonomous & Affiliated Colleges in IV Year B. Tech. I & II Semesters for 40 (20+20) credits suitably modified with the courses offered by VEDA IIT, Hyderabad for 40 credits to make the students industry ready in the area of VLSI Design or Embedded System Design for B. Tech. - ECE/EEE/EIE/CSE IV Year I & II Semester students for the Academic Year 2024-25.

Hence, the Principals of JNTUH University, Autonomous and Affiliated Colleges are requested to inform to the students and submit Expression of Interest (EoI) indicating the list of students (with name & H.T No.) willing to opt. IV Year -I & II semester courses in area of VLSI & Embedded Systems offered at VEDA Educational Society (VEDA IIT), Aydiv IT Park, Hyderabad on or before 22.07.2024 to the Registrar, JNTUH and simultaneously register in www.vedaiit.org.

Yours sincerely,

REGISTRAR

Encl: Admission Notification, Approved course structure IV Year I and II semester
procedure for conducting of internal examination and Semester End Examination.
Copy to:
The Director, University Examinations (DUEX), JNTUH,
The Controller of Examination, JNTUH.
PA to VC/Rector/Registrar for information.

GATEWAY TO A LUCRATIVE CAREER IN SEMICONDUCTOR INDUSTRY

VEDA IIT OFFERS

VEDA Learn. Explore. Excel.

INDUSTRY-READY PROFESSIONAL TRAINING IN
VLSI DESIGN/EMBEDDED SYSTEM DESIGN

In fulfilment of coursework for 2 Semesters of B.Tech 4th Year for JNTUH Constituent, Affiliated and Autonomous Colleges

in collaboration with

SoCtronics

- VEDA IIT, one of the top VLSI Engineering Institutes, conducts the program
- Knowledge-intensive and Industry-Oriented Program
- JNTUH approved transfer of 40 credits earned at VEDA IIT for award of B.Tech Degree
- Internship in 2nd Semester in VEDA IIT / Consortium company*

*Terms and conditions apply. For details contact HR, VEDA IIT

COMMON ENTRANCE TEST

Preliminary Test(Online): 26th July, 2024; 10 AM - 11AM
Main Test(Offline): 4th August, 2024; 10 AM – 1 PM
Test Center: Hyderabad
Last Date to apply: 22nd July, 2024
Syllabus: visit www.vedaiit.org
Apply online at www.vedaiit.org

ELIGIBILITY & ADMISSION

➢ Students of JNTUH Constituent, Autonomous & Affiliated colleges, completing B.Tech. III year by 31st August, 2024 in ECE/EEE/EIE/CSE

➢ Admission: Based on performance in the entrance tests & Interview

ABOUT VEDA IIT

- Expert faculty from reputed VLSI Design Houses/Institutes
- Projects with proven design methodologies
- Well-equipped infrastructure with high-end state-of-the-art computing facilities
- Excellent placement track record in leading Technology Companies since inception
- Partnership with global leaders of technology

- Innovative Teaching-Company model with Industry and University participation - first of its kind
- Listed among top five institutes in VLSI Engineering by Times of India
- Pioneered in incubating many successful design teams/companies
- Lead organizers of International VLSI Conferences - VLSI 2006, 2008, 2012 & 2017
- Holistic education covering various aspects of VLSI and System Design

Hands-on Experience with best-in-class tools covering the entire ASIC design flow from the world class EDA Vendors

cadence™   Mentor Graphics®   SYNOPSYS® Silicon to Software™

VEDA Learn. Explore. Excel.

VEDA IIT, A Unit of The VEDA Educational Society
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