

A
National Level
Technical Symposium
EMBLAZON'19

April 2-3, 2019

Registration form

Please put tick ✓ on the event(s) to register

Paper Poster Project

Workshop Technical Quiz

1. Title of Paper / Poster / Project
2. Name
3. College / Institution
4. Course of Study / Branch
5. Address for Correspondence
6. Email:
Mobile:
7. Registration Fee Details
Amount DD No.
Date Name of Bank
8. Accommodation required: Yes / No
9. Signature of Applicant
10. Recommendation of the Head of Dept./Institution

Date:

Place:

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Vice-Chancellor, JNTU Hyderabad

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Professor & Head, Dept. of ECE

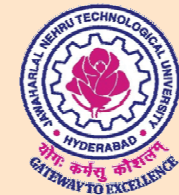
Co-Convener
Sri. S. Praveen Kumar, Asst. Prof., ECE

Student Coordinators
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Organised by

Department of
Electronics & Communication Engineering

JNTUH College of Engineering Jagtial

Nachupally (Kondagattu)
Jagtial Dist. - 505 501, Telangana

College Website: www.jntuhcej.ac.in

About the College

JNTUH College of Engineering, Nachupally (Kondagattu), Jagtial Dist. (JNTUH CEJ) was established in the year 2007, as one of the Constituent colleges of JNTU Hyderabad with five different branches, viz. ECE, CSE, IT, EEE and ME. The College is committed to raise the intellectual tone of young students in understanding and incorporating the foundations of rapidly progressing changes in the fields of Science and Technology. The location of sprawling campus surrounded by lush green hills, temples and academic ambience upholds the traditional and cultural values among the students. JNTUH CEJ is located at Nachupally near Kondagattu which is about 22 km from Jagtial and about 38 km from Karimnagar.

About the Department

The Department of Electronics and Communication Engineering has a team of experienced and dynamic faculty and staff. The Department offers B.Tech course in Electronics & Communication Engineering and M.Tech course in Digital Systems and Computer Electronics.

About the Symposium

Keeping in view, the recent phenomenal advances in the field of Electronics and Communication Engineering and related application fields, it is proposed to conduct a National Level Technical Symposium EMBLAZON'19 during April 2-3, 2019. This Symposium aims at encouraging all participants to explore their technical skills and enhance their conceptual knowledge.

The technical program consists of various events including:

- ❖ Paper Presentation (Rostrum)
- ❖ Poster Presentation (Urdhvaa)
- ❖ Project Expo (Antharya)
- ❖ Technical Quiz (Technocrats)
- ❖ Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software
- ❖ Electronics Crossword
- ❖ Gyan Dhrushya (Video exhibits in ECE domain)

Scope and Topics

The areas to be covered in the symposium include, but not limited to:

- ❖ Electronic Devices, Circuits and Systems
- ❖ Digital Electronics, VLSI and Embedded Systems
- ❖ Microwave, Satellite and Optical Communications
- ❖ Wireless Sensor Networks, Internet of Things
- ❖ Wearable Devices, Vehicular Networks, Robotics
- ❖ Artificial Intelligence, Machine Learning, Data Science
- ❖ Power Systems, Renewable Energy Systems, Smart Grid, Instrumentation
- ❖ Future Internet Applications & Services: E-Health, Location Based Services, Social Networks

Abstract Submission

Authors are invited to send an electronic version of the abstract for paper / poster / project presentation typed in MS WORD in about 200 words on any topic related to the broad themes of the symposium, via email so as to reach us by 22nd March 2019. Acceptance will be communicated to the corresponding author by 25th March 2019.

Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software (from National Instruments Corporation, USA)

In association with M/s. Trident TechLabs Pvt. Ltd., Hyderabad.

- ❖ Introduction to NI Multisim Software
- ❖ Hands on Simulation: Transient Analysis of RLC Circuits, Diode Circuits, Transistor Circuits, Voltage and Power Amplifiers, Oscillators
- ❖ Simulation of Operational Amplifier Circuits

The Two-Day Workshop will be held during April 2-3, 2019 from 10:00 AM to 5:00 PM. The workshop will be practically oriented and number of registrations is limited to 60. Interested students and faculty are required to register in advance by sending the duly filled registration form along with Registration Fee of Rs. 600/- addressed to the Convener.

Registration Fee

Paper/Poster/ Project Expo

Rs. 200/- (Two Hundred only) per head

Technical Quiz

Rs. 50/- (Fifty only) per head

B.E./B.Tech, M.E./M.Tech Students in ECE, CSE, IT, EEE.

Two Day National Workshop on Electronic Circuit Simulation using NI Multisim Software

B.E./B.Tech, M.E./M.Tech Students in ECE, CSE, IT, EEE, and Faculty of Engineering College.

Rs. 600/- (Six Hundred only) per head

Demand Draft should be drawn at in favour of "**Convener, Emblazon, JNTUHCEJ**" payable at Jagtial.

Important Dates

Deadline for Submission of Abstract	22nd March 2019
Intimation of Acceptance	25th March 2019
Deadline for Submission of Registration Form	29th March 2019

Venue

ECE DEPARTMENT (Academic Block-II)
JNTUHCEJ, Nachupally (Kondagattu), Jagtial

Time

10:00 AM to 5:00 PM

Accommodation

Accommodation for participants will be provided in College hostel/guest house on nominal payment.

Send the duly filled Registration form along with Registration fee to the following address:

**Prof. Dhiraj Sunehra
Convener, Emblazon'19**

JNTUH College of Engineering Jagtial

Nachupally (Kondagattu), Jagtial-505 501, Telangana
Ph: 7893278314, 9440268314

E-mail: emblazon2k19ece@gmail.com

Brochure can be downloaded from College Website

www.jntuhcej.ac.in